

## Features

- Available Watchdog Timeout Periods 10 ms, 100 ms, 0.6 s, 1.6 s and 10 s
- Chip Enable Input
- Open Drain or Push-Pull Active Low  $\overline{WDO}$  Output
- Low Power Consumption: 6  $\mu$ A
- Guaranteed Output Valid to  $V_{CC} = 1.67$  V
- Package
  - SOT23-5: Auto Grade 1 Qualified
  - SC70-5
- Operation Temperature Range:  $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$

## Applications

- Automotive Cabin
- T-Box
- BMS
- ESS
- Industry Equipment

## Description

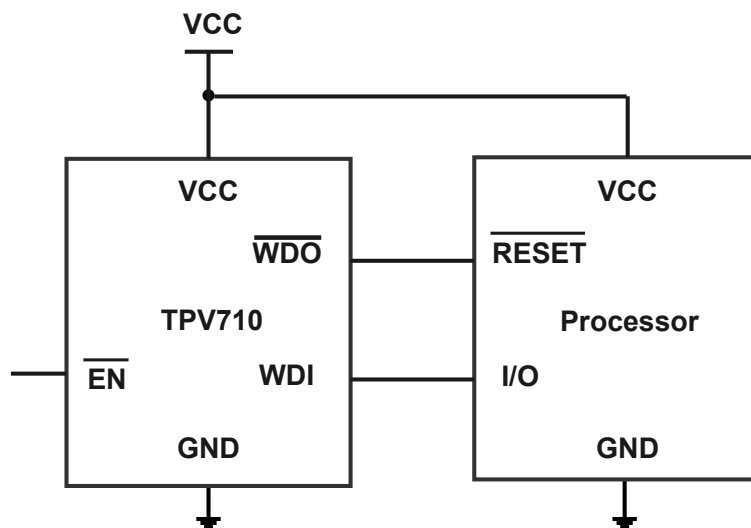
The TPV710 is a watchdog timer circuit which prevents system failures caused by certain types of hardware errors or software errors.

The TPV710 watchdog timer circuit has an input,  $\overline{WDI}$ , and output  $\overline{WDO}$ . The input is used to clear the internal watchdog timer periodically within the specified timeout period  $t_{WD}$ . While the system operates correctly, it periodically toggles the watchdog input,  $\overline{WDI}$ . If the system fails, the watchdog timer is not reset, and the watchdog output,  $\overline{WDO}$ , is asserted.

The TPV710 has an enable input,  $\overline{EN}$ , which can enable or disable the watchdog functionality. The  $\overline{EN}$  is connected to the internal pull-down resistor. The device is enabled if the  $\overline{EN}$  pin is left floating.

The TPV710 is available in SOT23-5 and SC70-5 packages. The TPV710 with SOT23-5 package is auto-grade 1 qualified. The TPV710 with SC70-5 is industry grade. The operation temperature range is  $-40^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ .

## Typical Application Circuit



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## Product Family Table

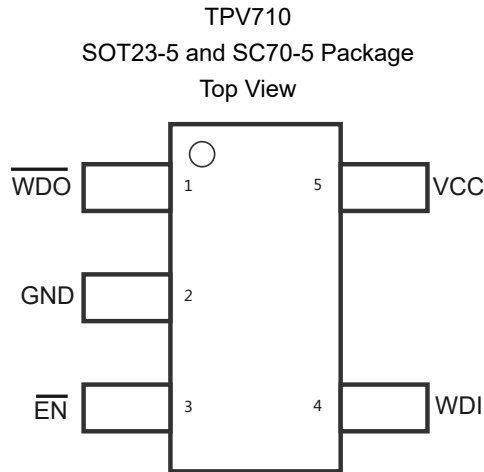
| Order Number                    | Watchdog Timer | Open Drain or Push Pull | Marking Information | Package | Quality Grade |
|---------------------------------|----------------|-------------------------|---------------------|---------|---------------|
| TPV710NXQ-S5TR-S <sup>(1)</sup> | 10ms           | Open Drain              | NXQ                 | SOT23-5 | Automotive    |
| TPV710NWQ-S5TR-S                | 100ms          | Open Drain              | NWQ                 | SOT23-5 | Automotive    |
| TPV710NZQ-S5TR-S                | 0.6s           | Open Drain              | NZQ                 | SOT23-5 | Automotive    |
| TPV710NYQ-S5TR-S                | 1.6s           | Open Drain              | NYQ                 | SOT23-5 | Automotive    |
| TPV710PY-SC5R <sup>(1)</sup>    | 1.6s           | Push Pull               | 7PY                 | SC70-5  | Industry      |
| TPV710NV-S5TR <sup>(1)</sup>    | 12.8s          | Open Drain              | 7NV                 | SOT23-5 | Industry      |

(1) For future products, contact the 3PEAK factory for more information and samples.

## Revision History

| Date       | Revision | Notes               |
|------------|----------|---------------------|
| 2024-04-18 | Rev.A.0  | Initiation version. |

## Pin Configuration and Functions



**Table 1. Pin Functions**

| Pin |                         | I/O | Description                                                                                                                                                                                                                                           |
|-----|-------------------------|-----|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| NO. | Name                    |     |                                                                                                                                                                                                                                                       |
| 1   | $\overline{\text{WDO}}$ | O   | Watchdog Output. Pulls low for $t_{RP}$ if WDI remains low or high for the duration of the watchdog timeout, and does not go high again until the watchdog is cleared. Whenever VCC is below the reset threshold, $\overline{\text{WDO}}$ stays high. |
| 2   | GND                     | -   | Ground.                                                                                                                                                                                                                                               |
| 3   | $\overline{\text{EN}}$  | I   | Enable pin, enable or disable watchdog. $\overline{\text{EN}}$ pin is connected to the internal pull-down resistor. The device is enabled if the $\overline{\text{EN}}$ is left floating.                                                             |
| 4   | WDI                     | I   | Watchdog Input. The timer is cleared if a logic transition occurs on this pin.                                                                                                                                                                        |
| 5   | VCC                     | —   | Power Supply Voltage Monitored.                                                                                                                                                                                                                       |

## Watchdog Timer Circuit with EN Control

### Specifications

#### Absolute Maximum Ratings

| Parameter        |                                                                   | Min  | Max | Unit |
|------------------|-------------------------------------------------------------------|------|-----|------|
| Input Voltage    | VCC, WDI, $\overline{\text{WDO}}$ , $\overline{\text{EN}}$ to GND | -0.3 | 6   | V    |
| Output Current   | $\overline{\text{WDO}}$                                           |      | 20  | mA   |
| T <sub>J</sub>   | Maximum Junction Temperature                                      | -40  | 150 | °C   |
| T <sub>STG</sub> | Storage Temperature Range                                         | -65  | 150 | °C   |
| T <sub>L</sub>   | Lead Temperature (Soldering 10 sec)                               |      | 260 | °C   |

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

(2) This data was taken with the JEDEC low effective thermal conductivity test board.

(3) This data was taken with the JEDEC standard multilayer test boards.

#### ESD, Electrostatic Discharge Protection

| Parameter |                          | Condition                             | Minimum Level | Unit |
|-----------|--------------------------|---------------------------------------|---------------|------|
| HBM       | Human Body Model ESD     | ANSI/ESDA/JEDEC JS-001 <sup>(1)</sup> | ±2000         | V    |
| CDM       | Charged Device Model ESD | ANSI/ESDA/JEDEC JS-002 <sup>(2)</sup> | ±1000         | V    |

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

#### Thermal Information

| Package Type | $\theta_{JA}$ | $\theta_{JC}$ | Unit |
|--------------|---------------|---------------|------|
| SOT23-5      | 128           | 67            | °C/W |

## Watchdog Timer Circuit with EN Control

### Electrical Characteristics

All test conditions:  $V_{CC} = 1.67\text{ V}$  to  $5.5\text{ V}$ ,  $T_A = -40^\circ\text{C}$  to  $+125^\circ\text{C}$ , unless otherwise noted.

| Parameter                  |                                                               | Conditions                                                         | Min                   | Typ  | Max                  | Unit |
|----------------------------|---------------------------------------------------------------|--------------------------------------------------------------------|-----------------------|------|----------------------|------|
| Supply Voltage and Current |                                                               |                                                                    |                       |      |                      |      |
| V <sub>CC</sub>            | V <sub>CC</sub> Operating Voltage Range                       |                                                                    | 1.67                  |      | 5.5                  | V    |
| I <sub>CC</sub>            | Supply Current                                                | WDI and $\overline{\text{EN}}$ unconnected (V <sub>CC</sub> = 5 V) |                       | 6    | 15                   | μA   |
| V <sub>START</sub>         | Watchdog Timer Startup Voltage                                |                                                                    |                       | 2.19 |                      | V    |
| V <sub>IL</sub>            | Input Threshold Voltage Low for WDI, $\overline{\text{EN}}$   |                                                                    |                       |      | 0.3× V <sub>CC</sub> | V    |
| V <sub>IH</sub>            | Input Threshold Voltage High for WDI, $\overline{\text{EN}}$  |                                                                    | 0.7× V <sub>CC</sub>  |      |                      | V    |
| V <sub>OL</sub>            | $\overline{\text{WDO}}$ Output Voltage Low                    | V <sub>CC</sub> ≥ 1.67 V, I <sub>SINK</sub> = 1.2 mA               |                       |      | 0.3                  | V    |
| V <sub>OH</sub>            | $\overline{\text{WDO}}$ Output Voltage High (Push-Pull Only)  | V <sub>CC</sub> ≥ 1.67 V, I <sub>SOURCE</sub> = 500 μA             | 0.8 × V <sub>CC</sub> |      |                      | V    |
| t <sub>PW_EN</sub>         | $\overline{\text{EN}}$ Input Pulse Width                      |                                                                    | 1                     |      |                      | μs   |
| t <sub>GR_EN</sub>         | $\overline{\text{EN}}$ Glitch Rejection                       |                                                                    |                       | 250  |                      | ns   |
| t <sub>d_EN</sub>          | $\overline{\text{EN}}$ to $\overline{\text{WDO}}$ Delay       |                                                                    |                       | 300  |                      | ns   |
| R <sub>PD_EN</sub>         | $\overline{\text{EN}}$ Pull-Down Resistance                   |                                                                    |                       | 50   |                      | kΩ   |
| WDI Pin                    |                                                               |                                                                    |                       |      |                      |      |
| t <sub>WD</sub>            | Watchdog Timeout Period                                       | TPV710xX                                                           | 7                     | 10   | 14                   | ms   |
|                            |                                                               | TPV710xW                                                           | 70                    | 100  | 140                  | ms   |
|                            |                                                               | TPV710xZ                                                           | 0.42                  | 0.6  | 0.84                 | s    |
|                            |                                                               | TPV710xY                                                           | 1.12                  | 1.6  | 2.24                 | s    |
|                            |                                                               | TPV710xV                                                           | 8.9                   | 12.8 | 17.9                 | s    |
| t <sub>RP</sub>            | $\overline{\text{WDO}}$ pull low period when watchdog timeout | TPV710xX                                                           | 4.2                   | 6    | 8.4                  | ms   |
|                            |                                                               | TPV710xW                                                           | 67                    | 96   | 134                  | ms   |
|                            |                                                               | TPV710xZ                                                           | 53                    | 75   | 105                  | ms   |
|                            |                                                               | TPV710xV or TPV710xY                                               | 140                   | 200  | 280                  | ms   |
| t <sub>PW_WD</sub>         | WDI Pulse Width                                               |                                                                    | 50                    |      |                      | ns   |
| t <sub>GR_WD</sub>         | WDI Glitch Rejection                                          |                                                                    |                       | 20   |                      | ns   |

## Watchdog Timer Circuit with EN Control

### Typical Performance Characteristics

All test conditions:  $V_{CC} = 3.3\text{ V}$ ,  $T_A = +25^\circ\text{C}$ , unless otherwise noted.

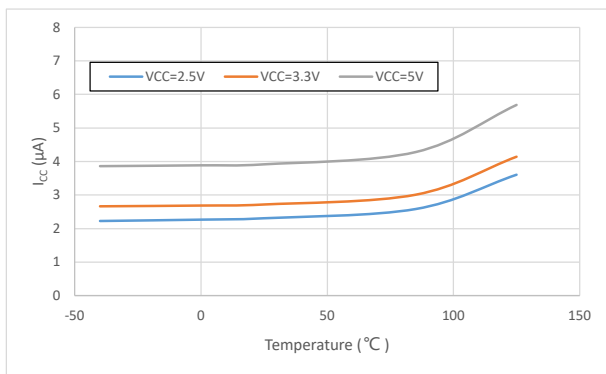


Figure 1. Supply Current vs. Temperature

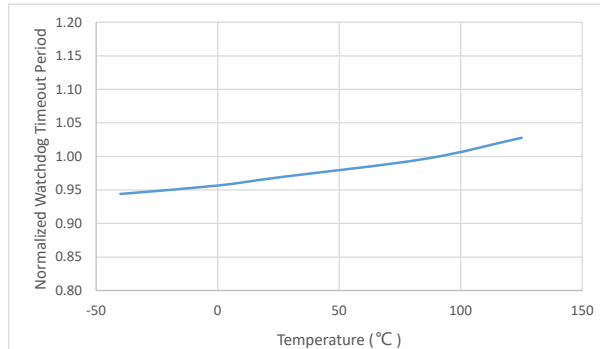


Figure 2. Normalized Watchdog Timeout Period vs. Temperature

## Detailed Description

### Overview

The TPV710 is a watchdog timer circuit which prevent system failures that are caused by certain types of hardware errors or software errors. The TPV710 watchdog timer circuit has an input, WDI, and output  $\overline{WDO}$ . The input is used to clear the internal watchdog timer periodically within the specified timeout period  $t_{WD}$ . While the system is operating correctly, it periodically toggles the watchdog input, WDI. If the system fails, the watchdog timer is not reset, the watchdog output,  $\overline{WDO}$ , is asserted. The TPV710 has an enable input,  $\overline{EN}$ , which can enable or disable the watchdog functionality. The  $\overline{EN}$  is connected to the internal pull down resistor. The device is enabled if the  $\overline{EN}$  pin is left floating. The TPV710 is available in SOT23-5 and SC70-5 package. The TPV710 with SOT23-5 package is auto grade 1 qualified. The TPV710 with SC70-5 is industry grade. The operation temperature range is  $-40^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ .

### Functional Block Diagram

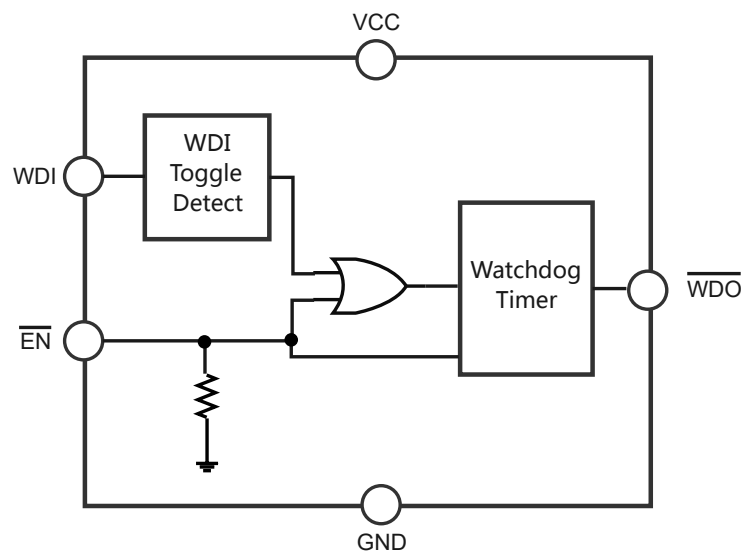


Figure 3. Functional Block Diagram



## Feature Description

### Watchdog Input

The TPV710 features a watchdog timer, which monitors microprocessor activity. A timer circuit is cleared with every low-to-high or high-to-low logic transition on the watchdog input pin (WDI). If the timer counts through the watchdog timeout period ( $t_{WD}$ ),  $\overline{WDO}$  is asserted. The microprocessor is required to toggle the WDI pin to avoid being reset.

### Enable Control

TPV710 has  $\overline{EN}$  pin to control the watchdog timer, if  $\overline{EN}$  is pull low, the watchdog timer is enabled, if  $\overline{EN}$  is pull high, the watchdog timer is disabled and the timer count is cleared.  $\overline{EN}$  has internal pull down resistor, which means watchdog timer is enabled if  $\overline{EN}$  is not connected. In addition to  $\overline{EN}$  control, the watchdog timer is also cleared by an under-voltage condition on  $V_{CC}$ . After  $V_{CC}$  ramps above  $V_{UV}$ , the watchdog timer can be controlled by  $\overline{EN}$  control, and the timer starts counting.

### Watchdog Output

When watchdog timer out occurs, the  $\overline{WDO}$  goes low for  $t_{PR}$ , and then goes high. If no WDI toggling,  $\overline{WDO}$  goes low again after  $t_{WD}$ .

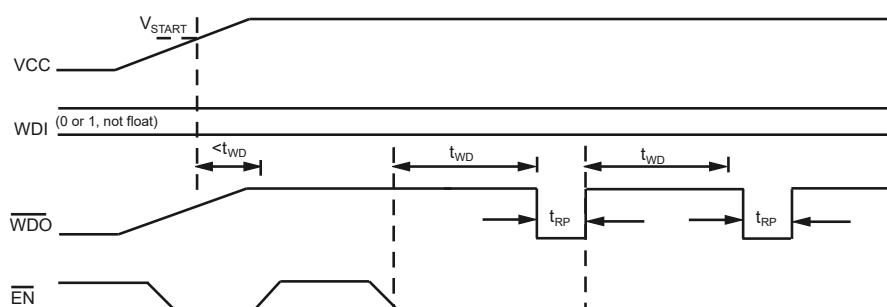


Figure 4. Watchdog Timing Diagram

## Application and Implementation

### Note

Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

## Typical Application

The following figure shows the typical application schematic.

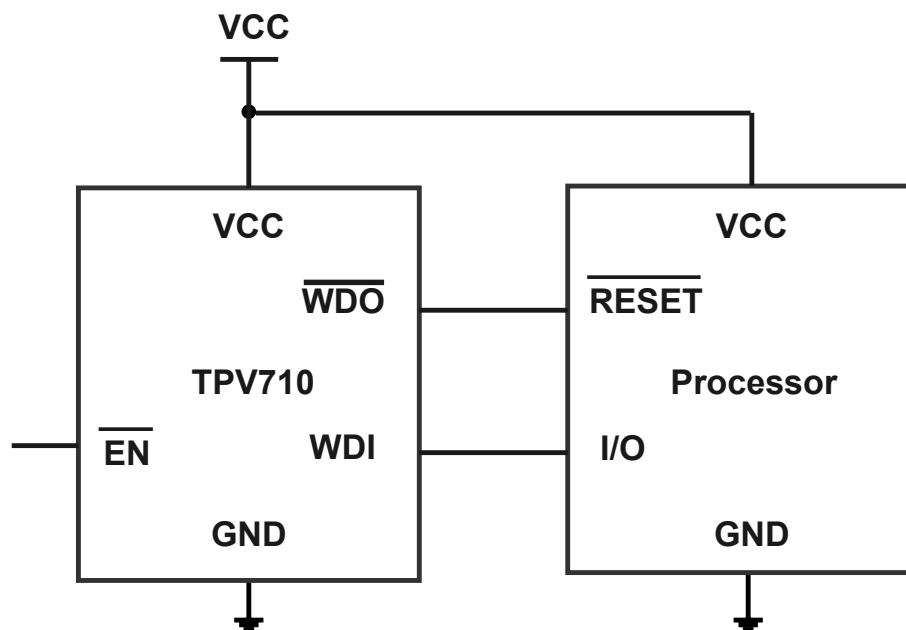
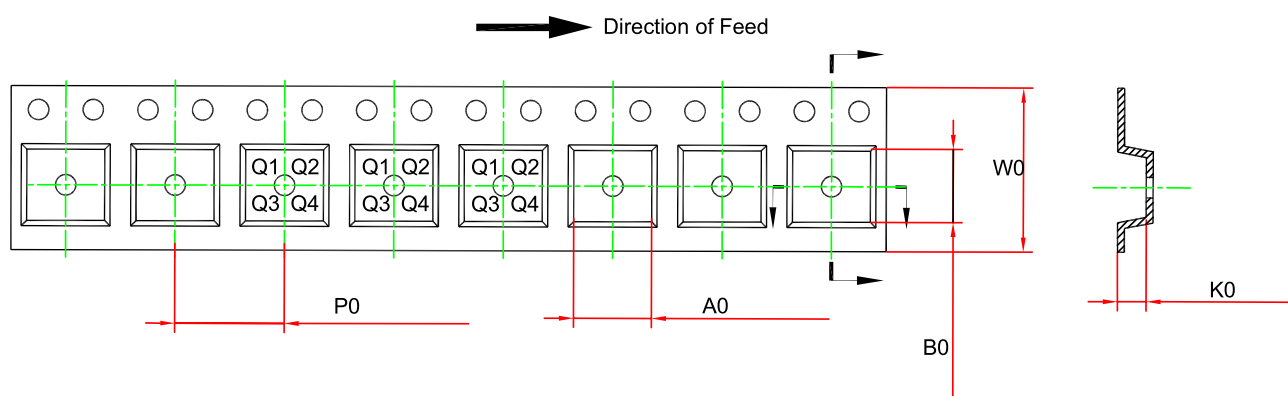
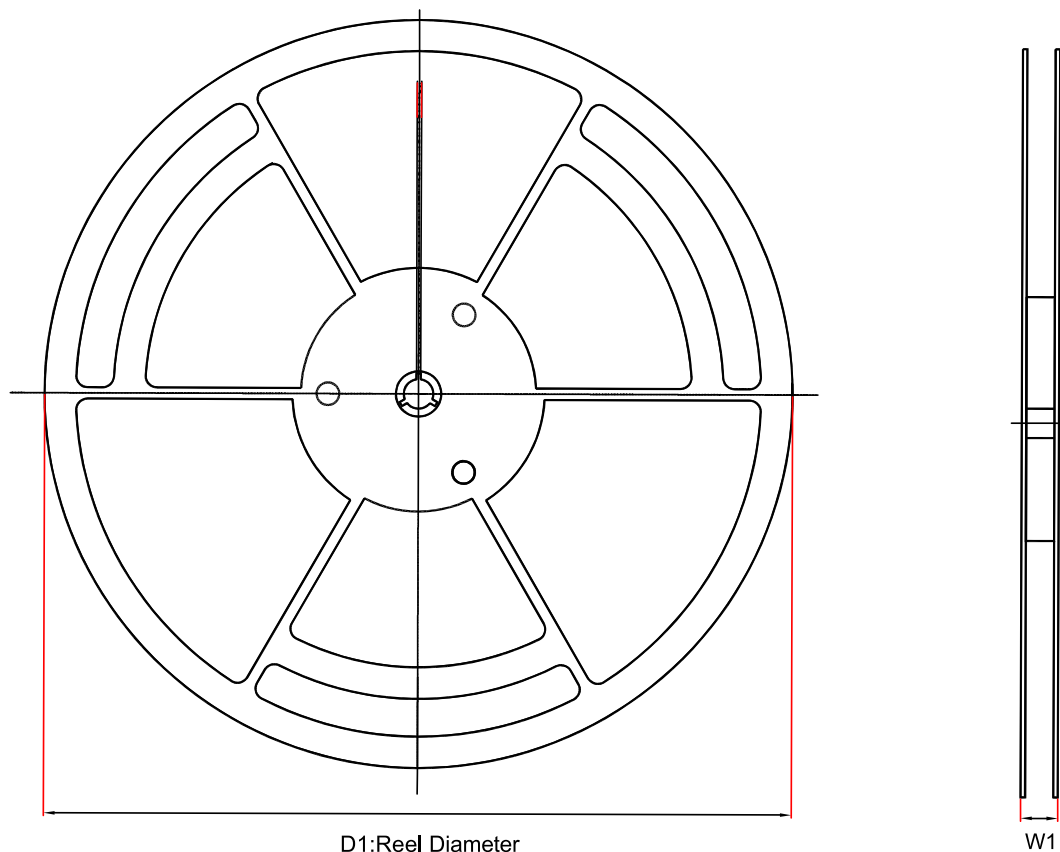


Figure 5. Typical Application Circuit

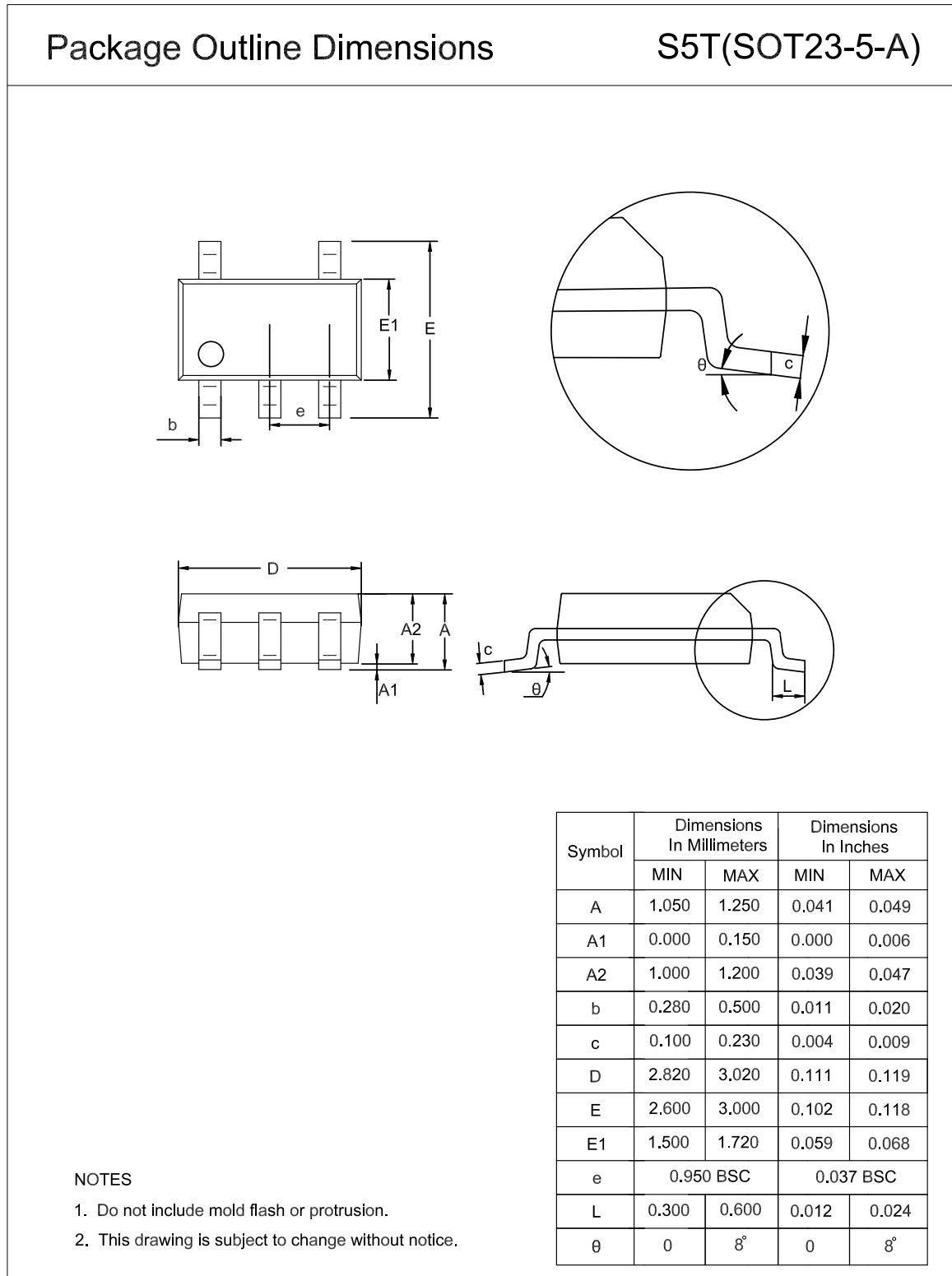
## Tape and Reel Information

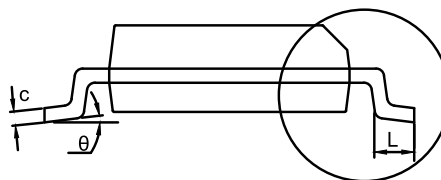
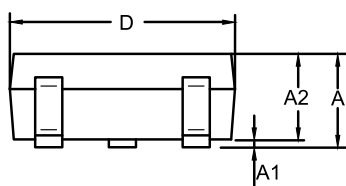
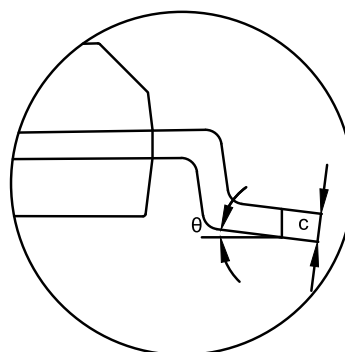
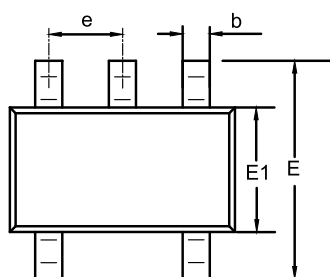


| Order Number     | Package  | D1 (mm) | W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P0 (mm) | W0 (mm) | Pin1 Quadrant |
|------------------|----------|---------|---------|---------|---------|---------|---------|---------|---------------|
| TPV710xxQ-S5TR-S | SOT23-5  | 179     | 12      | 3.3     | 3.25    | 1.4     | 4       | 8       | Q3            |
| TPV710xx-SC5R    | SOT353-5 | 178     | 12.1    | 2.4     | 2.5     | 1.2     | 4       | 8       | Q3            |

## Package Outline Dimensions

### SOT23-5



**SOT353-5**
**Package Outline Dimensions**
**SC5(SOT353-5-A)**


| Symbol   | Dimensions<br>In Millimeters |       | Dimensions<br>In Inches |       |
|----------|------------------------------|-------|-------------------------|-------|
|          | MIN                          | MAX   | MIN                     | MAX   |
| A        | 0.850                        | 1.100 | 0.033                   | 0.043 |
| A1       | 0.000                        | 0.100 | 0.000                   | 0.004 |
| A2       | 0.800                        | 1.000 | 0.031                   | 0.039 |
| b        | 0.150                        | 0.350 | 0.006                   | 0.014 |
| c        | 0.110                        | 0.230 | 0.004                   | 0.009 |
| D        | 2.000                        | 2.200 | 0.079                   | 0.087 |
| E        | 2.150                        | 2.450 | 0.085                   | 0.096 |
| E1       | 1.150                        | 1.350 | 0.045                   | 0.053 |
| e        | 0.650 BSC                    |       | 0.026 BSC               |       |
| L        | 0.260                        | 0.460 | 0.010                   | 0.018 |
| $\theta$ | 0                            | 8°    | 0                       | 8°    |

**NOTES**

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

## Order Information

| Order Number                    | Operating Temperature Range | Package  | Marking Information | MSL | Transport Media, Quantity | Eco Plan |
|---------------------------------|-----------------------------|----------|---------------------|-----|---------------------------|----------|
| TPV710NXQ-S5TR-S <sup>(1)</sup> | -40°C to 125°C              | SOT23-5  | NXQ                 | 1   | Tape and Reel, 3,000      | Green    |
| TPV710NWQ-S5TR-S                | -40°C to 125°C              | SOT23-5  | NWQ                 | 1   | Tape and Reel, 3,000      | Green    |
| TPV710NZQ-S5TR-S                | -40°C to 125°C              | SOT23-5  | NZQ                 | 1   | Tape and Reel, 3,000      | Green    |
| TPV710NYQ-S5TR-S                | -40°C to 125°C              | SOT23-5  | NYQ                 | 1   | Tape and Reel, 3,000      | Green    |
| TPV710PY-SC5R <sup>(1)</sup>    | -40°C to 125°C              | SOT353-5 | 7PY                 | 1   | Tape and Reel, 3,000      | Green    |
| TPV710NV-S5TR <sup>(1)</sup>    | -40°C to 125°C              | SOT23-5  | 7NV                 | 1   | Tape and Reel, 3,000      | Green    |

(1) For future products, contact the 3PEAK for more information and samples.

**Green:** 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

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