

Features

- Qualified for Automotive Applications
 - AEC-Q100 Grade 1, TA: -40°C to +125°C
- Reference Voltage Tolerance
 - 2.495 V Typically
 - 0.5% at 25°C
- · Adjustable Output Voltage
 - VREF to 36 V
- Low Output Noise
- Typical Output Impedance: 0.2-Ω
- · Sink Current Capability: 1 mA to 80 mA
- Package: SOT23G-3

Applications

- Power Module
- LED Lighting
- Current Sensing System
- Instrumentation
- Industrial Control

Description

The TPR433Q and the TPR434Q are 3-terminal adjustable shunt voltage references. The output voltage of both devices can be set to any value within the range from V_{REF} to 36 V with an external feedback resistor network.

The TPR433Q and the TPR434Q have the same electrical performance and the same package but different pin orders. The device provides a 0.2- Ω output impedance and a quick turn-on characteristic, making it an excellent replacement for ordinary Zener diode in many applications.

The TPR433Q and the TPR434Q support a wide output current range from 1 to 80 mA with a SOT23G-3 package. Both devices are qualified with an operating temperature range from −40°C up to +125°C.

Typical Application Circuit

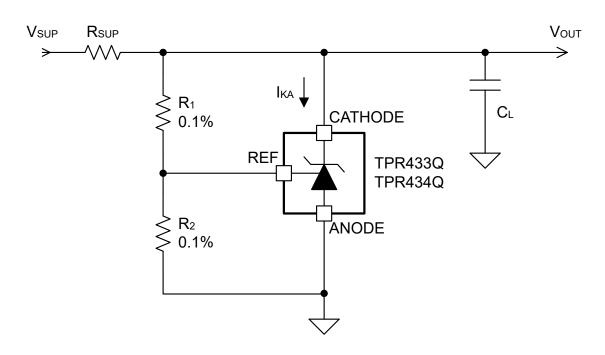




Table of Contents

Features	1
Applications	1
Description	1
Typical Application Circuit	1
Product Family Table	3
Revision History	
Pin Configuration and Functions	
Specifications	5
Absolute Maximum Ratings ⁽¹⁾	5
ESD, Electrostatic Discharge Protection	5
Recommended Operating Conditions	5
Thermal Information	5
Electrical Characteristics	6
Typical Performance Characteristics	7
Detailed Description	10
Overview	10
Functional Block Diagram	10
Feature Description	10
Application and Implementation	11
Application Information	11
Typical Application	11
Layout	13
Layout Guideline	13
Layout Example	13
Tape and Reel Information	14
Package Outline Dimensions	15
SOT23G-3	15
Order Information	16
IMPORTANT NOTICE AND DISCLAIMED	47



Product Family Table

Order Number	Output Voltage	Package		
TPR433BQ-3STR-S	0.5%	SOT23G-3		
TPR434BQ-3STR-S	0.5%	SOT23G-3		

Revision History

Date	Revision	Notes
2024-12-30	Rev.Pre.0	Preliminary revision.
2025-3-25	Rev.A.0	Initial released.

www.3peak.com 3 / 18 DA20231201A0



Pin Configuration and Functions

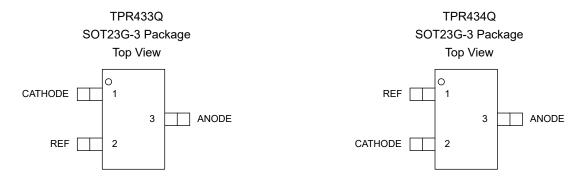


Table 1. Pin Functions: TPR433Q and TPR434Q

Pin	No.	Nama	1/0	Description
TPR433Q	TPR434Q	Name	I/O	Description
3	3	ANODE	0	Common ANODE pin. Suggest connecting this pin to the ground directly.
1	2	CATHODE	I/O	CATHODE pin. The input of the shunt current/voltage.
2	1	REF	I	REF threshold pin.

www.3peak.com 4 / 18 DA20231201A0



Specifications

Absolute Maximum Ratings (1)

	Parameter	Min	Max	Unit
CATHODE	ATHODE Voltage (2)			
Continuous	CATHODE Current	-100	150	mA
Reference	ence Input Current		10	mA
TJ	Maximum Junction Temperature	-40	150	°C
T _A	Operating Temperature Range	-40	125	°C
T _{STG}	Storage Temperature Range	-65	150	°C
TL	Lead Temperature (Soldering 10 sec)		260	°C

⁽¹⁾ Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

ESD, Electrostatic Discharge Protection

	Parameter	Condition	Minimum Level	Unit
НВМ	Human Body Model ESD	ANSI/ESDA/JEDEC JS-001 (1)	±2000	V
CDM	Charged Device Model ESD	ANSI/ESDA/JEDEC JS-002 (2)	±1000	V

⁽¹⁾ JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

Recommended Operating Conditions

	Parameter	Min	Max	Unit
VKA	CATHODE Voltage	V _{REF}	36	V
I _{KA}	CATHODE Current	1	80	mA
TJ	Junction Temperature Range	-40	125	°C

Thermal Information

Package Type	θ _{JA}	θυς	Unit
SOT23G-3	189.9	151.3	°C/W

www.3peak.com 5 / 18 DA20231201A0

⁽²⁾ Voltage values are with respect to ANODE, unless other noted.

⁽²⁾ JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

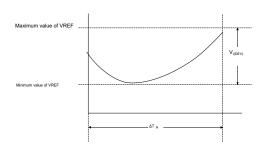


Electrical Characteristics

All test condition: $T_A = +25$ °C, unless otherwise noted.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{REF}	Reference Voltage	V _{KA} = V _{REF} , I _{KA} = 10 mA	2.483	2.495	2.507	V
Reference Input Voltage		$V_{KA} = V_{REF}$, $I_{KA} = 10$ mA, $T_A = -40$ to 85°C		5	15	mV
Range (1)	$V_{KA} = V_{REF}$, $I_{KA} = 10$ mA, $T_A = -40$ to 125°C		11	30	mV	
۸۷,	Ratio of the Change in Reference	I_{KA} = 10 mA, V_{KA} = 10 V to V_{REF}	-1.5	0.3	1.5	mV/V
$\frac{\Delta V_{REF}}{\Delta V_{KA}}$ Voltage to the Change in CATHODE Voltage	I _{KA} = 10 mA, V _{KA} = 36 V to 10 V	-1	0.1	1	mV/V	
I _{REF}	Reference Input Current	I _{KA} = 10 mA, R1 = 10 K, R2 Open		1.8	4	μA
I _{I(DEV)}	Reference Input Current Deviation over Temperature Range (1)	I_{KA} = 10 mA, R1 = 10 K, R2 Open, T _A = -40 to 125°C		0.1	1	μА
I _{KA(MIN)}	Minimum CATHODE Current for Regulation	V _{KA} = V _{REF}		0.4	0.6	mA
		V _{KA} = 36 V, V _{REF} = 0 V		0.2	0.5	μA
I _{KA(OFF)}	Off-State CATHODE Current	$V_{KA} = 36 \text{ V}, V_{REF} = 0 \text{ V}, T_A = -40$ to 125°C		0.2	1.5	μА
Z _{KA}	Dynamic Output Impedance (1)	$V_{KA} = V_{REF}$, f <= 1 kHz, I_{KA} = 1 mA to 80 mA		0.2	2.6	Ω

(1) The deviation parameters $V_{I(DEV)}$ and $I_{I(DEV)}$ are defined as the differences between the minimum value and the maximum value obtained over the temperature range. The average full-range temperature coefficient of the reference input voltage α_{VREF} is defined as $|\alpha_{VREF}| \left(\frac{ppm}{^{\circ}C}\right) = \frac{V_{I(DEV)}/V_{REF,25^{\circ}C}}{\Delta T_{A}} \times 10^{6}$. Where, $V_{REF,25^{\circ}C}$ is the typical value at room temperature of 25°C, ΔT_{A} is the rated operating ambient temperature range of the device. α_{VREF} is positive or negative, depending on whether the minimum value or maximum value of the V_{REF} occurs at the lower temperature.



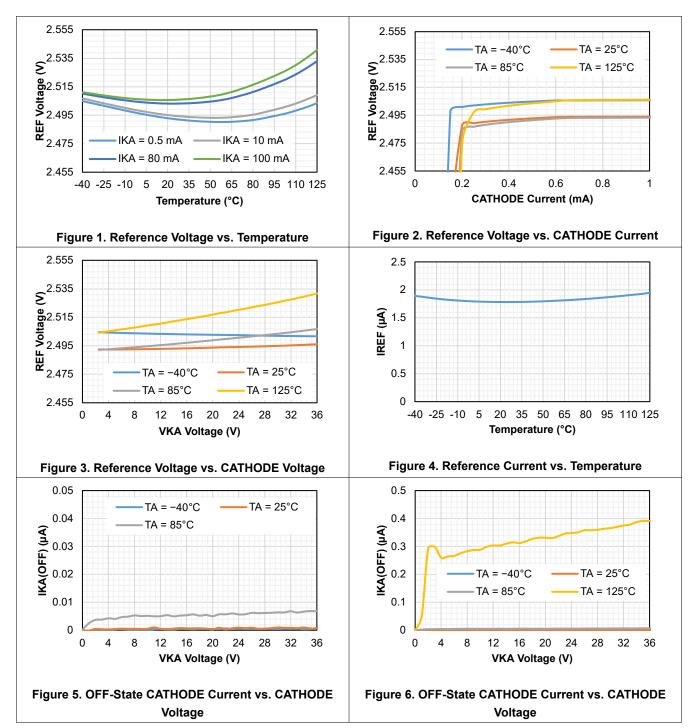
(2) The dynamic impedance is defined as $|Z_{KA}| = \frac{\Delta V_{KA}}{\Delta I_{KA}}$. When the device is operating with two external resistors, the total dynamic impedance of the circuit is $|Z'| = \frac{\Delta V}{\Delta I}$, which is approximately equal to $|Z_{KA}| \left(1 + \frac{R1}{R2}\right)$.

www.3peak.com 6 / 18 DA20231201A0



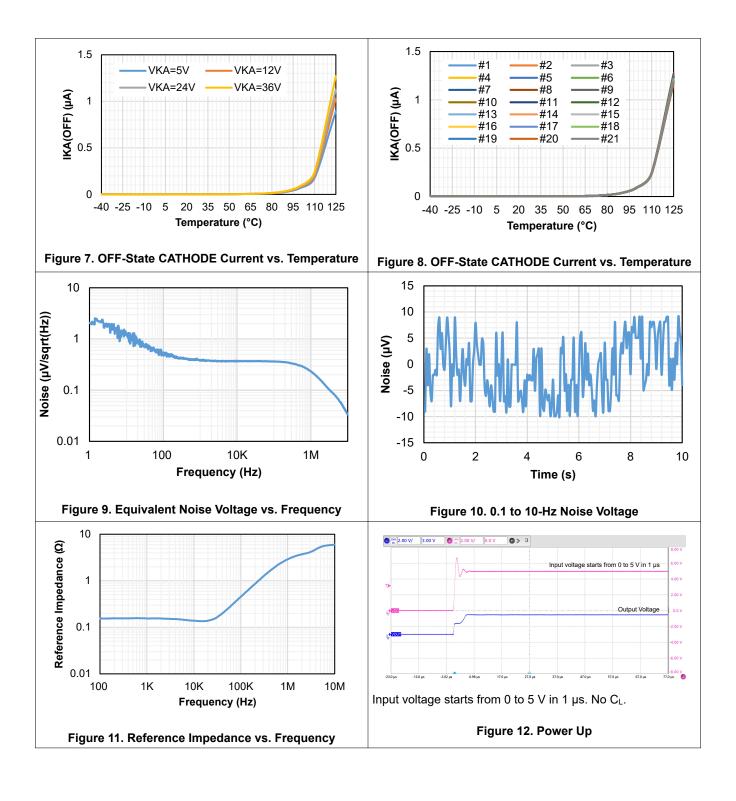
Typical Performance Characteristics

All test conditions: $T_A = 25$ °C, $V_{OUT} = 2.5$ V, $I_{KA} = 10$ mA, unless otherwise noted.

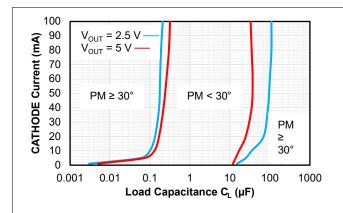


www.3peak.com 7 / 18 DA20231201A0



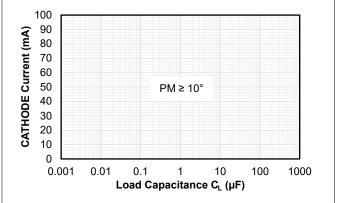






The areas within the boundary represent application V_{OUT} ≥ 2.5 V, all current and all capacitance conditions have conditions that may cause the phase margin (PM) ≤ 30°. V_{OUT} ≥ 10 V, all current and all capacitance conditions have ≥ 30° phase margin.

Figure 13. Stability Boundary Conditions



≥ 10° phase margin.

Figure 14. Stability Boundary Conditions



Detailed Description

Overview

The TPR433Q and the TPR434Q are 3-terminal adjustable shunt voltage references. The output voltage of both devices can be set to any value within the range from V_{REF} to 36 V with an external feedback resistor network.

The TPR433Q and the TPR434Q have the same electrical performance and the same package but different pin orders. The device provides a $0.2-\Omega$ output impedance and a quick turn-on characteristic, making it an excellent replacement for ordinary Zener diode in many applications.

The TPR433Q and the TPR434Q support a wide output current range from 1 to 80 mA with a SOT23G-3 package. Both devices are qualified with an operating temperature range from −40°C up to +125°C.

Functional Block Diagram

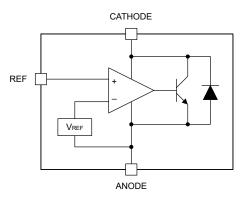


Figure 15. Functional Block Diagram

Feature Description

The TPR433Q and the TPR434Q products are 3-terminal adjustable shunt voltage references. They consist of an internal reference and amplifier that outputs a sink current based on the difference between the REF pin and the internal reference (shown in the Functional Block Diagram).

Closed Loop Operation

The TPR433Q and the TPR434Q products operate in a closed loop when the voltage or current of the CATHODE pin is fed back to the REF pin. In this manner, the device regulates a fixed voltage or current. The feedback allows the device to function as an error amplifier, adjusting a portion of the output voltage to maintain the desired regulation. This is achieved by relating the output voltage back to the reference pin in order to make it equal to the internal reference voltage, which can be done through resistive or direct feedback.

Open Loop Operation

The TPR433Q and the TPR434Q products operate in the open loop when the voltage or current of the CATHODE pin is not being fed to the REF pin in any way. When proper CATHODE current (I_{KA}) is applied, the device exhibits the characteristics shown in Figure 15. With such high gain in this setup, the device is typically used as a comparator. The integrated reference makes it the preferred choice for users to monitor a specific signal level.

www.3peak.com 10 / 18 DA20231201A0



Application and Implementation

Note

Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

Application Information

The TPR433Q and the TPR434Q products are 3-terminal adjustable shunt voltage references. The output voltage of both devices can be set to any value within the range from V_{REF} to 36 V with an external feedback resistor network. The following section shows the typical usage of the device.

Typical Application

Shunt Regulator/Reference

Figure 16 shows the typical application schematic in the shunt regulator/reference (closed loop) mode.

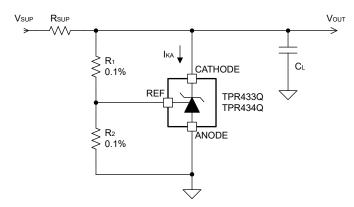


Figure 16. Typical Application Circuit in Shunt Regulator/Reference Mode

As discussed in the Feature Description, a feedback resistors network is required at the device CATHODE pin and REF pin to get regulated output voltage. The CATHODE voltage can be set to the value between V_{REF} to 36 V with the below Equation 1.

$$V_{OUT} = \left(1 + \frac{R1}{R2}\right) \times V_{REF} \tag{1}$$

Where, R1 and R2 are the feedback resistors, V_{REF} is 2.495 V typically.

Comparator with Integrated Reference

Figure 17 shows the typical application schematic in the comparator (open loop) mode.

www.3peak.com 11 / 18 DA20231201A0



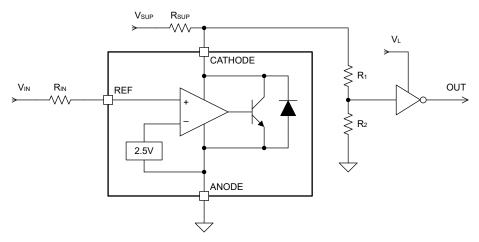


Figure 17. Typical Application Circuit in Comparator Mode

As discussed in the Feature Description, the device operates as a comparator with the configuration in the above figure. By comparing the V_{REF} pin voltage to the internal reference voltage, the device outputs a logic signal accordingly. With a proper CATHODE current ($\geq I_{KA(MIN)}$), the device has enough open loop gain to provide a quick response.

www.3peak.com 12 / 18 DA20231201A0



Layout

Layout Guideline

- Both input bypass capacitors and output bypass capacitors must be placed as close to the device pins as possible.
- It is recommended to use wide trace and thick copper to minimize I×R drop for the high current path.

Layout Example

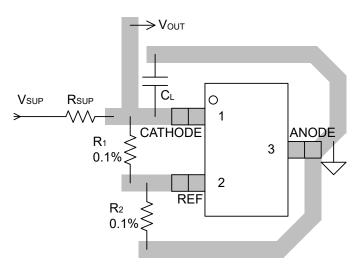


Figure 18. Layout Example of the TPR433Q in Shunt Regulator/Reference Mode

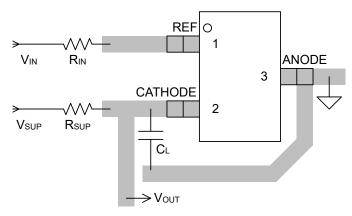
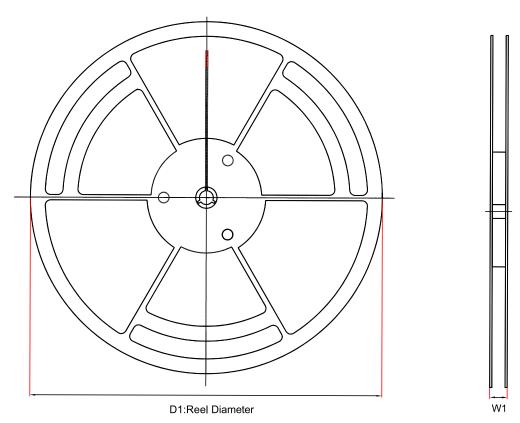


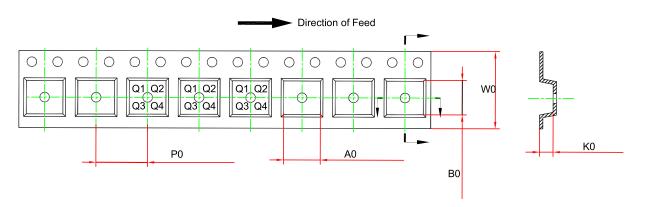
Figure 19. Layout Example of the TPR434Q in Comparator Mode

www.3peak.com 13 / 18 DA20231201A0



Tape and Reel Information





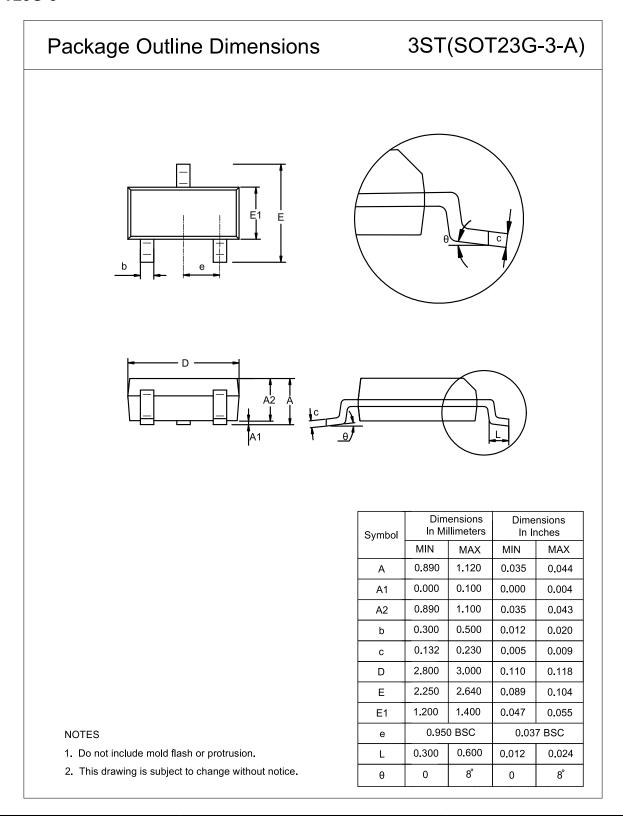
Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPR433BQ-3STR-S	SOT23G-3	178	13.1	3.15	2.77	1.22	4.0	8.0	Q3
TPR434BQ-3STR-S	SOT23G-3	178	13.1	3.15	2.77	1.22	4.0	8.0	Q3

www.3peak.com 14 / 18 DA20231201A0



Package Outline Dimensions

SOT23G-3



www.3peak.com 15 / 18 DA20231201A0



Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPR433BQ-3STR-S	-40°C to +125°C	SOT23G-3	R33	MSL1	3,000	Green
TPR434BQ-3STR-S	-40°C to +125°C	SOT23G-3	R34	MSL1	3,000	Green

Green: 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

www.3peak.com 16 / 18 DA20231201A0



IMPORTANT NOTICE AND DISCLAIMER

Copyright[©] 3PEAK 2012-2025. All rights reserved.

Trademarks. Any of the 思瑞浦 or 3PEAK trade names, trademarks, graphic marks, and domain names contained in this document /material are the property of 3PEAK. You may NOT reproduce, modify, publish, transmit or distribute any Trademark without the prior written consent of 3PEAK.

Performance Information. Performance tests or performance range contained in this document/material are either results of design simulation or actual tests conducted under designated testing environment. Any variation in testing environment or simulation environment, including but not limited to testing method, testing process or testing temperature, may affect actual performance of the product.

Disclaimer. 3PEAK provides technical and reliability data (including data sheets), design resources (including reference designs), application or other design recommendations, networking tools, security information and other resources "As Is". 3PEAK makes no warranty as to the absence of defects, and makes no warranties of any kind, express or implied, including without limitation, implied warranties as to merchantability, fitness for a particular purpose or non-infringement of any third-party's intellectual property rights. Unless otherwise specified in writing, products supplied by 3PEAK are not designed to be used in any life-threatening scenarios, including critical medical applications, automotive safety-critical systems, aviation, aerospace, or any situations where failure could result in bodily harm, loss of life, or significant property damage. 3PEAK disclaims all liability for any such unauthorized use.

www.3peak.com 17 / 18 DA20231201A0



This page intentionally left blank