

5-V Low-Power Comparators with Push-Pull Output**Features**

- Power Supply Voltage: 1.5 V to 5.5 V
- Low Supply Current: 40 μ A per Channel
- High-to-Low Propagation Delay: 100 ns
- Internal Hysteresis Ensures Clean Switching
- Offset Voltage: ± 5 mV
- Input Bias Current: 10 pA (Typ)
- Input Common-Mode Range Extends 200 mV
- Push-Pull Output

Description

The devices are low-power comparators with internal hysteresis. The common-mode input voltage range extends 200 mV beyond the power rail. The devices have 100-ns propagation delay which makes the devices suitable for general applications. The internal input hysteresis eliminates output switching caused by input noise voltage. The devices have push-pull output to support rail-to-rail output swing.

The operating temperature range of the devices is from -40°C to $+125^{\circ}\text{C}$.

Applications

- Peak and Zero-Crossing Detectors
- Threshold Detectors/Discriminators
- Sensing at the Ground or Supply Line
- Logic Level Shifting or Translation
- Window Comparators
- IR Receivers

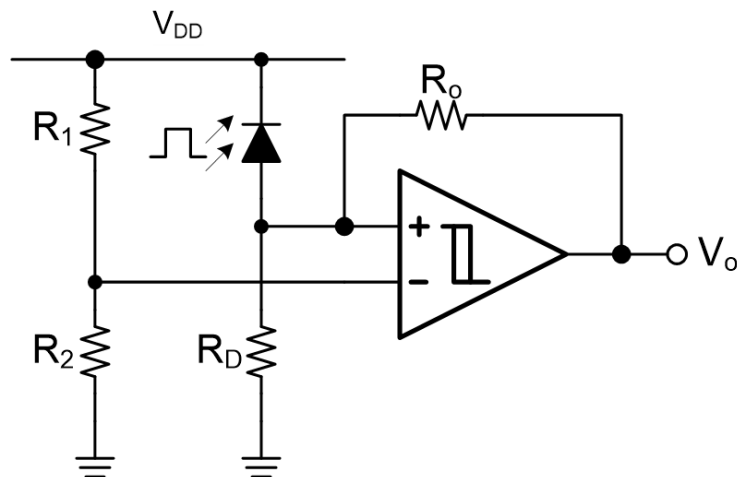
Typical Application Circuit

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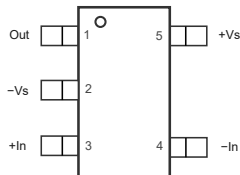
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Revision History

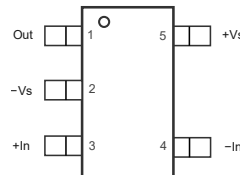
Date	Revision	Notes
2025-04-14	Rev.A.0	Initial release.
2025-08-20	Rev.A.1	The following updates are all about new formats or typo corrections. The actual product remains unchanged: • Updated the POD information and orderable part number list.

Pin Configuration and Functions

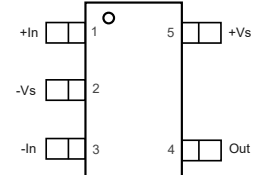
TPCMP191-S5TR
SOT23-5
Top View



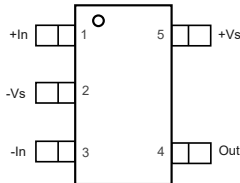
TPCMP191-SC5R
SOT353 (SC70-5)
Top View



TPCMP191U-S5TR
SOT23-5
Top View



TPCMP191U-SC5R
SOT353 (SC70-5)
Top View



TPCMP191U-DF0R
DFN0.8X0.8-4 (5PIN)
Top View

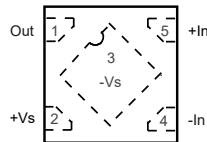
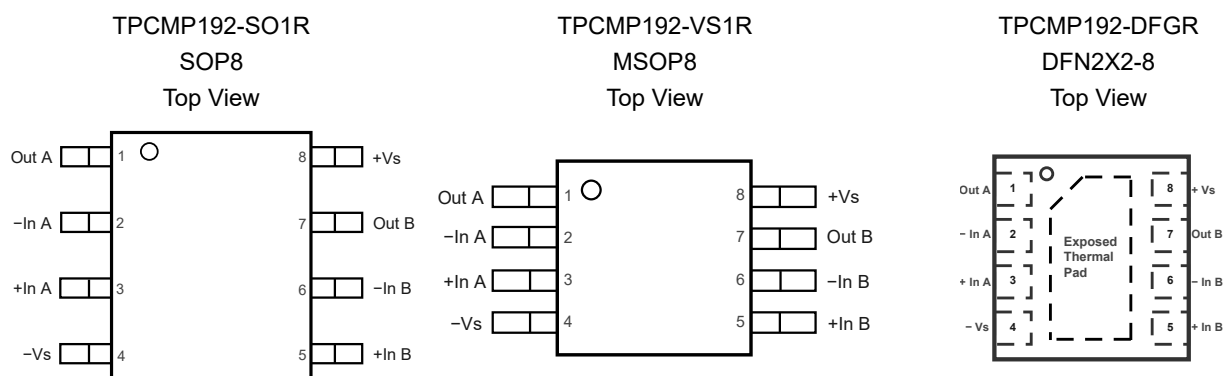


Table 1. Pin Functions: TPCMP191

Pin No.					Name	I/O	Description
TPCMP191-S5TR	TPCMP191-SC5R	TPCMP191U-S5TR	TPCMP191U-SC5R	TPCMP191U-DF0R			
1	1	4	4	1	Out	O	Output
2	2	2	2	3	-Vs	-	Negative power supply
3	3	1	1	5	+In	I	Non-inverting input
4	4	3	3	4	-In	I	Inverting input
5	5	5	5	2	+Vs	-	Positive power supply

5-V Low-Power Comparators with Push-Pull Output

Table 2. Pin Functions: TPCMP192-SO1R/TPCMP192-VS1R/TPCMP192-DFGR

Pin No.			Name	I/O	Description
TPCMP192-SO1R	TPCMP192-VS1R	TPCMP192-DFGR			
1	1	1	Out A	O	Output
2	2	2	-In A	I	Inverting input
3	3	3	+In A	I	Non-inverting input
4	4	4	-Vs	-	Negative power supply
5	5	5	+In B	I	Non-inverting input
6	6	6	-In B	I	Inverting input
7	7	7	Out B	O	Output
8	8	8	+Vs	-	Positive power supply.
-	-	EP	Exposed Thermal Pad	-	Exposed Thermal Pad. The exposed pad is tied to the -Vs.

Specifications

Absolute Maximum Ratings ⁽¹⁾

Parameter		Min	Max	Unit
	Supply Voltage, (+V _S) – (–V _S)	0	5.5	V
	Input Voltage	(–V _S) – 0.3	(+V _S) + 0.3	V
	Input Current: +I _N , –I _N ⁽²⁾	–10	10	mA
	Output Current: OUT	–10	10	mA
	Output Short-Circuit Duration ⁽³⁾		Continuous	
T _J	Maximum Junction Temperature		150	°C
T _A	Operating Temperature Range	–40	125	°C
T _{STG}	Storage Temperature Range	–65	150	°C
T _L	Lead Temperature (Soldering 10 sec)		260	°C

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

(2) The inputs are protected by ESD protection diodes to each power supply. If the input extends more than 500 mV beyond the negative power supply, the input current should be limited to less than 10 mA.

(3) A heat sink may be required to keep the junction temperature below the absolute maximum. This depends on the power supply voltage and how many comparators are shorted. Thermal resistance varies with the amount of PC board metal connected to the package. The specified values are for short traces connected to the leads.

ESD, Electrostatic Discharge Protection

Parameter		Condition	Level	Unit
HBM	Human Body Model ESD	ANSI/ESDA/JEDEC JS-001 ⁽¹⁾	2	kV
CDM	Charged Device Model ESD	ANSI/ESDA/JEDEC JS-002 ⁽²⁾	1	kV

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

Recommended Operating Conditions

Parameter		Min	Typ	Max	Unit
V _S	Supply Voltage, (+V _S) – (–V _S)	1.5		5.5	V

5-V Low-Power Comparators with Push-Pull Output**Thermal Information**

Package Type	θ_{JA}	θ_{JC}	Unit
SOT353 (SC70-5)	400	150	°C/W
SOT23-5	250	81	°C/W
SOP8	158	43	°C/W
MSOP8	210	45	°C/W
DFN0.8X0.8-4(5PIN)	500	200	°C/W
DFN2X2-8	100	60	°C/W

5-V Low-Power Comparators with Push-Pull Output
Electrical Characteristics

All test conditions: $V_S = 5\text{ V}$, $T_A = 25^\circ\text{C}$, unless otherwise noted.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Power Supply						
IQ	Quiescent Current per Comparator	V _S = 5 V, No Load, Output High, V _{INP} = 1 V, V _{INN} = 0 V		38	70	μA
		V _S = 5 V, No Load, Output Low, V _{INP} = 0V, V _{INN} = 1 V		34	70	μA
		V _S = 1.8 V, No Load, Output High, V _{INP} = 1 V, V _{INN} = 0 V		27	60	μA
		V _S = 1.8 V, No Load, Output Low, V _{INP} = 0 V, V _{INN} = 1 V		26	60	μA
PSRR	Power Supply Rejection Ratio	V _S = 1.5 V to 5.5 V, V _{CM} = 0 V	65	91		dB
		V _S = 1.5 V to 5.5 V, V _{CM} = 0 V, T _A = −40°C to 125°C	60			dB
Input Characteristics						
V _{OS}	Input Offset Voltage ⁽¹⁾	V _S = 5 V, V _{CM} = 0 V	−5	1.2	5	mV
		V _S = 5 V, V _{CM} = 0 V, T _A = −40°C to 125°C	−6		6	mV
V _{OS} TC	Input Offset Voltage Drift ⁽²⁾	V _S = 5 V, V _{CM} = 0 V, T _A = −40°C to 125°C		5		μV/°C
V _{HYST}	Input Hysteresis Voltage ⁽¹⁾	V _S = 5 V, V _{CM} = 0	3	4.5	7	mV
		V _S = 5 V, V _{CM} = 0 V, T _A = −40°C to 125°C	2		8	mV
V _{OS}	Input Offset Voltage ⁽¹⁾	V _S = 1.8 V, V _{CM} = 0	−5	1.2	5	mV
		V _S = 1.8 V, V _{CM} = 0 V, T _A = −40°C to 125°C	−6		6	mV
V _{OS} TC	Input Offset Voltage Drift ⁽²⁾	V _S = 1.8 V, V _{CM} = 0V, T _A = −40°C to 125°C		2		μV/°C
V _{HYST}	Input Hysteresis Voltage ⁽¹⁾	V _S = 1.8 V, V _{CM} = 0 V	3	4.5	7	mV
		V _S = 1.8 V, V _{CM} = 0 V, T _A = −40°C to 125°C	2		8	mV
I _B	Input Bias Current	V _S = 5 V, V _{CM} = V _S /2		10		pA
		V _S = 5 V, V _{CM} = V _S /2, T _A = −40°C to 125°C			200	pA
I _{OS}	Input Offset Current	V _S = 5 V, V _{CM} = V _S /2		1		pA
		V _S = 5 V, V _{CM} = V _S /2, T _A = −40°C to 125°C	−100		100	pA
V _{CM}	Common-mode Voltage Range	V _S = 5 V, T _A = −40°C to 125°C	−0.2		5.2	V

5-V Low-Power Comparators with Push-Pull Output

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
CMRR	Common-mode Rejection Ratio	V _S = 5 V, V _{CM} = −0.2 V to 5.2 V	60	80		dB
		V _S = 5 V, V _{CM} = −0.2 V to 5.2 V, T _A = −40°C to 125°C	55			dB
Output						
V _{OL}	Output Voltage Swing from Negative Rail	V _S = 5 V, I _{OL} = 4 mA		150	190	mV
		V _S = 5 V, I _{OL} = 4 mA, T _A = −40°C to 125°C			220	mV
		V _S = 3.3 V, I _{OL} = 1 mA		40	55	mV
		V _S = 3.3 V, I _{OL} = 1 mA, T _A = −40°C to 125°C			65	mV
V _{OH}	Output Voltage Swing from Positive Rail	V _S = 5 V, I _{OH} = 4 mA		150	210	mV
		V _S = 5 V, I _{OH} = 4 mA, T _A = −40°C to 125°C			240	mV
		V _S = 3.3 V, I _{OH} = 1 mA		40	60	mV
		V _S = 3.3 V, I _{OH} = 1 mA, T _A = −40°C to 125°C			70	mV
I _{SOURCE}	Source Current	V _S = 5 V		70		mA
I _{SINK}	Sink Current	V _S = 5 V		60		mA
Switching Characteristics ⁽³⁾						
T _{PLH}	Propagation Delay Time, Low to High	V _S = 5 V, ΔV _{IN} = 1 V, V _{CM} = V _S /2, 100mV overdrive, Delay time is measured from mid-point of input to mid-point of output.		90		ns
		V _S = 5 V, ΔV _{IN} = 1 V, V _{CM} = V _S /2, 20mV overdrive, Delay time is measured from mid-point of input to mid-point of output.		200		ns
T _{PHL}	Propagation Delay Time, High to Low	V _S = 5 V, ΔV _{IN} = 1 V, V _{CM} = V _S /2, 100mV overdrive, Delay time is measured from mid-point of input to mid-point of output.		100		ns
		V _S = 5 V, ΔV _{IN} = 1 V, V _{CM} = V _S /2, 20mV overdrive, Delay time is measured from mid-point of input to mid-point of output.		190		ns
T _R	Rise Time ⁽⁴⁾⁽⁵⁾	V _S = 5 V		1.85		ns
T _F	Fall Time ⁽⁴⁾⁽⁵⁾	V _S = 5 V		1.7		ns
T _{on}	Power-up Time ⁽⁴⁾	V _S = 5 V, The time between V _S exceed 1.5V and the output is in a correct state.		20		us
f _{toggle}	Toggle Frequency ⁽⁴⁾	V _S = 5 V		5		MHz

(1) The input offset voltage is the average of the input-referred trip points. The input hysteresis is the difference between the input-referred trip points.

(2) Provided by bench tests and design simulation.

(3) Delay time is measured from the mid-point of the input to the mid-point of the output.

(4) Provided by design simulation.

(5) Measured between 20% of V_S and 80% of V_S .

5-V Low-Power Comparators with Push-Pull Output

Typical Performance Characteristics

All test conditions: $V_S = 5\text{ V}$, $V_{CM} = 2.5\text{ V}$, unless otherwise noted.

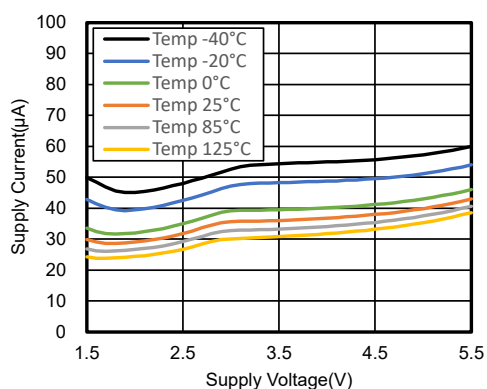


Figure 1. Supply Current vs. Supply Voltage, Output High

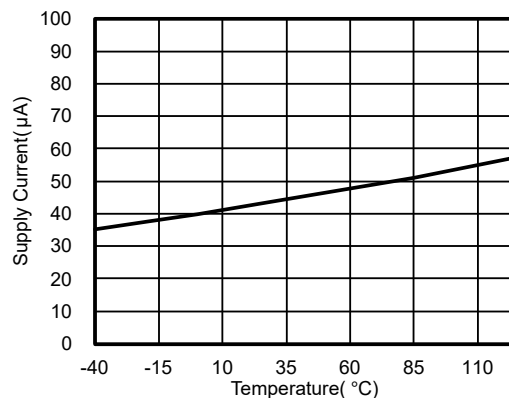


Figure 2. Supply Current vs. Temperature

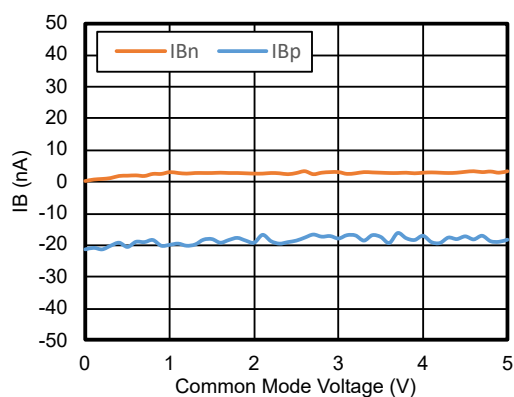


Figure 3. IB vs. Common-Mode Voltage

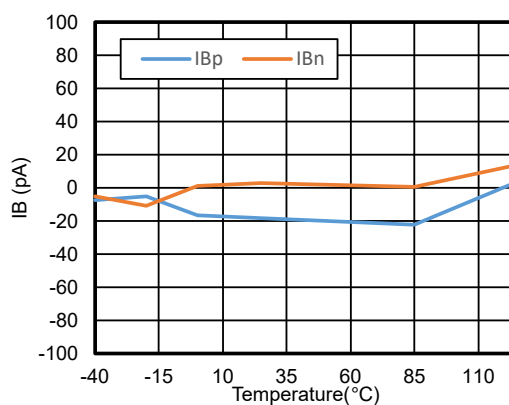


Figure 4. IB vs. Temperature

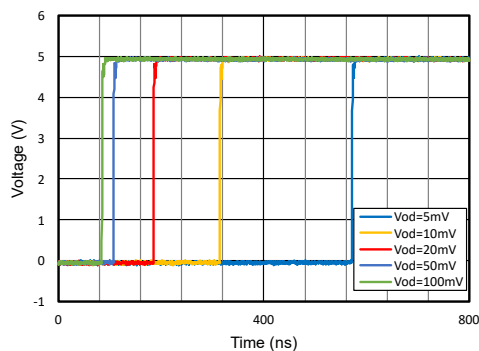


Figure 5. Propagation Delay, Low to High, 5 V

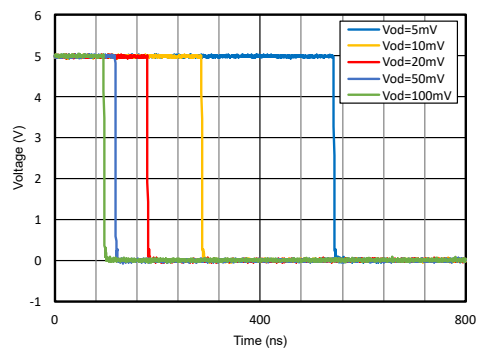


Figure 6. Propagation Delay, High to Low, 5 V

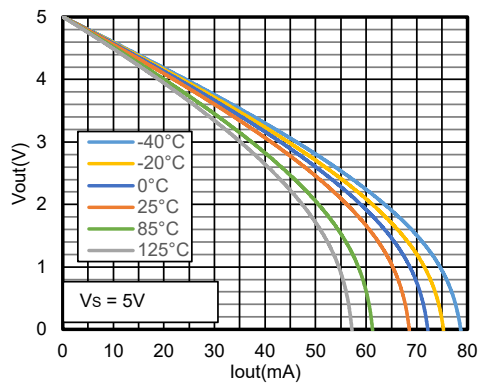
5-V Low-Power Comparators with Push-Pull Output


Figure 7. Output Voltage vs. Output Sourcing Current, 5 V

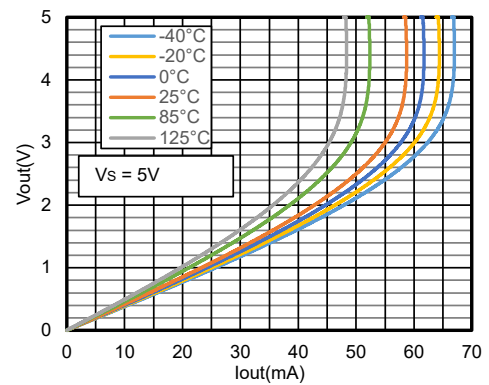


Figure 8. Output Voltage vs. Output Sinking Current, 5 V

Detailed Description

Overview

The devices feature 100-ns response time and include 4.5 mV of internal hysteresis for improved noise immunity with an input common-mode range that extends 0.2 V beyond the power supply rails, having the ability to operate from 1.5 V to 5.5 V on the supply pin.

Functional Block Diagram

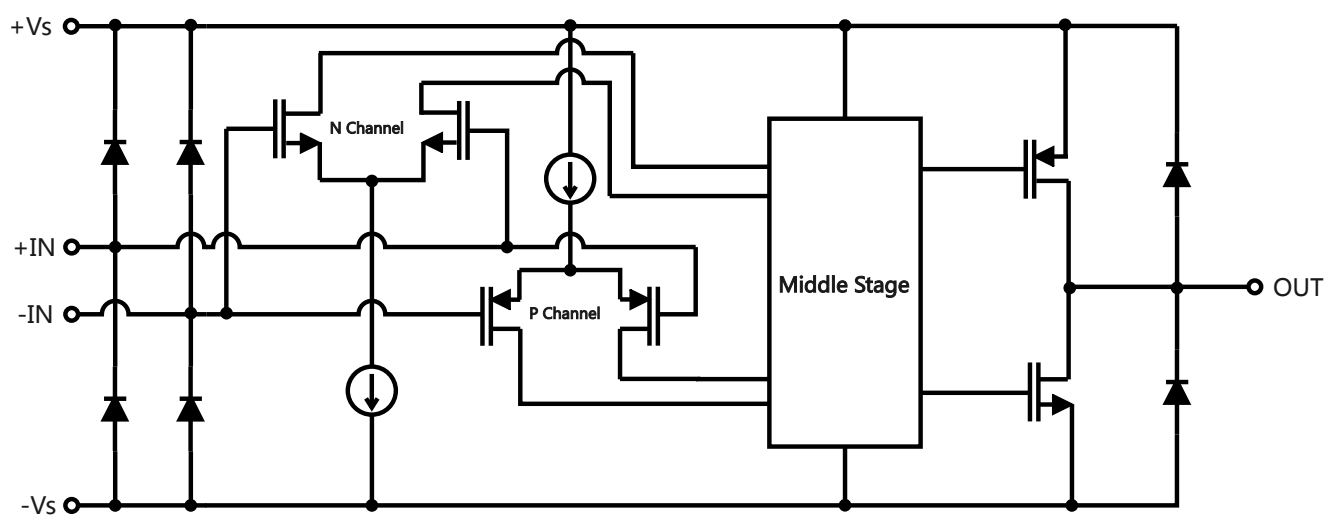


Figure 9. Functional Block Diagram

Application and Implementation

Note

Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

Application Information

Power Supply Layout and Bypass

The power supply pins of TPCMP191 and TPCMP192 families should have local bypass capacitors (i.e., 0.01 μF to 0.1 μF) within 2 mm for high-frequency performance. They can also use a bulk capacitor (i.e., 1 μF or larger) within 100 mm to provide large and slow currents. This bulk capacitor can be shared with other analog parts.

A good ground layout improves performance by decreasing the amount of stray capacitance and noise at the inputs and outputs of the comparator. To decrease stray capacitance, minimize PCB lengths and resistor leads, and place external components to the pins of the comparator as close as possible.

Operation Outside of the Common Input Voltage Range

A list of input voltage situations and the corresponding outcomes are as follows:

1. When both $-IN$ and $+IN$ are within the common-mode range:
 - a. If the voltage at the $-IN$ pin is higher than the voltage at the $+IN$ pin and the offset voltage, the output is low, and the output MOSFET is sinking current.
 - b. If the voltage at the $-IN$ pin is lower than the voltage at the $+IN$ pin and the offset voltage, the output is high impedance.
2. When the voltage at the $-IN$ pin is higher than the common-mode voltage range and the voltage at the $+IN$ pin is within the common-mode voltage range, the output is low, and the output MOSFET is sinking current.
3. When the voltage at the $+IN$ pin is higher than the common-mode voltage range and the voltage at the $-IN$ pin is within the common-mode voltage range, the output is high impedance.
4. When the voltages at the $-IN$ and $+IN$ pins are both higher than the common-mode voltage range, the output is in an uncertain state.

Typical Application

IR Receiver

The device is an ideal candidate to be used as an infrared receiver shown in [Figure 10](#). The infrared photo diode creates a current relative to the amount of infrared light present. The current creates a voltage across R_D . When this voltage level crosses the voltage applied by the voltage divider to the inverting input, the output transitions. Optional R_O provides additional hysteresis for noise immunity.

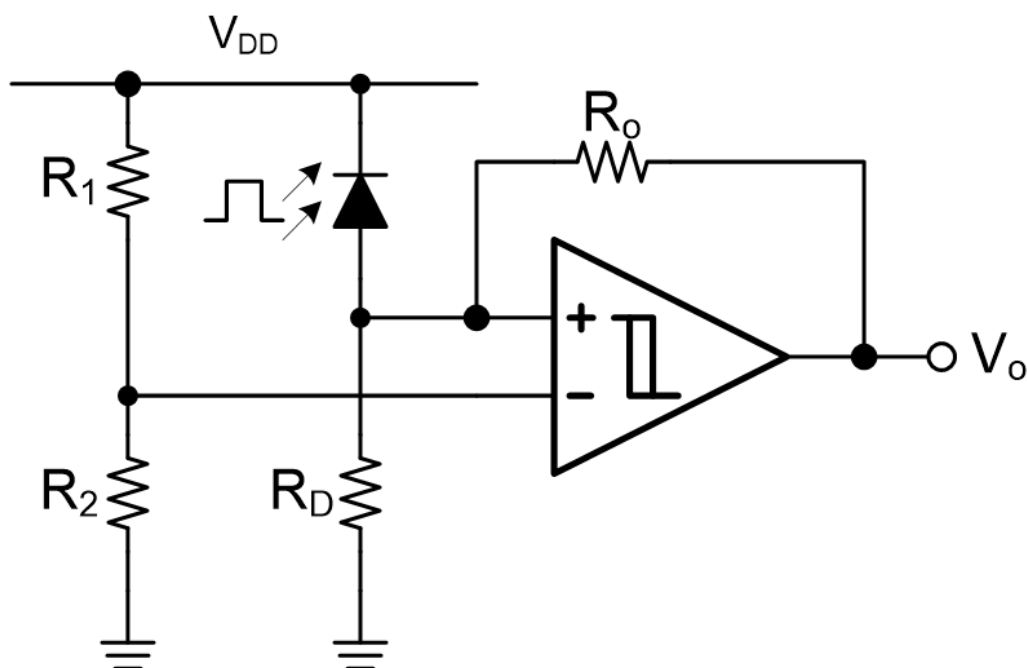
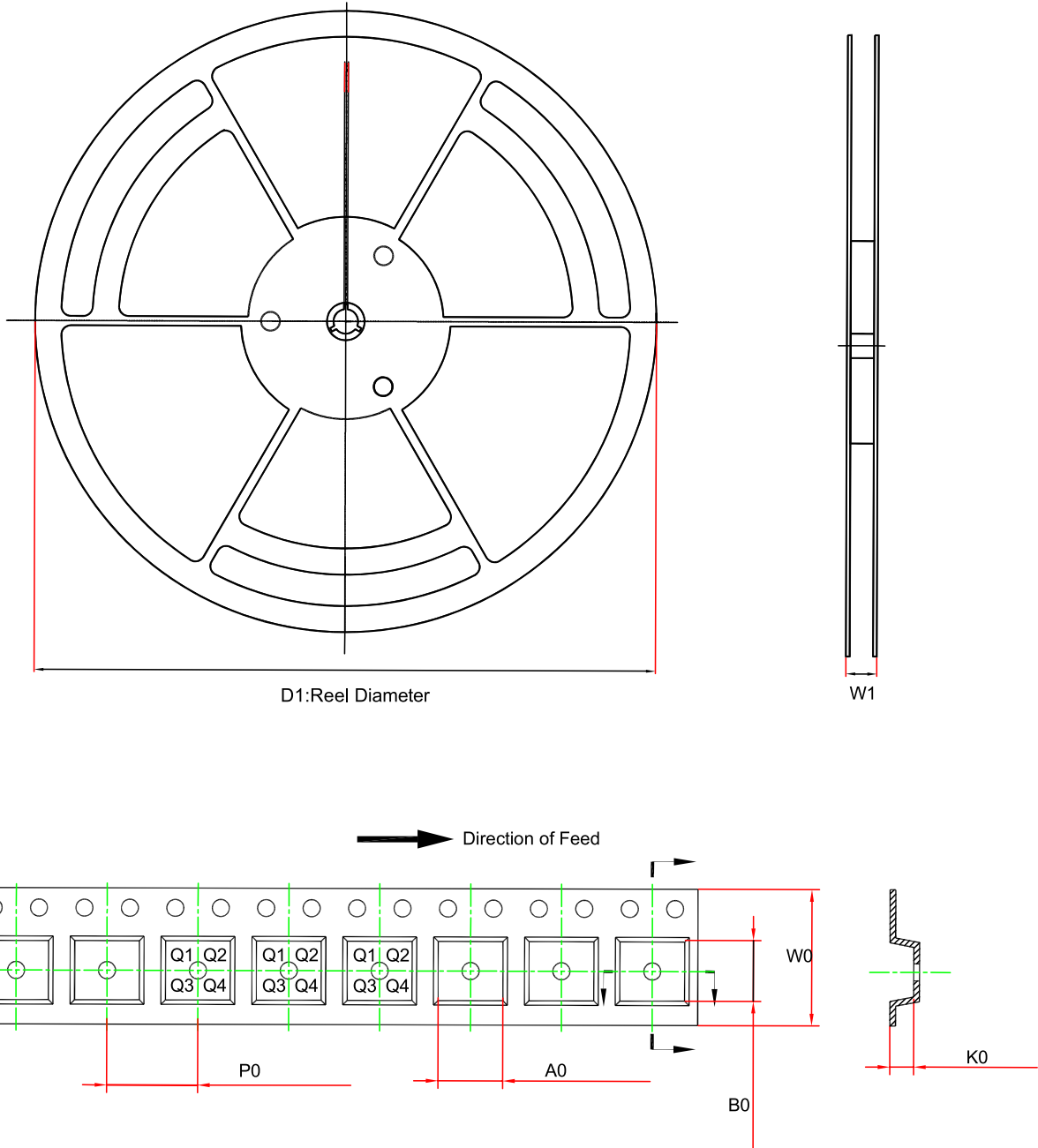


Figure 10. Typical Application Circuit

Tape and Reel Information



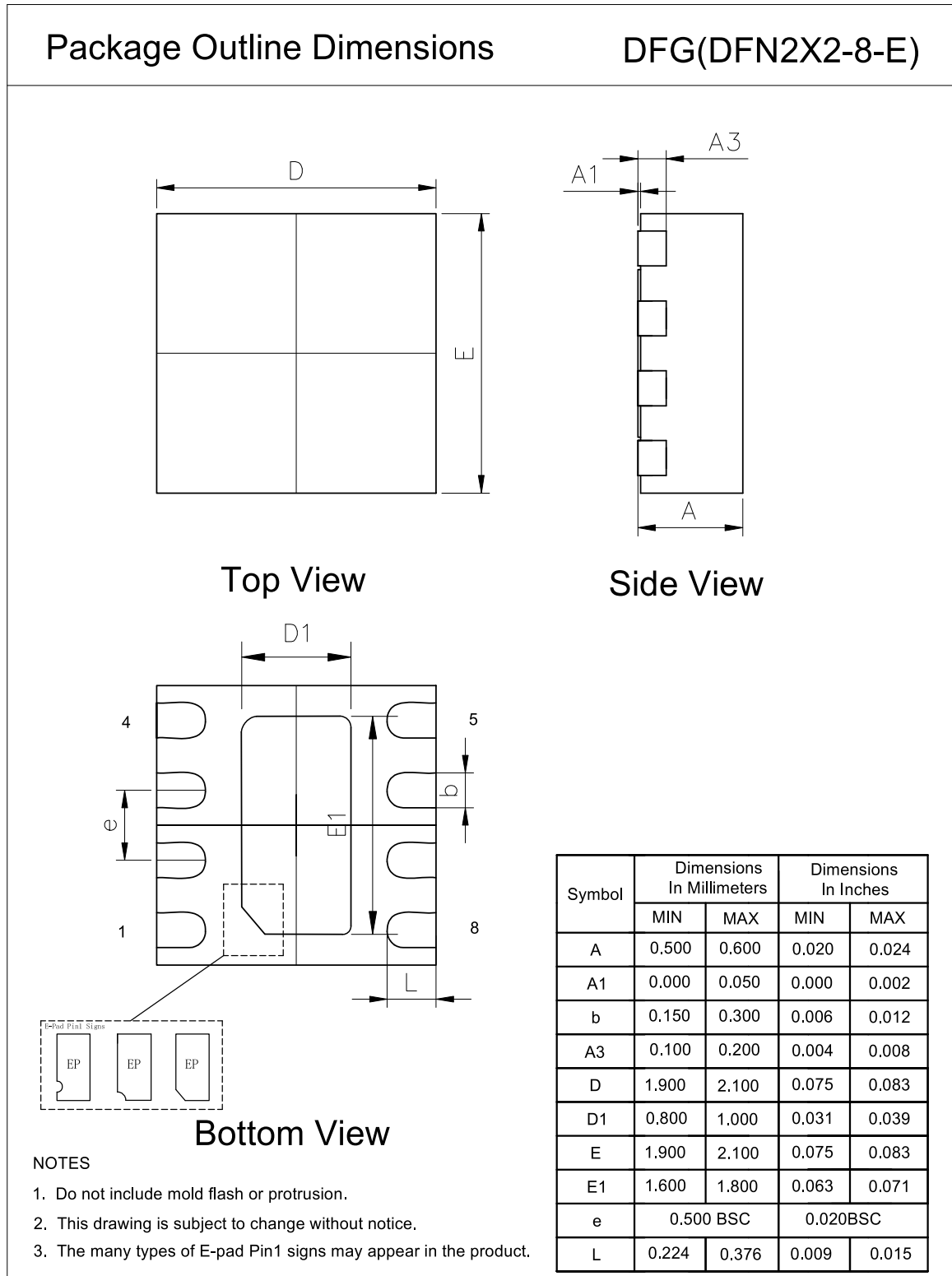
Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm) ⁽¹⁾	B0 (mm) ⁽¹⁾	K0 (mm) ⁽¹⁾	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPCMP191-S5TR	SOT23-5	180	12	3.3	3.25	1.4	4	8	Q3
TPCMP191-SC5R	SOT353(SC70-5)	178	12.1	2.4	2.5	1.2	4	8	Q3
TPCMP191U-S5TR	SOT23-5	180	12	3.3	3.25	1.4	4	8	Q3
TPCMP191U-SC5R	SOT353(SC70-5)	178	12.1	2.4	2.5	1.2	4	8	Q3

5-V Low-Power Comparators with Push-Pull Output

Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm) ⁽¹⁾	B0 (mm) ⁽¹⁾	K0 (mm) ⁽¹⁾	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPCMP191U-DF0R	DFN0.8X0.8-4(5PIN)	180	12.5	0.91	0.91	0.5	2	8	Q2
TPCMP192-SO1R	SOP8	330	17.6	6.5	5.4	2	8	12	Q1
TPCMP192-VS1R	MSOP8	330	17.6	5.3	3.4	1.3	8	12	Q1
TPCMP192-DFGR	DFN2X2-8	180	12.5	2.3	2.3	1.1	4	8	Q2

(1) The value is for reference only. Contact the 3PEAK factory for more information.

Package Outline Dimensions

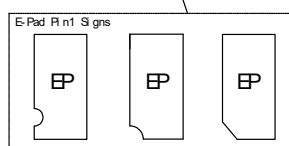
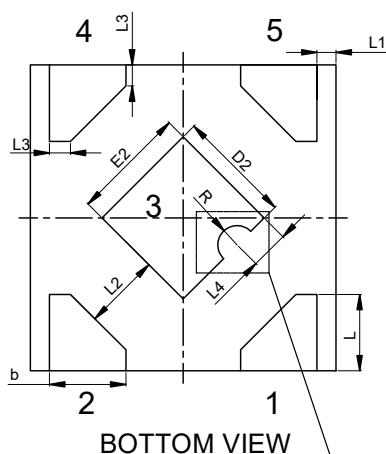
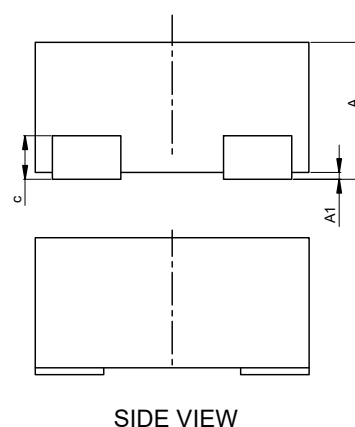
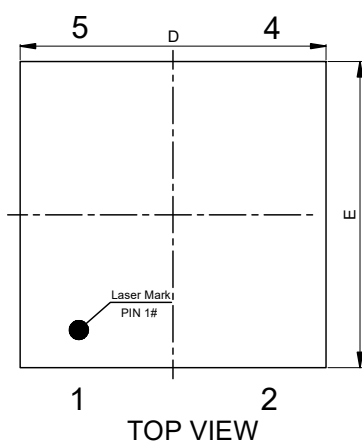
DFN2×2-8


5-V Low-Power Comparators with Push-Pull Output

DFN0.8×0.8-4

Package Outline Dimensions

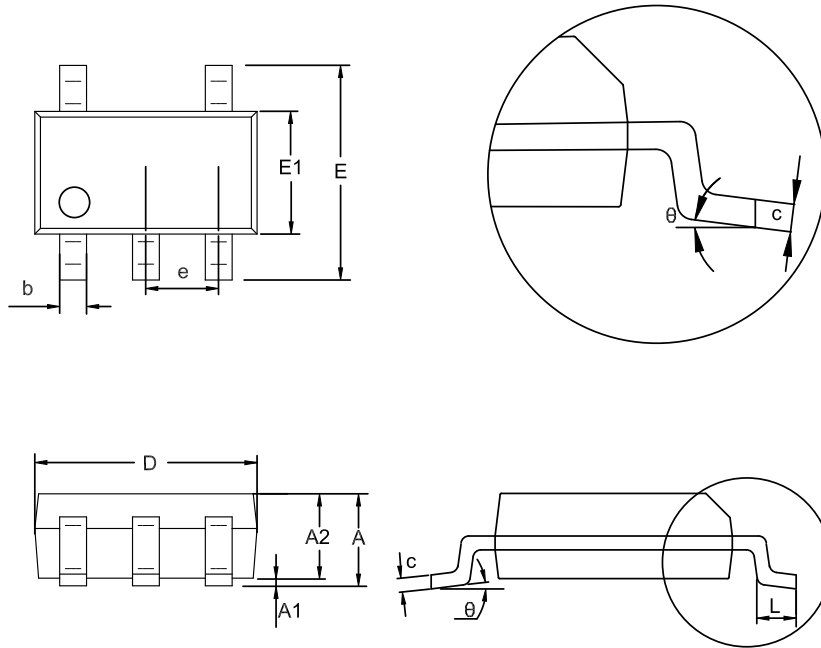
DF0(DFN0.8X0.8-4-A)



NOTES

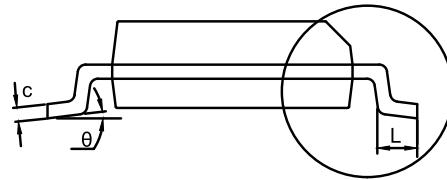
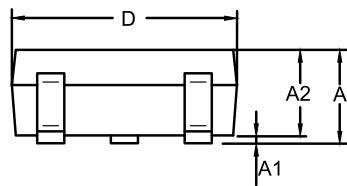
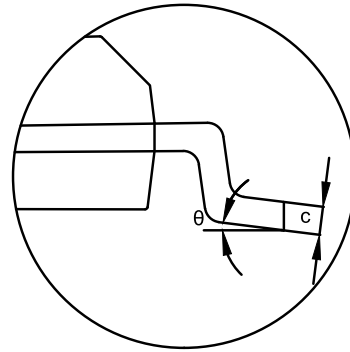
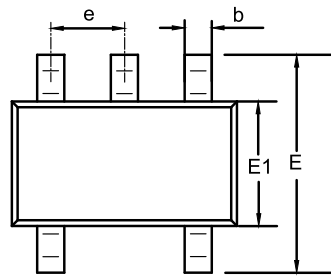
1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.
3. The many types of E-pad Pin1 signs may appear in the product.

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.370	0.430	0.015	0.017
A1	0.000	0.050	0.000	0.002
b	0.150	0.250	0.006	0.010
c	0.100	0.150	0.004	0.006
D	0.750	0.850	0.030	0.033
D2	0.200	0.400	0.008	0.016
E	0.750	0.850	0.030	0.033
E2	0.200	0.400	0.008	0.016
e	0.500 BSC		0.020 BSC	
L	0.150	0.250	0.006	0.010
L1	0.050 BSC		0.002 BSC	
L2	0.200 BSC		0.008 BSC	
L3	0.055 BSC		0.002 BSC	
L4	0.100 BSC		0.004 BSC	

SOT23-5
Package Outline Dimensions
S5T(SOT23-5-A)

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

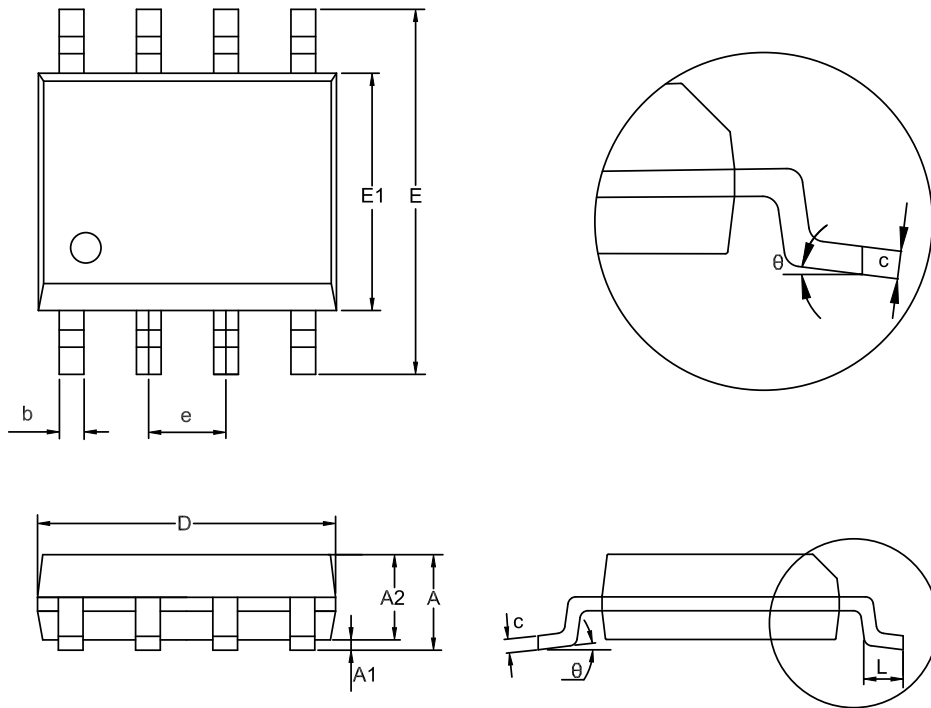
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.050	1.250	0.041	0.049
A1	0.000	0.150	0.000	0.006
A2	1.000	1.200	0.039	0.047
b	0.280	0.500	0.011	0.020
c	0.100	0.230	0.004	0.009
D	2.820	3.020	0.111	0.119
E	2.600	3.000	0.102	0.118
E1	1.500	1.720	0.059	0.068
e	0.950 BSC		0.037 BSC	
L	0.300	0.600	0.012	0.024
θ	0	8°	0	8°

SOT353 (SC70-5)
Package Outline Dimensions
SC5(SOT353-5-A)


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.850	1.100	0.033	0.043
A1	0.000	0.100	0.000	0.004
A2	0.800	1.000	0.031	0.039
b	0.150	0.350	0.006	0.014
c	0.110	0.230	0.004	0.009
D	2.000	2.200	0.079	0.087
E	2.150	2.450	0.085	0.096
E1	1.150	1.350	0.045	0.053
e	0.650 BSC		0.026 BSC	
L	0.260	0.460	0.010	0.018
θ	0	8°	0	8°

NOTES

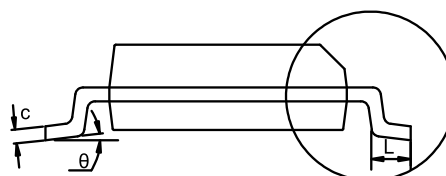
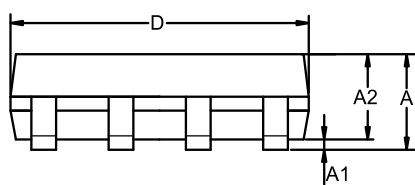
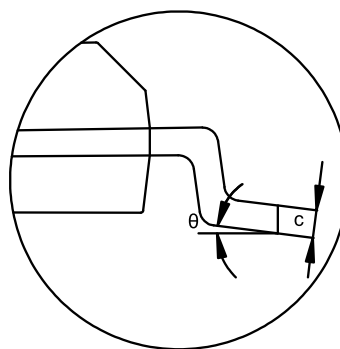
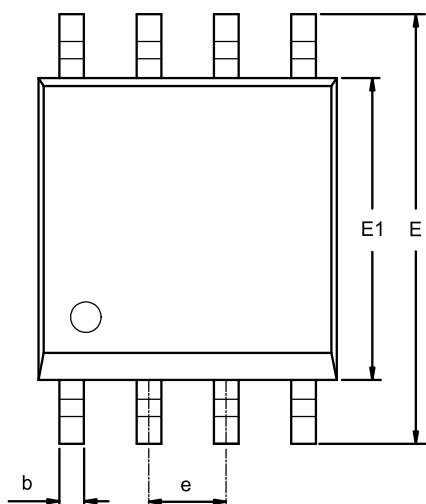
1. Do not include mold flash or protrusion.
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SOP8
Package Outline Dimensions
SO1(SOP-8-A)


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.350	1.750	0.053	0.069
A1	0.050	0.250	0.002	0.010
A2	1.250	1.550	0.049	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
e	1.270 BSC		0.050 BSC	
L	0.400	1.000	0.016	0.039
θ	0	8°	0	8°

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

MSOP8
Package Outline Dimensions
VS1(MSOP-8-A)


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.800	1.100	0.031	0.043
A1	0.020	0.150	0.001	0.006
A2	0.750	0.950	0.030	0.037
b	0.250	0.380	0.010	0.015
c	0.090	0.230	0.004	0.009
D	2.900	3.100	0.114	0.122
E	4.700	5.100	0.185	0.201
E1	2.900	3.100	0.114	0.122
e	0.650 BSC		0.026 BSC	
L	0.400	0.800	0.016	0.031
θ	0	8°	0	8°

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPCMP191-S5TR	-40 to 125°C	SOT23-5	A9J	MSL1	Tape and Reel,3000	Green
TPCMP191-SC5R ⁽¹⁾	-40 to 125°C	SOT353(SC70-5)	A9J	MSL1	Tape and Reel,3000	Green
TPCMP191U-S5TR ⁽¹⁾	-40 to 125°C	SOT23-5	A9K	MSL1	Tape and Reel,3000	Green
TPCMP191U-SC5R ⁽¹⁾	-40 to 125°C	SOT353(SC70-5)	A9K	MSL1	Tape and Reel,3000	Green
TPCMP191U-DF0R ⁽¹⁾	-40 to 125°C	DFN0.8X0.8-4(5PIN)	5	MSL3	Tape and Reel,12000	Green
TPCMP192-SO1R	-40 to 125°C	SOP8	CM192	MSL1	Tape and Reel,4000	Green
TPCMP192-VS1R ⁽¹⁾	-40 to 125°C	MSOP8	CM192	MSL1	Tape and Reel,3000	Green
TPCMP192-DFGR ⁽¹⁾	-40 to 125°C	DFN2X2-8	A9L	MSL1	Tape and Reel,3000	Green

(1) For future products, contact the 3PEAK factory for more information and samples.

Green: 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

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