

## Features

- Supply Voltage: 4.5 V to 36 V
- Unity-Gain Stable
- Offset Voltage:  $\pm 1.5$  mV Maximum at 25°C
- High Speed:
  - Bandwidth: 50 MHz
  - Slew Rate: 20 V/ $\mu$ s
- Very Low Distortion:
  - THD+N =  $-119$  dBc (f = 1 kHz, BW = 80 kHz)
- Rail-to-Rail Output
- Low Noise: 7 nV/ $\sqrt{\text{Hz}}$  at 1 kHz

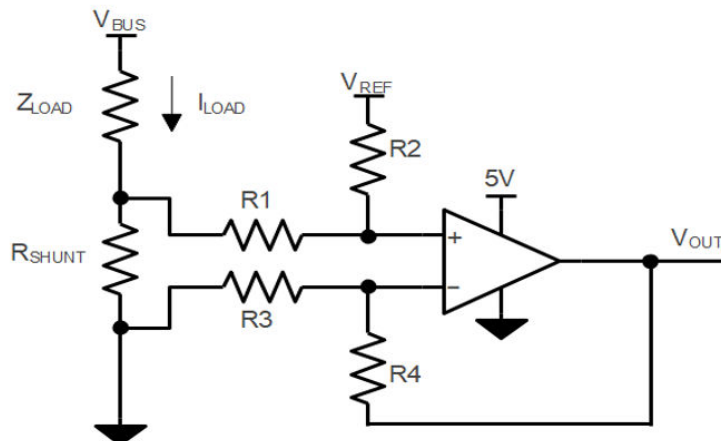
## Description

The TPA250x is a series of ultra-low voltage noise, high-speed voltage feedback amplifiers. The TPA250x is an excellent choice for applications requiring low voltage noise, including communications and imaging. The TPA250x offers very good AC performance with 50 MHz bandwidth, gain = 1 V/V, 20 V/ $\mu$ s slew rate, and 120 ns settling time (0.1%). These amplifiers draw only 8 mA (Type) supply current per channel at 25°C under 36 V supply voltage. The TPA250x has a very low noise of 7 nV/ $\sqrt{\text{Hz}}$  at 1 kHz, which is designed for applications requiring low distortion and low noise, such as buffering analog-to-digital converters.

## Applications

- Low-noise, Wide-band Amplifier for Industrial Applications
- Active Filters
- Video Amplifiers
- Guitar and other Instrument Amplifier
- Soundbar

### Typical Application Circuit



$$V_{\text{OUT}} = (I_{\text{LOAD}} \times R_{\text{SHUNT}}) \times (R_2 / R_1) + V_{\text{REF}}$$

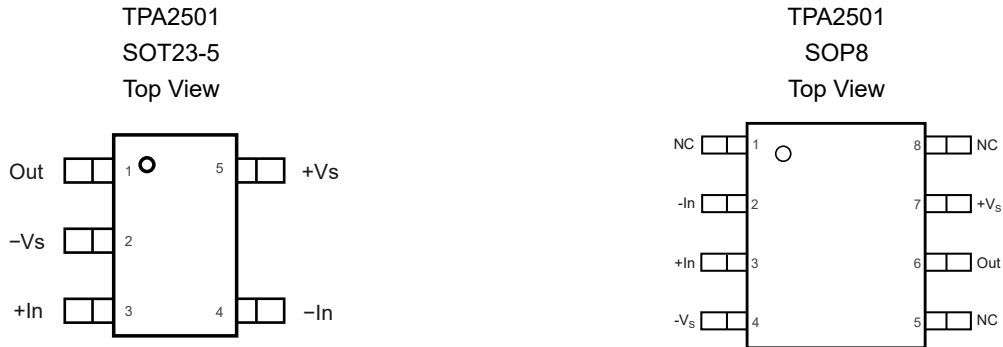
$$\text{When } R_3 = R_1, R_2 = R_4, R_{\text{SHUNT}} \ll R_1$$

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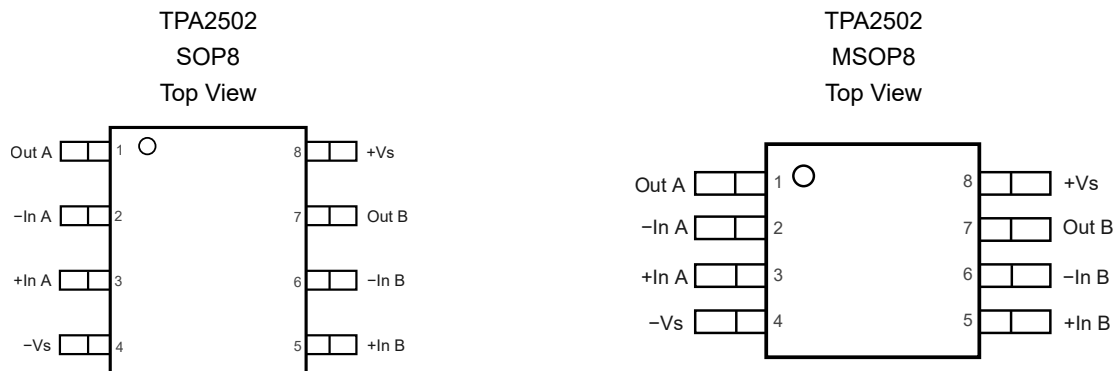
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## Revision History

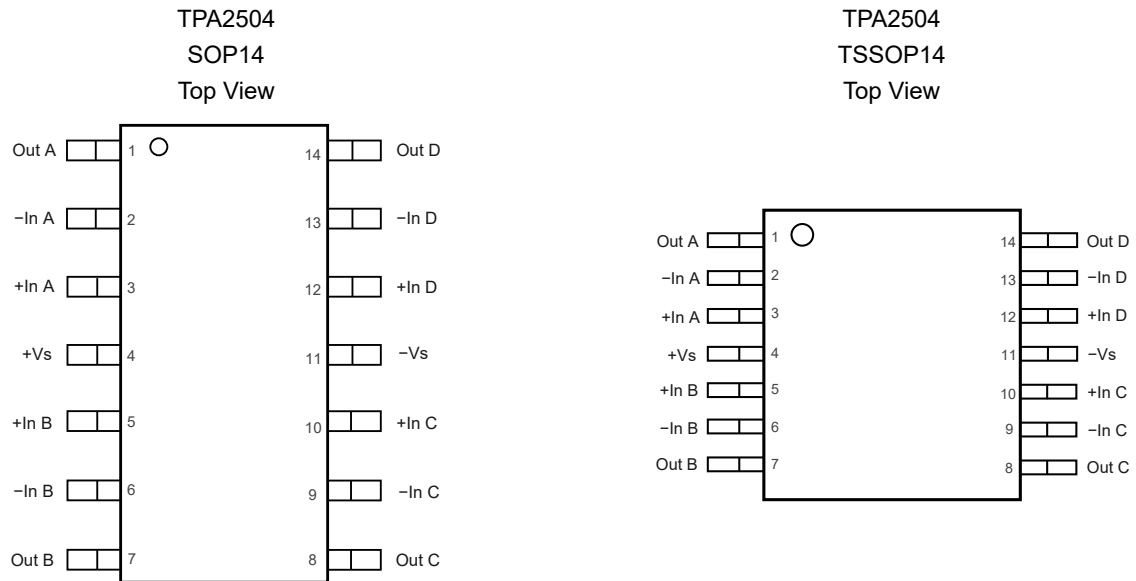
Date	Revision	Notes
2025-12-24	Rev.A.0	Initial version.

**Pin Configuration and Functions**

**Table 1. Pin Functions: TPA2501**

Pin No.		Name	I/O	Description
SOT23-5	SOP8			
1	6	Out	O	Output
2	4	-Vs		Negative power supply
3	3	+In	I	Non-inverting input
4	2	-In	I	Inverting input
5	7	+Vs		Positive power supply
	1	NC		Not Connected
	5	NC		Not Connected
	8	NC		Not Connected


**Table 2. Pin Functions: TPA2502**

Pin No.		Name	I/O	Description
SOP8	MSOP8			
1	1	Out A	O	Output
2	2	-In A	I	Inverting input
3	3	+In A	I	Non-inverting input
4	4	-Vs		Negative power supply
5	5	+In B	I	Non-inverting input
6	6	-In B	I	Inverting input
7	7	Out B	O	Output
8	8	+Vs		Positive power supply


**Table 3. Pin Functions: TPA2504**

Pin No.		Name	I/O	Description
SOP14	TSSOP14			
1	1	Out A	O	Output
2	2	-In A	I	Inverting input
3	3	+In A	I	Non-inverting input
4	4	+Vs		Positive power supply
5	5	+In B	I	Non-inverting input
6	6	-In B	I	Inverting input
7	7	Out B	O	Output
8	8	Out C	O	Output
9	9	-In C	I	Inverting input
10	10	+In C	I	Non-inverting input
11	11	-Vs		Negative power supply
12	12	+In D	I	Non-inverting input
13	13	-In D	I	Inverting input
14	14	Out D	O	Output

## Specifications

### Absolute Maximum Ratings <sup>(1)</sup>

Parameter		Min	Max	Unit
	Supply Voltage, (+V <sub>S</sub> ) – (–V <sub>S</sub> )		40	V
	Input Voltage	(–V <sub>S</sub> ) – 0.3	(+V <sub>S</sub> ) + 0.3	V
	Differential Input Voltage	–1.5	1.5	V
	Input Current: +IN, –IN <sup>(2)</sup>	–10	10	mA
	Output Voltage	(–V <sub>S</sub> ) – 0.3	(+V <sub>S</sub> ) + 0.3	V
	Output Short-Circuit Duration <sup>(3)</sup>		Infinite	
T <sub>J</sub>	Maximum Junction Temperature		150	°C
T <sub>A</sub>	Operating Temperature Range	–40	125	°C
T <sub>STG</sub>	Storage Temperature Range	–65	150	°C
T <sub>L</sub>	Lead Temperature (Soldering, 10 sec)		260	°C

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

(2) The inputs are protected by ESD-protection diodes to the power supply. If the input extends more than 300 mV beyond the power supply, the input current should be limited to less than 10 mA.

(3) A heat sink may be required to keep the junction temperature below the absolute maximum rating. This depends on the power dissipation of the application. The thermal resistance varies with the amount of PC board metal connected to the package.

### ESD, Electrostatic Discharge Protection

Symbol	Parameter	Condition	Minimum Level	Unit
HBM	Human Body Model ESD	ANSI/ESDA/JEDEC JS-001 <sup>(1)</sup>	7	kV
CDM	Charged Device Model ESD	ANSI/ESDA/JEDEC JS-002 <sup>(2)</sup>	1.5	kV

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

### Recommended Operating Conditions

Parameter		Min	Typ	Max	Unit
V <sub>S</sub>	Supply Voltage, (+V <sub>S</sub> ) – (–V <sub>S</sub> )	4.5		36	V
T <sub>A</sub>	Operating Temperature Range	–40		125	°C

### Thermal Information

Package Type	θ <sub>JA</sub>	θ <sub>Jc</sub>	Unit
SOT23-5	250	81	°C/W

Package Type	$\theta_{JA}$	$\theta_{JC}$	Unit
SOP8	158	43	°C/W
MSOP8	210	45	°C/W
SOP14	120	36	°C/W
TSSOP14	180	35	°C/W

## Electrical Characteristics

All test conditions:  $V_S = 36\text{ V}$ ,  $V_{CM} = V_S / 2$ ,  $T_A = 25^\circ\text{C}$ ,  $R_L = 2\text{ k}\Omega$ , unless otherwise noted.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Power Supply</b>						
$V_S$	Supply Voltage Range	$(+V_S) - (-V_S)$	4.5		36	V
$I_Q$	Quiescent Current per Amplifier	$V_S = 5\text{ V}$		5.5	6.5	mA
		$V_S = 5\text{ V}$ , $T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$			8.5	mA
		$V_S = 36\text{ V}$		8	9	mA
		$V_S = 36\text{ V}$ , $T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$			10.5	mA
PSRR	Power Supply Rejection Ratio	$V_S = 4.5\text{ V}$ to $36\text{ V}$	95	105		dB
		$V_S = 4.5\text{ V}$ to $36\text{ V}$ , $T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$	90			dB
<b>Input Characteristics</b>						
$V_{OS}$	Input Offset Voltage	$V_S = 5\text{ V}$ , $V_{CM} = 2.5\text{ V}$	-1	$\pm 0.2$	1	mV
		$V_S = 5\text{ V}$ , $T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$	-1.5		1.5	mV
		$V_S = 36\text{ V}$ , $V_{CM} = 18\text{ V}$	-1.5	$\pm 0.2$	1.5	mV
		$V_S = 36\text{ V}$ , $T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$	-2		2	mV
$V_{OS\ TC}$	Input Offset Voltage Drift	$V_S = 5\text{ V}$ , $T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$		2.5		$\mu\text{V}/^\circ\text{C}$
		$V_S = 36\text{ V}$ , $T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$		2.5		$\mu\text{V}/^\circ\text{C}$
$I_B$	Input Bias Current	$V_S = 5\text{ V}$ to $36\text{ V}$	-2.0	$\pm 1.6$	2.0	$\mu\text{A}$
$I_{OS}$	Input Offset Current	$V_S = 4.5\text{ V}$ to $36\text{ V}$		$\pm 35$		nA
$A_v$	Open-Loop Voltage Gain	$V_{OUT} = 0.4\text{ V}$ to $4.6\text{ V}$	85	90		dB
		$V_{OUT} = 0.4\text{ V}$ to $4.6\text{ V}$ , $T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$	80			dB
$V_{CMR}$	Common-Mode Input Voltage Range		$(-V_S) + 1.3$		$(+V_S) - 1.5$	V
CMRR	Common-Mode Rejection Ratio	$V_S = 5\text{ V}$ , $V_{CM} = 1.3\text{ V}$ to $3.5\text{ V}$	90	105		dB
		$V_S = 5\text{ V}$ , $V_{CM} = 1.3\text{ V}$ to $3.5\text{ V}$ , $T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$	85			dB
		$V_S = 36\text{ V}$ , $V_{CM} = 1.3\text{ V}$ to $34.5\text{ V}$	90	105		dB
		$V_S = 36\text{ V}$ , $V_{CM} = 1.3\text{ V}$ to $34.5\text{ V}$ , $T_A = -40^\circ\text{C}$ to $125^\circ\text{C}$	85			dB
<b>Output Characteristics</b>						
	Output Swing from Positive Rail	$V_S = 5\text{ V}$ , $R_L = 2\text{ k}\Omega$ to $V_S / 2$		20	40	mV
		$V_S = 36\text{ V}$ , $R_L = 2\text{ k}\Omega$ to $V_S / 2$		180	200	mV
		$V_S = 5\text{ V}$ , $R_{mOL} = 75\ \Omega$ to $V_S / 2$		410	450	mV
	Output Swing from Negative Rail	$V_S = 5\text{ V}$ , $R_L = 2\text{ k}\Omega$ to $V_S / 2$		20	40	mV

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I <sub>sc</sub>	Output Short-Circuit Current	V <sub>S</sub> = 36 V, R <sub>L</sub> = 2 kΩ to V <sub>S</sub> / 2		160	180	mV
		V <sub>S</sub> = 5 V, R <sub>L</sub> = 75 Ω to V <sub>S</sub> / 2		410	450	mV
		V <sub>S</sub> = 5 V, R <sub>L</sub> = 75 Ω to V <sub>S</sub> , Sink		50		mA
		V <sub>S</sub> = 5 V, R <sub>L</sub> = 75 Ω to ground, Source		50		mA
<b>AC Specifications</b>						
GBW	Gain-Bandwidth Product	G = 10		50		MHz
PM	Phase Margin	G = 10, R <sub>L</sub> = 2 kΩ, C <sub>L</sub> = 10 pF		45		°
SR	Slew Rate Up	G = 1, f = 1 kHz, V <sub>OUT</sub> = 2 V <sub>PP</sub>		20		V/μs
	Slew Rate Down	G = 1, f = 1 kHz, V <sub>OUT</sub> = 2 V <sub>PP</sub>		20		V/μs
t <sub>OR</sub>	Overload Recovery			150		ns
t <sub>s</sub>	Settling Time, 0.1%	G = 1, 2 V step		120		ns
<b>Noise Performance</b>						
e <sub>N</sub>	Input Voltage Noise Density	V <sub>S</sub> = 5 V, f = 1 kHz		7		nV/√Hz
THD+N	Total Harmonic Distortion and Noise	f = 1 kHz, V <sub>OUT</sub> = 0.5 V <sub>PP</sub> , V = ±2.25 V or 4.5 V, BW = 80 kHz		98		dB
		f = 1 kHz, V <sub>OUT</sub> = 1 V <sub>PP</sub> , V = ±2.5 V or 5 V, BW = 80 kHz		105		dB
		f = 1 kHz, V <sub>OUT</sub> = 6 V <sub>PP</sub> , V = ±5 V or 10 V, BW = 80 kHz		116		dB
		f = 1 kHz, V <sub>OUT</sub> = 3 V <sub>RMS</sub> , V = ±12 V or 24 V, BW = 80 kHz		119		dB

**Typical Performance Characteristics**

All test conditions:  $V_S = 30\text{ V}$ ,  $T_A = +25^\circ\text{C}$ ,  $R_L = 2\text{ k}\Omega$ , unless otherwise noted.

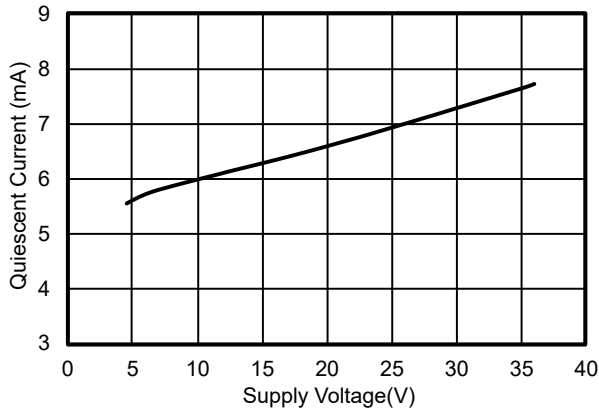


Figure 1. Supply Current vs. Supply Voltage, 1ch

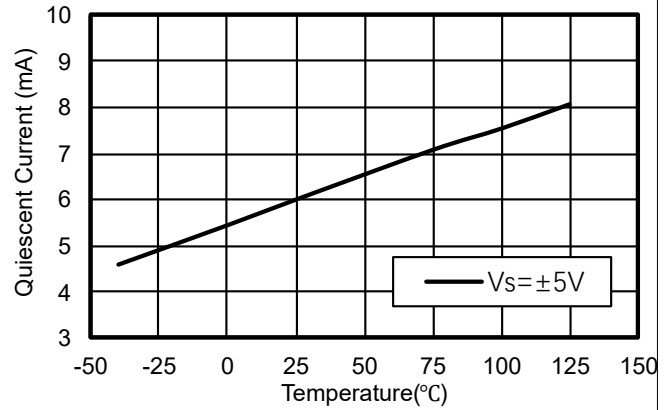


Figure 2. Supply Current vs. Temperature, 1ch

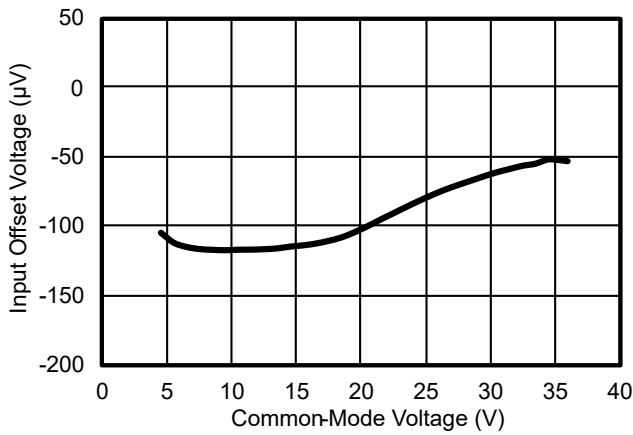


Figure 3. Offset Voltage vs. Supply Voltage

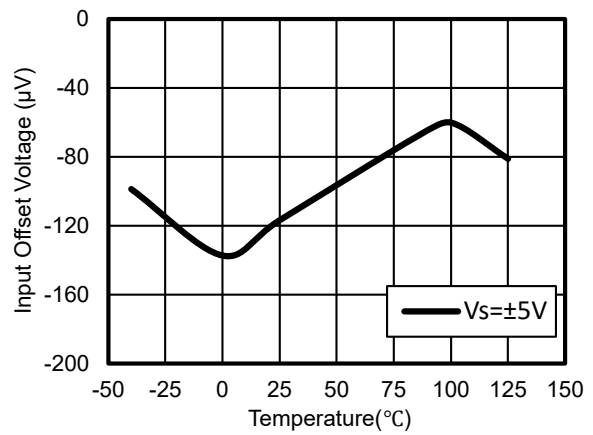


Figure 4. Offset Voltage vs. Temperature

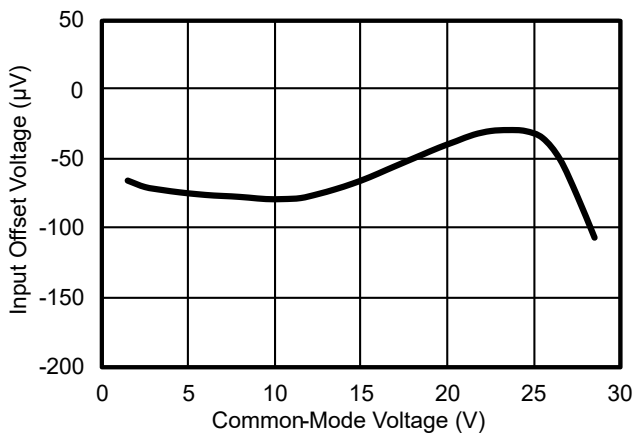


Figure 5. Offset Voltage vs. Common-Mode Voltage

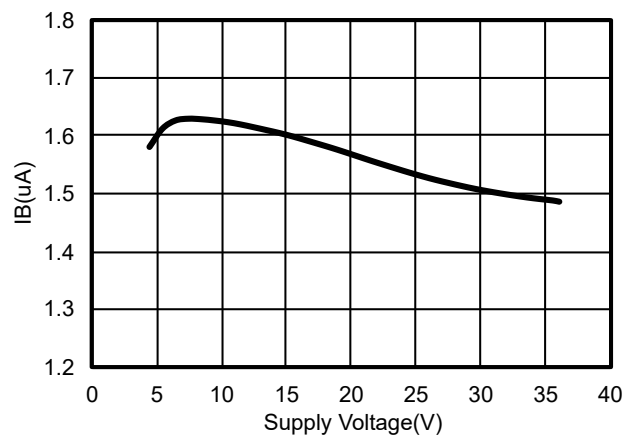


Figure 6.  $I_B$  vs. Supply Voltage

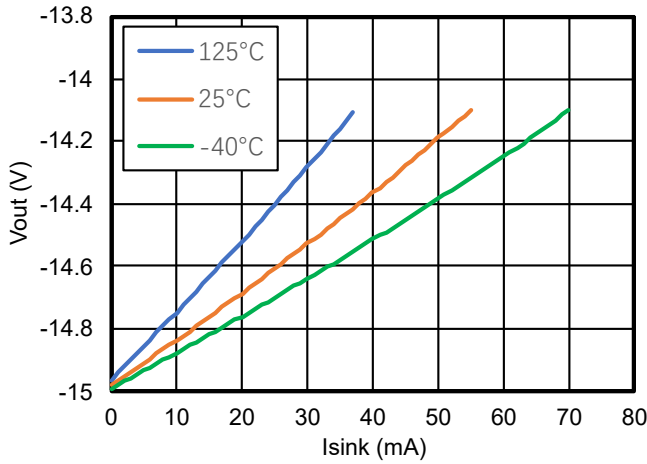


Figure 7.  $V_{OUT}$  vs.  $I_{OUT}$ , Sink,  $V_{CC} = \pm 15\text{ V}$

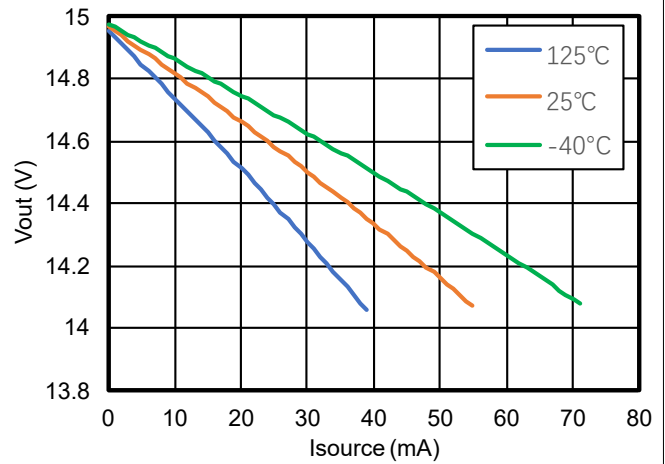


Figure 8.  $V_{OUT}$  vs.  $I_{OUT}$ , Source,  $V_{CC} = \pm 15\text{ V}$

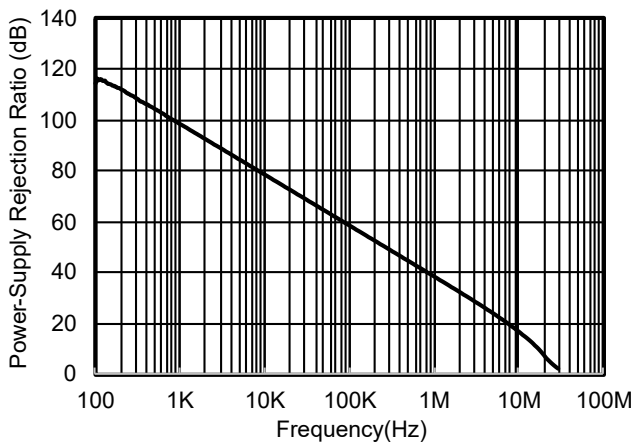


Figure 9. PSRR+ vs. Frequency

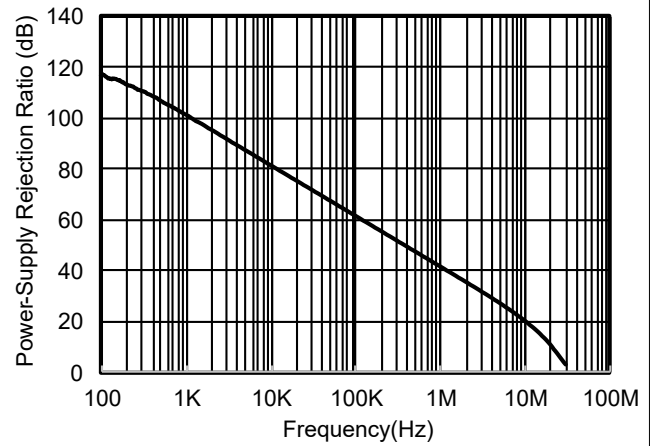


Figure 10. PSRR- vs. Frequency

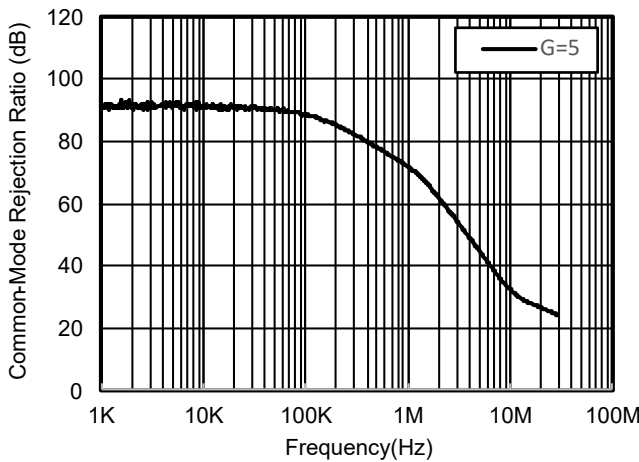


Figure 11. CMRR vs. Frequency

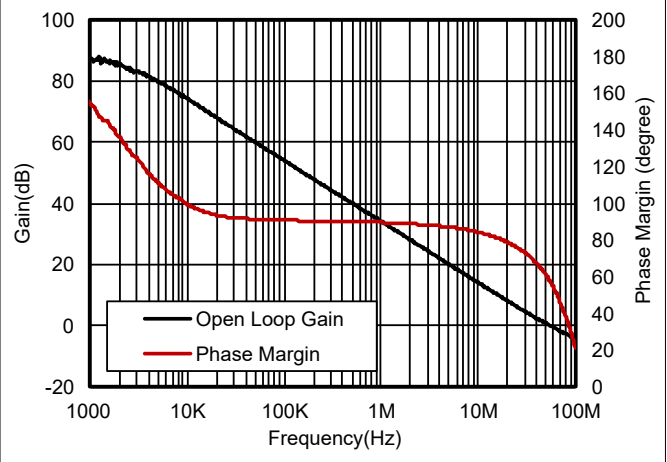


Figure 12. Open-Loop Gain and Phase vs. Frequency,  $R_L = 2\text{ k}\Omega$

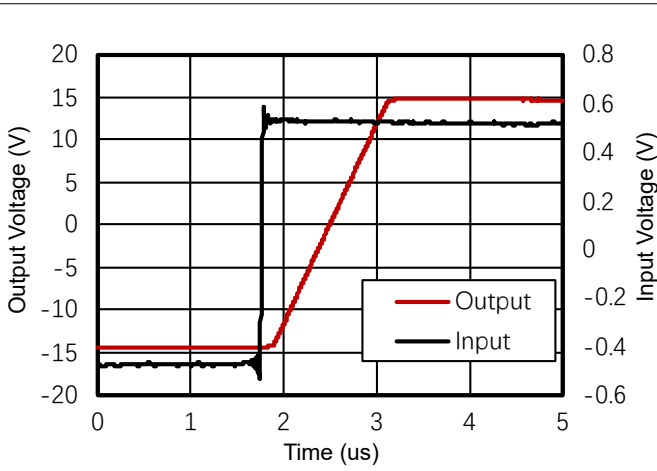


Figure 13. Positive Overload Recovery, G = 5

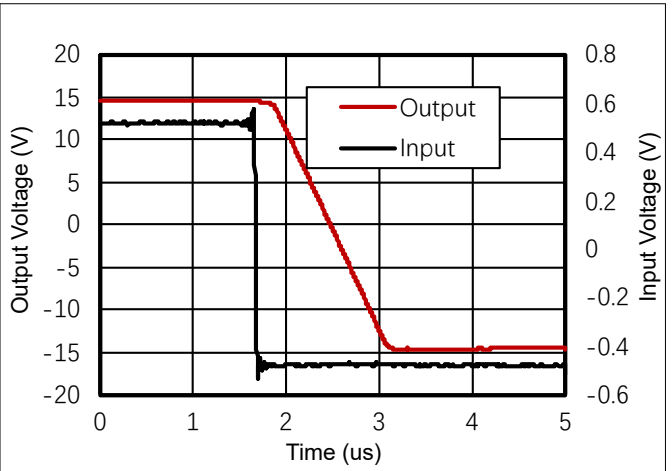


Figure 14. Negative Overload Recovery, G = 5

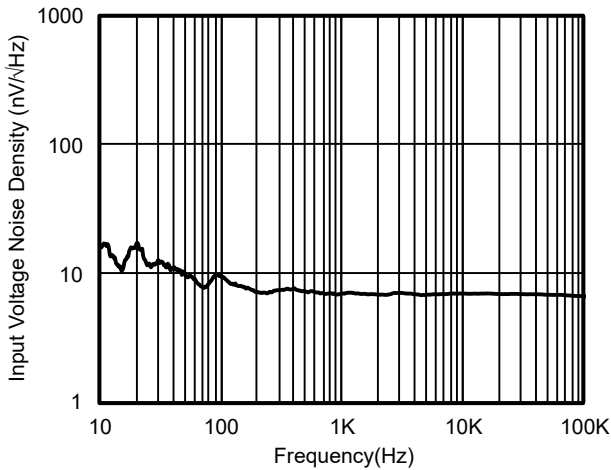


Figure 15. Voltage Noise Spectral Density vs. Frequency

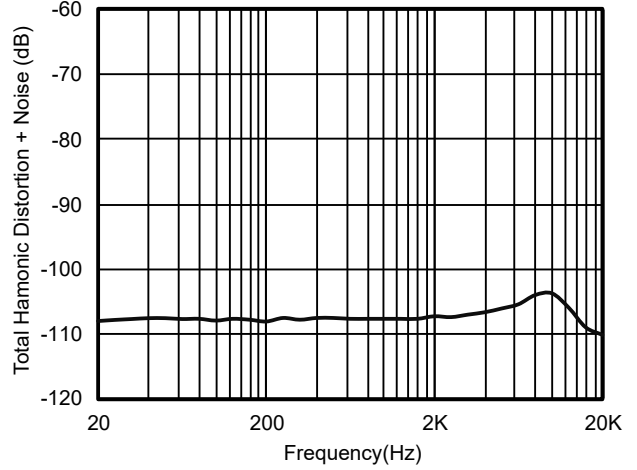


Figure 16. THD+N vs. Frequency, 3 VRMS

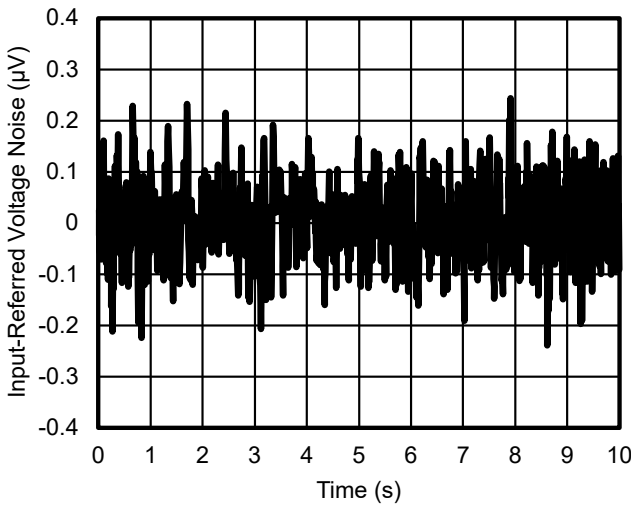


Figure 17. 0.1 Hz to 10 Hz Voltage Noise

## Detailed Description

### Overview

The TPA250x is a series of op amps that operate on a single-supply voltage (4.5 V to 36 V), or a split-supply voltage ( $\pm 2.25$  V to  $\pm 18$  V), making the series highly versatile and easy to use. The power-supply pins should have local bypass ceramic capacitors (typically 0.01  $\mu$ F to 0.1  $\mu$ F). These amplifiers are fully specified from 4.5 V to 36 V and over the extended temperature range from  $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ . Parameters that exhibit variance with regard to operating voltage or temperature are presented in [Typical Performance Characteristics](#).

### Functional Block Diagram

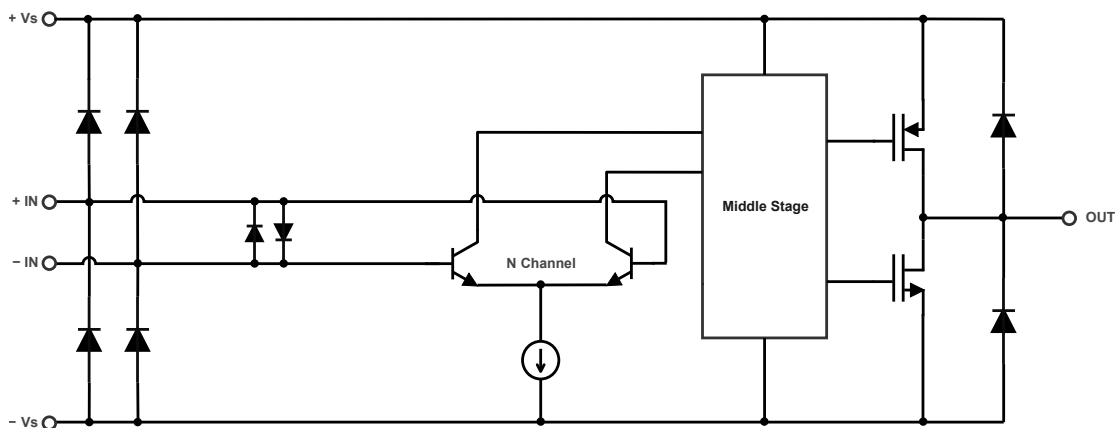


Figure 18. Functional Block Diagram

### Feature Description

#### Operating Voltage

The series is designed for single supply operation from 4.5 V to 36 V or dual supply operation from  $\pm 2.25$  V to  $\pm 18$  V.

The recommended operating voltage conditions are as follows:

Power supply voltage ( $+V_S$ ) - ( $-V_S$ ): 4.5 V to 36 V. The power supply voltage can support the following three scenarios:

- Single supply;
- Dual supplies with equal voltage values;
- Various voltage configurations, as long as the voltage range of ( $+V_S$ ) - ( $-V_S$ ) is within 4.5 V to 36 V.

For example, if operating with a single supply, ( $-V_S$ ) = 0 V, ( $+V_S$ ) can support 4.5 V to 36 V. If using dual supplies with equal absolute values, the minimum voltage is  $\pm 2.25$  V, and the maximum voltage is  $\pm 18$  V. It can even support other voltage configurations, such as ( $-V_S$ ) = -6 V, ( $+V_S$ ) = 30 V and so on.

#### Rail-to-Rail Output

The devices deliver rail-to-rail output swing capability with a class-AB output stage. Different load conditions change the ability of the amplifier to swing close to the rails.

## Application and Implementation

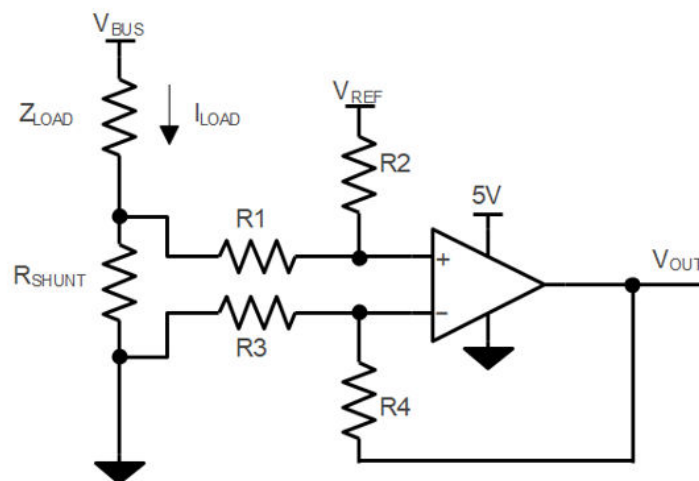
Note

Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

## Application Information

### Low-Side Current-Sensing Application

Figure 19 shows the devices configured in a low-side current-sensing application. The low-side current-sensing method is to place a sense resistor between the load and the circuit ground. The voltage dropping across the resistor is amplified by different amplifier circuits with the devices. The  $V_{REF}$  can be used to add a bias voltage to the output voltage. Particular attention must be paid to the matching and precision of R1, R2, R3, and R4, to maximize the accuracy of the measurement.



$$V_{OUT} = (I_{LOAD} \times R_{SHUNT}) \times (R2 / R1) + V_{REF}$$

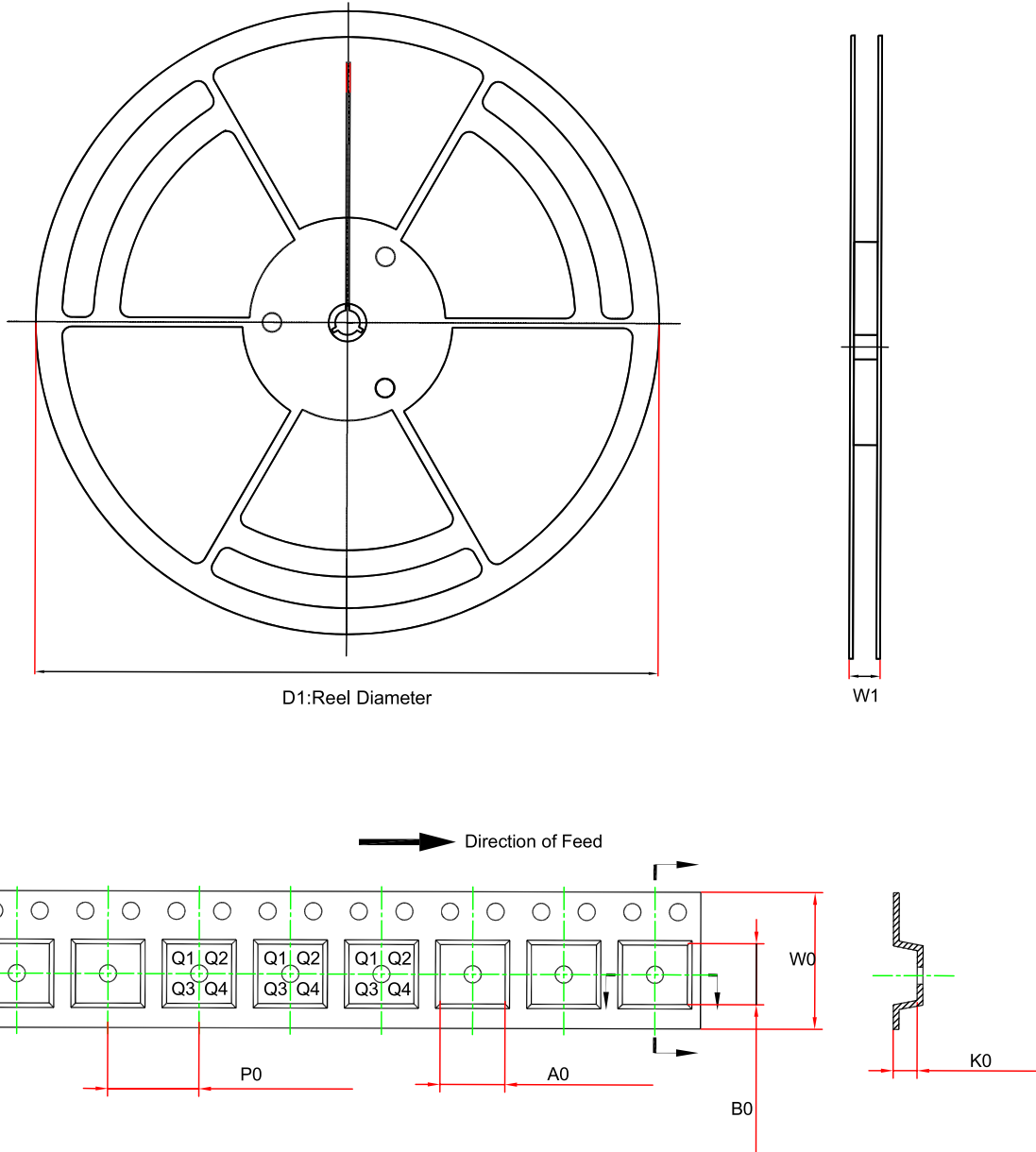
$$\text{When } R3 = R1, R2 = R4, R_{SHUNT} \ll R1$$

**Figure 19. Low-Side Current-Sensing Application**

### Power Supply Recommendations

Place 0.1- $\mu$ F bypass capacitors close to the power supply pins to reduce coupling errors from the noise or high-impedance power supplies.

### Tape and Reel Information

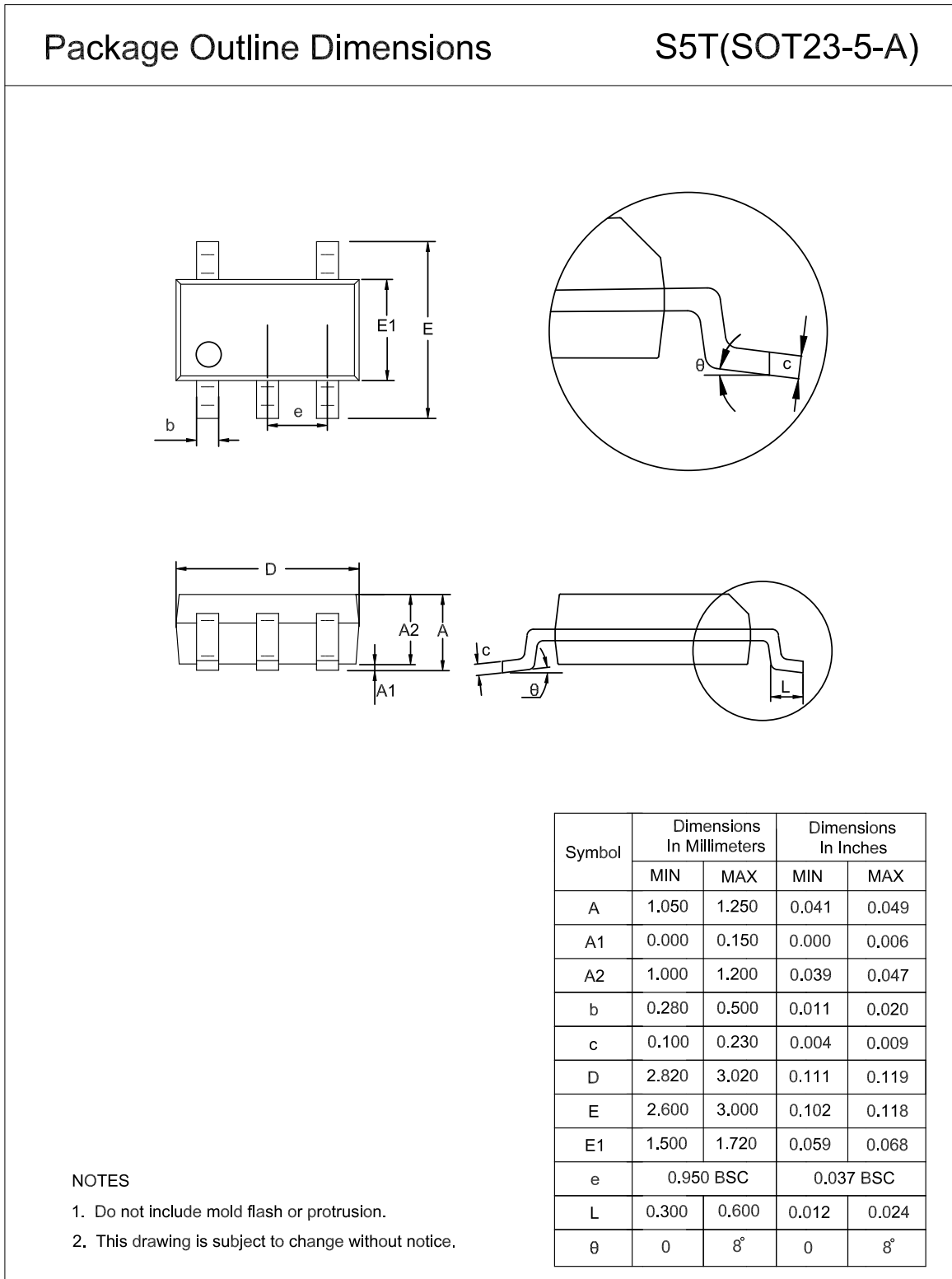


Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm) <sup>(1)</sup>	B0 (mm) <sup>(1)</sup>	K0 (mm) <sup>(1)</sup>	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPA2501-S5TR	SOT23-5	180	12	3.3	3.25	1.4	4	8	Q3
TPA2501-SO1R	SOP8	330	17.6	6.5	5.4	2	8	12	Q1
TPA2502-SO1R	SOP8	330	17.6	6.5	5.4	2	8	12	Q1
TPA2502-VS1R	MSOP8	330	17.6	5.3	3.4	1.3	8	12	Q1
TPA2504-SO2R	SOP14	330	21.6	6.5	9.3	2.1	8	16	Q1
TPA2504-TS2R	TSSOP14	330	17.6	6.8	5.5	1.5	8	12	Q1

(1) The value is for reference only. Contact the 3PEAK factory for more information.

Package Outline Dimensions

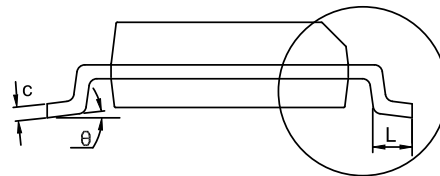
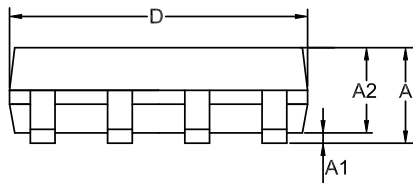
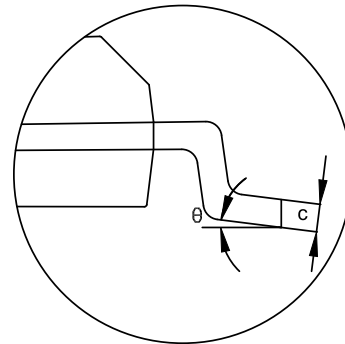
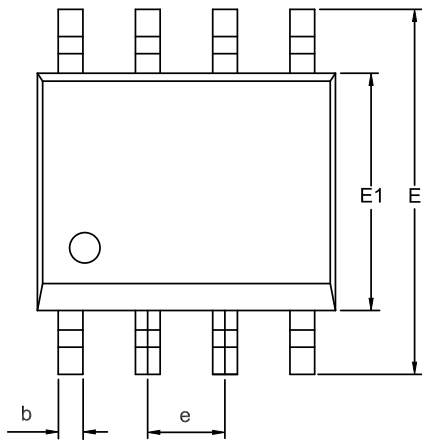
SOT23-5



SOP8

Package Outline Dimensions

SO1(SOP-8-A)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.350	1.750	0.053	0.069
A1	0.050	0.250	0.002	0.010
A2	1.250	1.550	0.049	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
e	1.270 BSC		0.050 BSC	
L	0.400	1.000	0.016	0.039
$\theta$	0	8°	0	8°

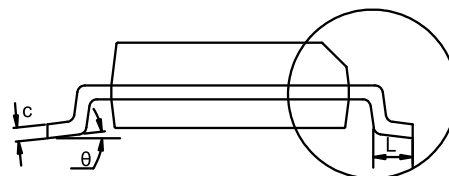
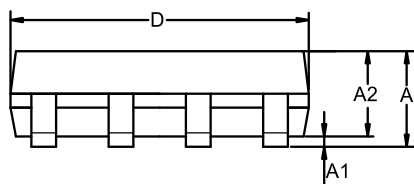
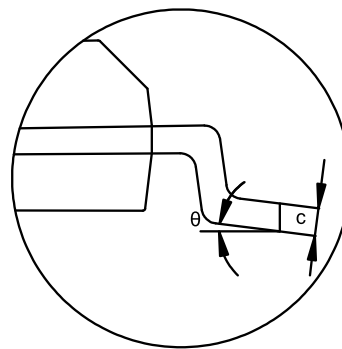
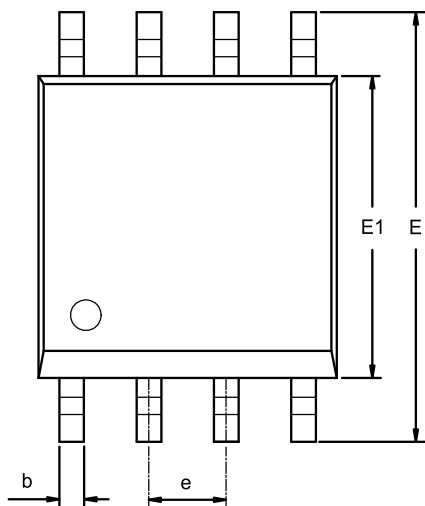
NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

MSOP8

Package Outline Dimensions

VS1(MSOP-8-A)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.800	1.100	0.031	0.043
A1	0.020	0.150	0.001	0.006
A2	0.750	0.950	0.030	0.037
b	0.250	0.380	0.010	0.015
c	0.090	0.230	0.004	0.009
D	2.900	3.100	0.114	0.122
E	4.700	5.100	0.185	0.201
E1	2.900	3.100	0.114	0.122
e	0.650 BSC		0.026 BSC	
L	0.400	0.800	0.016	0.031
$\theta$	0	8°	0	8°

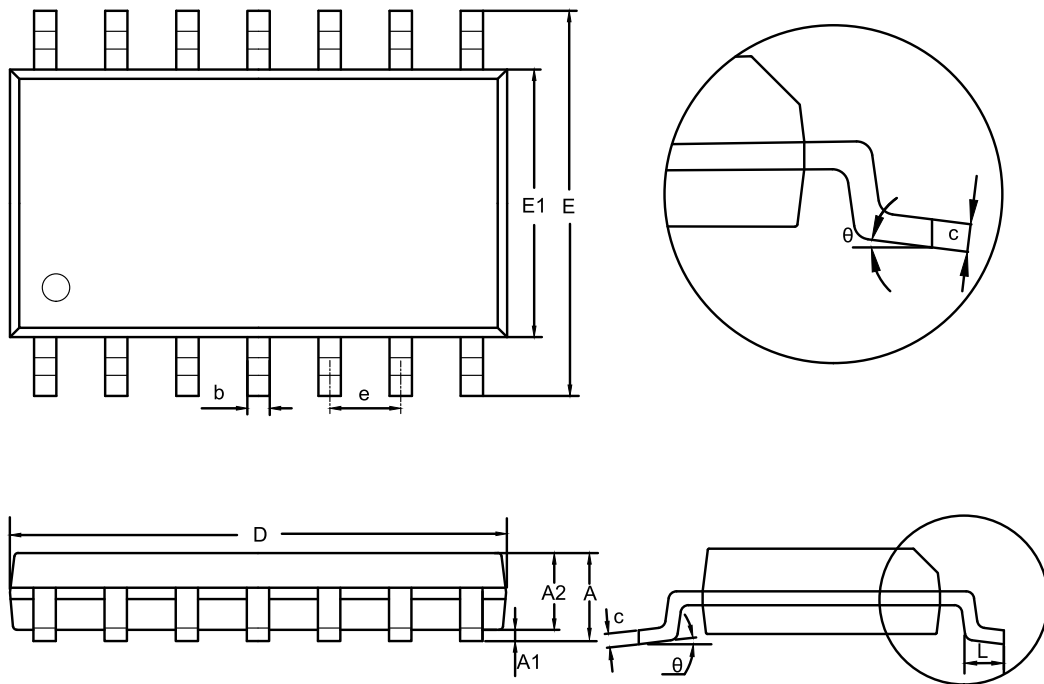
NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

SOP14

Package Outline Dimensions

SO2(SOP-14-A)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.350	1.750	0.053	0.069
A1	0.050	0.250	0.002	0.010
A2	1.250	1.650	0.049	0.065
b	0.310	0.510	0.012	0.020
c	0.100	0.250	0.004	0.010
D	8.450	8.850	0.333	0.348
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
e	1.270 BSC		0.050 BSC	
L	0.400	1.270	0.016	0.050
theta	0	8°	0	8°

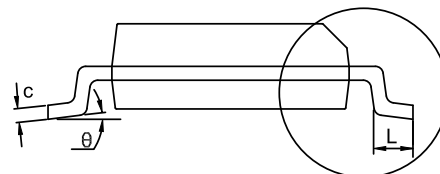
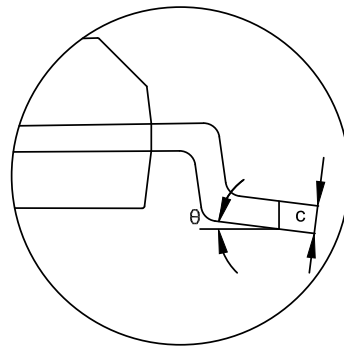
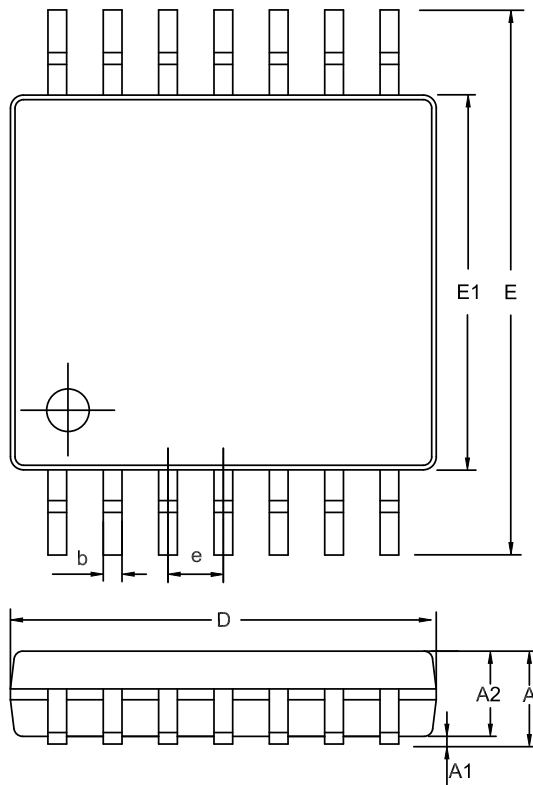
NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

TSSOP14

Package Outline Dimensions

TS2(TSSOP-14-A)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.900	1.200	0.035	0.047
A1	0.050	0.150	0.002	0.006
A2	0.800	1.050	0.031	0.041
b	0.190	0.300	0.007	0.012
c	0.090	0.200	0.004	0.008
D	4.900	5.100	0.193	0.201
E	6.200	6.600	0.244	0.260
E1	4.300	4.500	0.169	0.177
e	0.650 BSC		0.026 BSC	
L	0.450	0.750	0.018	0.030
θ	0	8°	0	8°

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

## Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPA2501-S5TR <sup>(1)</sup>	-40 to 125°C	SOT23-5	AA7	1	Tape and Reel, 3000	Green
TPA2501-SO1R <sup>(1)</sup>	-40 to 125°C	SOP8	A2501	1	Tape and Reel, 4000	Green
TPA2502-SO1R	-40 to 125°C	SOP8	A2502	1	Tape and Reel, 4000	Green
TPA2502-VS1R <sup>(1)</sup>	-40 to 125°C	MSOP8	A2502	1	Tape and Reel, 3000	Green
TPA2504-SO2R <sup>(1)</sup>	-40 to 125°C	SOP14	A2504	1	Tape and Reel, 2500	Green
TPA2504-TS2R <sup>(1)</sup>	-40 to 125°C	TSSOP14	A2504	3	Tape and Reel, 3000	Green

(1) For future products, contact the 3PEAK factory for more information and samples.

**Green:** 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

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