Features

- Supply Voltage: 4.5 V to 36 V or ±2.25 V to ±18 V
- Offset Voltage: ±40 µV Maximum at 25℃
- Differential Input Voltage Range to Supply Rail, can Work as a Comparator
- Input Rail to $-V_S$, Rail-to-Rail Output
- Bandwidth: 6 MHz, Slew Rate: 5 V/µs
- Excellent EMI Suppress Performance: 85 dB at 1 GHz
- Over-Temperature Protection
- Low Noise: 8 nV/√Hz at 1 kHz
- AEC-Q100 Qualification
- −40°C to 125°C Operation Temperature Range

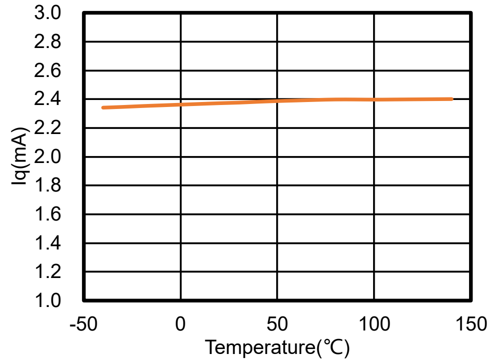
Applications

- Instrumentation
- Active Filters, ASIC Input or Output Amplifier
- Sensor Interface
- Motor Control
- Industrial Control

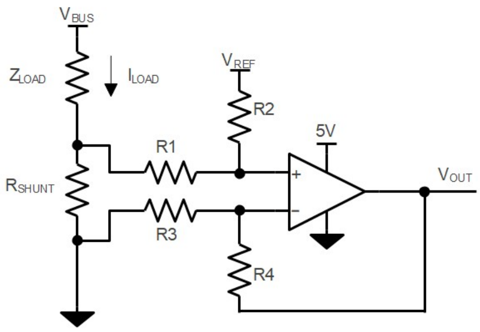
Description

The TPA186xQ series of amplifiers are the newest high-supply voltage amplifiers with 40-µV low offset, low noise, and stable high-frequency response. They incorporate 3PEAK's proprietary and patented design techniques to achieve excellent AC performance with 6-MHz bandwidth, 5-V/µs slew rate, and low distortion while drawing only 1.4-mA quiescent current per amplifier. The input common-mode voltage range extends to V−, and the outputs swing rail-to-rail.

The TPA186xQ has an over-temperature protection feature to guarantee chip safety. The output of the TPA186xQ enters high impendence when the die temperature reaches around 170°C and recovers the function when the die temperature is down to around 150°C. The product has a very small power temperature coefficient, which is helpful for temperature-sensitive applications.



Typical Application Circuit



$V_{OUT} = (I_{LOAD} \times R_{SHUNT}) \times (R2 / R1) + V_{REF}$

When R3 = R1, R2 = R4, $R_{SHUNT} << R1$

Table of Contents

Revision History

Date	Revision	Notes
2024-02-15	Rev.A.0	Initial version

Pin Configuration and Functions

TPA1861Q
SOT23-5 Package
Top View

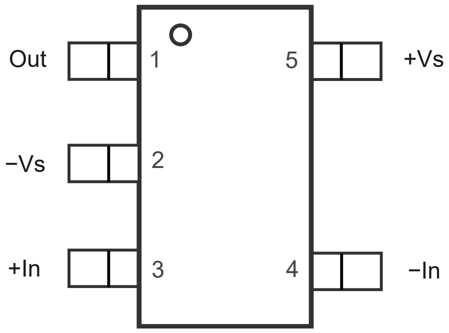


Table 1. Pin Functions: TPA1861Q

Pin SOT23-5	Name	I/O	Description
1	Out	Output	Output
2	$-V_S$		Negative power supply
3	+In	Input	Noninverting input
4	−In	Input	Inverting input
5	$+V_S$		Positive power supply

TPA1862Q
SOP8 Package
Top View

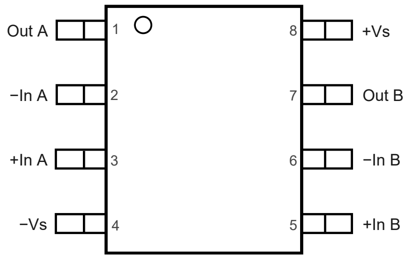


Table 2. Pin Functions: TPA1862Q

Pin	Name	I/O	Description
SOP8			
1	Out A	Output	Output
2	−In A	Input	Inverting input
3	+In A	Input	Noninverting input
4	−VS		Negative power supply
5	+In B	Input	Noninverting input
6	−In B	Input	Inverting input
7	Out B	Output	Output
8	+VS		Positive power supply

Specifications

Absolute Maximum Ratings (1)

All test conditions: Over operating ambient temperature, unless otherwise noted.

Parameter		Min	Max	Unit
	Supply Voltage, $(+V_S) - (-V_S)$		40 V	V
	Input Voltage	$(-V_S) - 0.3$	40 V	V
	Differential Input Voltage	$(-V_S) - (+V_S)$	$(+V_S) - (-V_S)$	V
	Input Current: +IN, −IN (2)	−10	10	mA
	Output Voltage	$(-V_S) - 0.3$	$(+V_S) + 0.3$	V
	Output Short-Circuit Duration (3)		Infinite	
T_J	Maximum Operating Junction Temperature		150	°C
T_A	Operating Temperature Range	−40	125	°C
T_{STG}	Storage Temperature Range	−65	150	°C
T_L	Lead Temperature (Soldering, 10 sec)		260	°C

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.
(2) The inputs are protected by ESD protection diodes to the negative power supply. If the input extends to more than 300 mV beyond the negative power supply, the input current should be limited to less than 10 mA.
(3) A heat sink may be required to keep the junction temperature below the absolute maximum. This depends on the power supply voltage and how many amplifiers are shorted. Thermal resistance varies with the amount of PC board metal connected to the package. The specified values are for short traces connected to the leads.

ESD, Electrostatic Discharge Protection

Parameter		Condition	Minimum Level	Unit
HBM	Human Body Model ESD	AEC-Q100-002	2	kV
CDM	Charged Device Model ESD	AEC-Q100-011	1	kV

Recommended Operating Conditions

Parameter			Min	Typ	Max	Unit
V_S	Supply Voltage	Single Supply	4.5		36	V
		Dual Supply	±2.25		±18	V
T_A	Operating Temperature Range		−40		125	°C

Thermal Information

Package Type	θ_{JA}	θ_{JC}	Unit
SOT23-5	250	81	°C/W

Package Type	θ_{JA}	θ_{JC}	Unit
SOP8	158	43	°C/W

Electrical Characteristics

All test conditions: V_S = 30 V, T_A = 25°C, R_L = 10 kΩ, unless otherwise noted.

Parameter		Conditions	T_A	Min	Typ	Max	Unit
Power Supply							
V_S	Supply Voltage Range	($+V_S$) - ($-V_S$)		4.5		36	V
I_Q	Quiescent Current per Amplifier	V_S = 30 V			1.4	1.6	mA
			−40°C to 125°C			2	mA
		V_S = 5 V			1.2	1.5	mA
			−40°C to 125°C			1.8	mA
PSRR	Power Supply Rejection Ratio	V_S= 4.5 V to 36 V		115	140		dB
			−40°C to 125°C	110			dB
Input Characteristics							
V_{OS}	Input Offset Voltage	V_S = 30 V, V_{CM} = 15 V		−40		40	μV
			−40°C to 125°C	−80		80	μV
		V_S = 5 V, V_{CM} = 2.5 V		−40		40	μV
			−40°C to 125°C	−80		80	μV
$V_{OS}TC$	Input Offset Voltage Drift		−40°C to 125°C		0.01		μV/°C
I_B	Input Bias Current				100	1000	pA
			−40°C to 125°C		100	5000	pA
I_{OS}	Input Offset Current				100	1000	pA
			−40°C to 125°C		100	5000	pA
I_{IN}	Different Input Current	V_S = 36 V, V_{ID} = 36 V			10	100	μA
			−40°C to 125°C			120	μA
C_{IN}	Input Capacitance	Differential Mode			5		pF
		Common Mode			2.5		pF
Av	Open-loop Voltage Gain	R_{LOAD} = 10 kΩ, VOUT = 0.5 V to 29.5 V		120	140		dB
			−40°C to 125°C	115			dB
V_{CMR}	Common-mode Input Voltage Range			(V−)		(V+) – 1.5	V
CMRR	Common Mode Rejection Ratio	V_{CM} = 0 V to 28.5 V		115	140		dB
			−40°C to 125°C	110			dB
Output Characteristics							
	Output Swing from Positive Rail	R_{LOAD} = 100 kΩ to V_S/2			12	25	mV
			−40°C to 125°C			40	mV
		R_{LOAD} = 10 kΩ to V_S/2			80	120	mV
			−40°C to 125°C			200	mV
		R_{LOAD} = 2 kΩ to V_S/2			370	500	mV

Parameter		Conditions	T_A	Min	Typ	Max	Unit
			−40°C to 125°C			750	mV
	Output Swing from Negative Rail	R_{LOAD} = 100 kΩ to V_S/2			5	25	mV
			−40°C to 125°C			30	mV
		R_{LOAD} = 10 kΩ to V_S/2			30	80	mV
			−40°C to 125°C			200	mV
		R_{LOAD} = 2 kΩ to V_S/2			140	300	mV
			−40°C to 125°C			500	mV
I_{SC}	Output Short-Circuit Current	Source		60	95		mA
			−40°C to 125°C	35			mA
		Sink		120	150		mA
			−40°C to 125°C	70			mA
	Capacitive Load Drive				1		nF
AC Specifications							
GBW	Gain-Bandwidth Product				6		MHz
SR	Slew Rate	G = 1, 10 V step		3	5		V/μs
			−40°C to 125°C	2.2			V/μs
t_{OR}	Overload Recovery				500		ns
t_S	Settling Time, 0.1%	G = 1, 10 V step			7		μs
	Settling Time, 0.01%				12		μs
PM	Phase Margin	R_L = 10 K, C_L = 100 pF			70		°
GM	Gain Margin	R_L = 10 K, C_L = 100 pF			15		dB
Noise Performance							
E_N	Input Voltage Noise	f = 0.1 Hz to 10 Hz			0.2		μV_{PP}
e_N	Input Voltage Noise Density	f = 0.1 Hz			8		nV/√Hz
		f = 1 kHz			8		nV/√Hz
		f = 10 kHz			10		nV/√Hz
		f = 100 kHz			20		nV/√Hz
i_N	Input Current Noise	f = 10 kHz			200		fA/√Hz
THD+N	Total Harmonic Distortion and Noise	f = 1 kHz, G =1, R_L = 10 kΩ, V_{OUT} = 6 V_{RMS}			0.0005		%

Typical Performance Characteristics

All test conditions: V_S = ±15 V, V_{CM} = 0 V, R_L = 10 kΩ, unless otherwise noted.

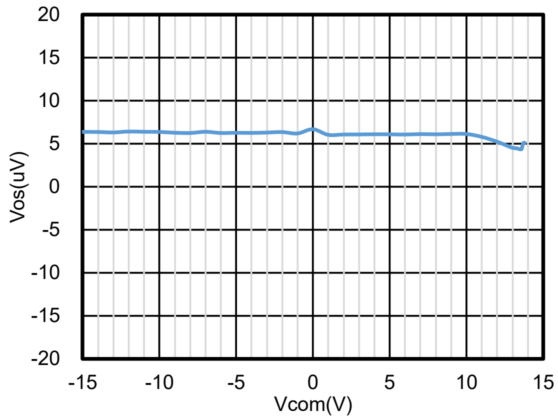


Figure 1. Offset Voltage vs. Common-Mode Voltage

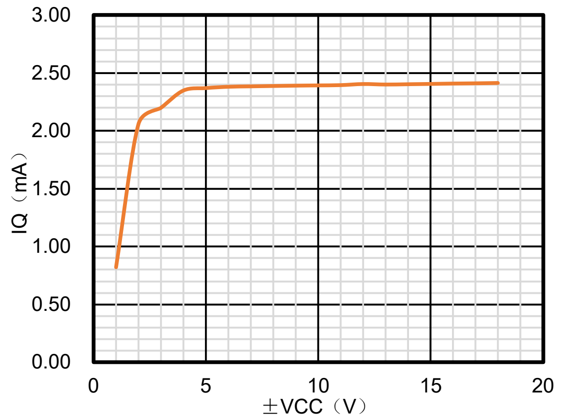


Figure 2. Iq vs. Supply Voltage

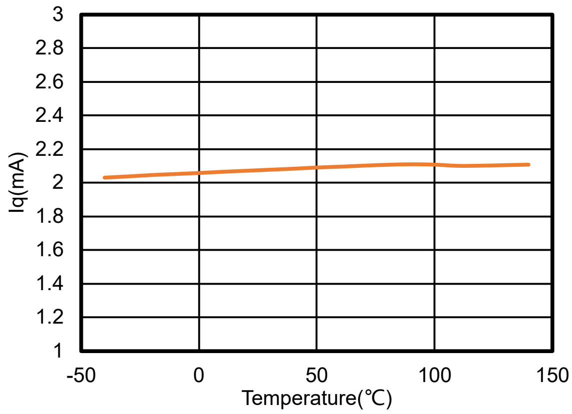


Figure 3. Iq vs. Temperature, ±2.5 V Supply, TPA1862Q

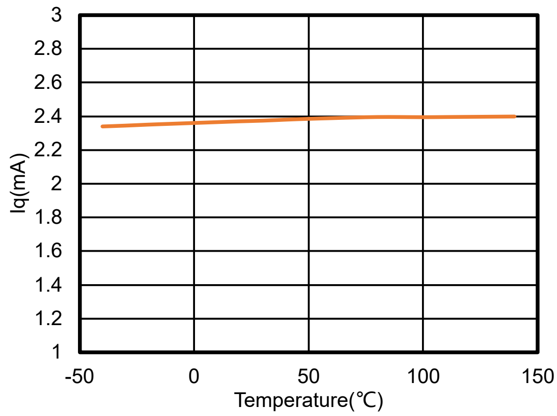


Figure 4. Iq vs. Temperature, ±15 V Supply, TPA1862Q

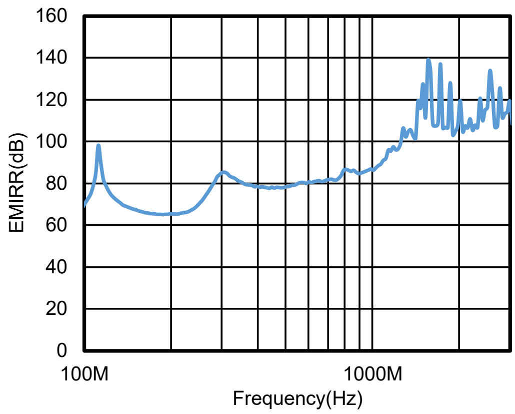


Figure 5. EMIRR vs. Frequency

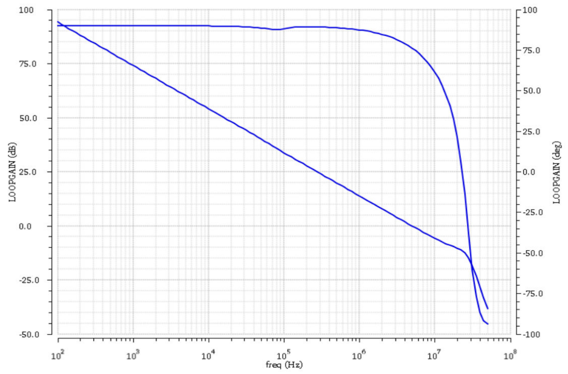


Figure 6. Open Loop Gain and Phase vs. Frequency R_L = 10 kΩ, C_L = 50 pF

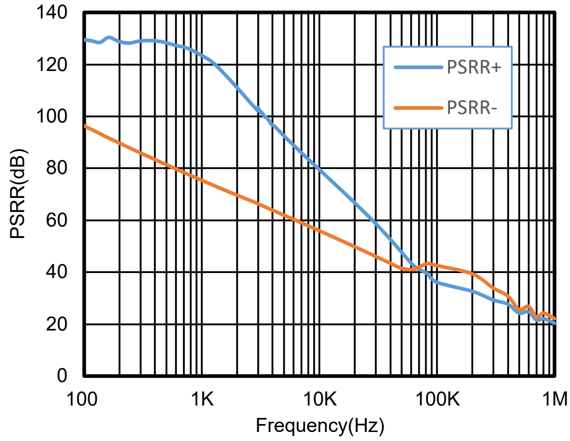


Figure 7. PSRR vs. Frequency

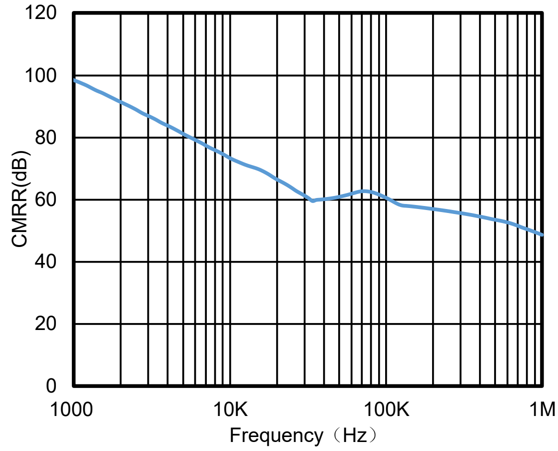
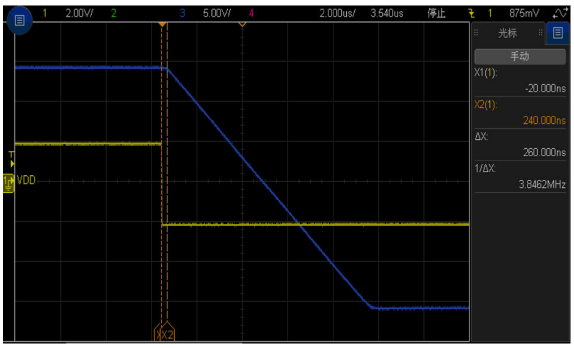


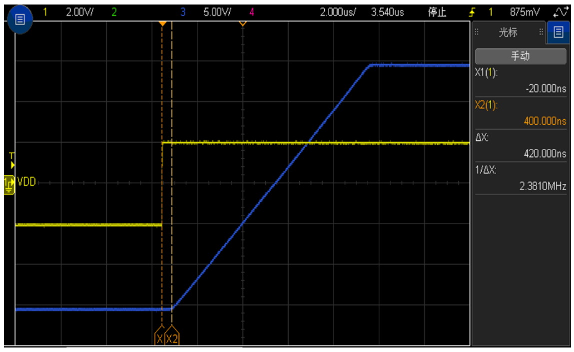
Figure 8. CMRR vs. Frequency



Time: 2 us/div, Measure Time: 260 ns

R_L= 2 K, C_L= 100 pF, G = 10

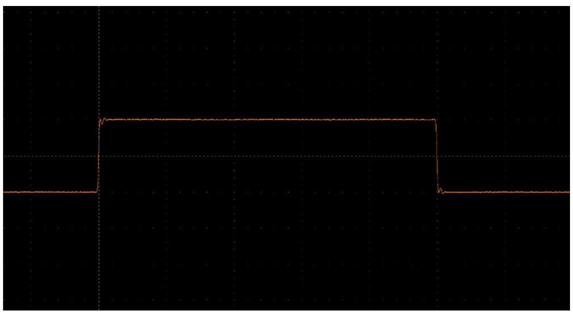
Figure 9. Positive Overload Recovery



Time: 2 us/div, Measure Time: 420 ns

R_L = 2 K, C_L = 100 pF, G = 10

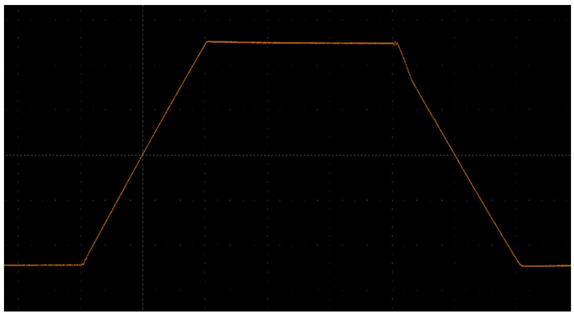
Figure 10. Negative Overload Recovery



Voltage: 50 mV/div, Time: 2 us/div

R_L = 2 K, C_L = 100 pF, G = 1

Figure 11. 100-mV Signal Step Response



Voltage: 2 V/div, Time: 2 μs/div

R_L =2 K, C_L =100 pF, G = 1

Figure 12. 10-V Signal Step Response

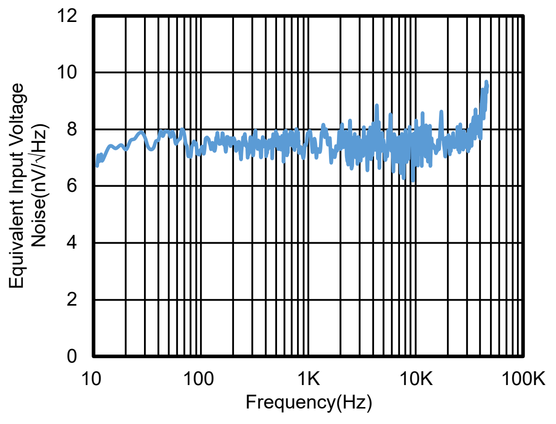


Figure 13. Voltage Noise Density vs. Frequency

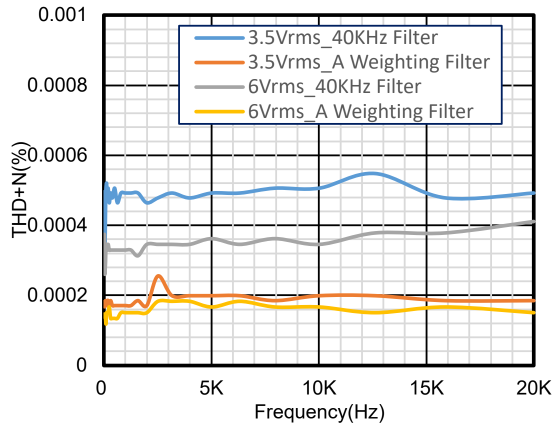


Figure 14. THD vs. Frequency, G = 1

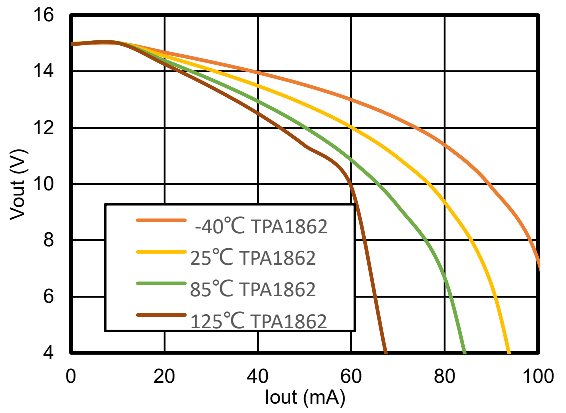


Figure 15. V_{OUT} vs. I_{OUT}, Source

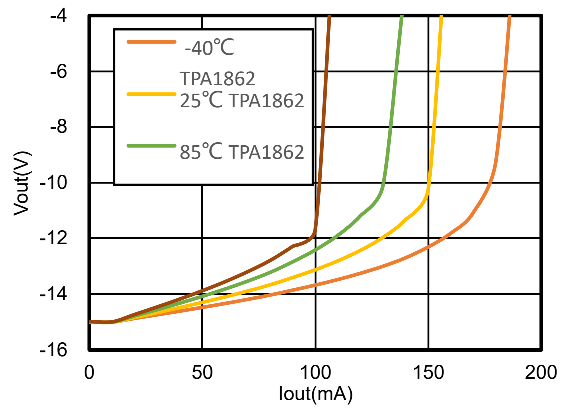


Figure 16. V_{OUT} vs. I_{OUT}, Sink

Detailed Description

Overview

The TPA186xQ series op amps can operate on a single-supply voltage (4.5 V to 36 V), or a split-supply voltage (±2.25 V to ±18 V), making them highly versatile and easy to use. The power-supply pins should have local bypass ceramic capacitors (typically 0.01 μF to 0.1 μF). Parameters that can exhibit variance with regard to operating voltage or temperature are presented in the Typical Performance Characteristics.

Functional Block Diagram

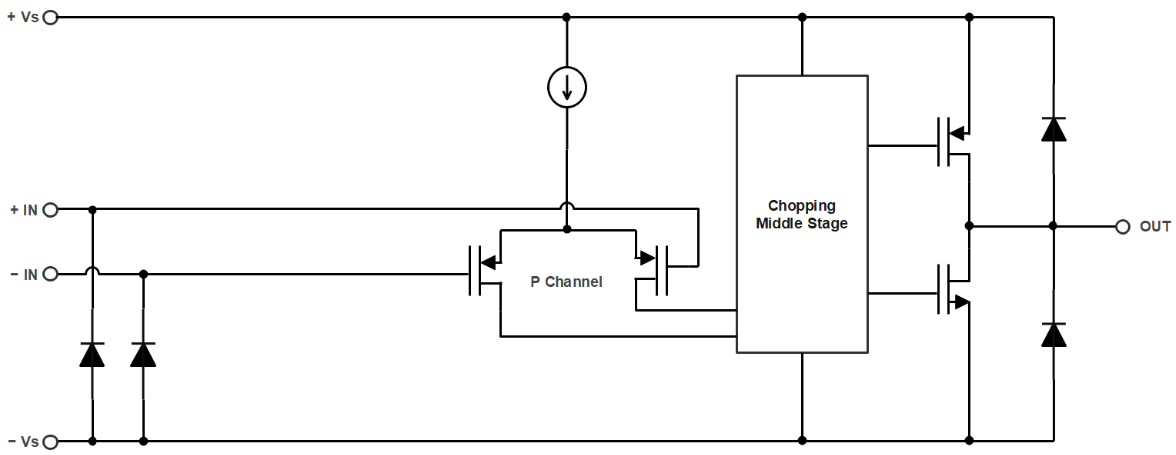


Figure 17. Functional Block Diagram

Feature Description

Operating Supply Voltage

The devices are designed for single supply operation from 4.5 V to 36 V or dual supply operation from ±2.25 V to ±18 V.

The recommended operating voltage conditions are as follows:

Power supply voltage ($+V_S$) - ($-V_S$): 4.5 V to 36 V. The power supply voltage can support the following three scenarios:

- Single supply
- Dual supplies with equal voltage values
- Various voltage configurations, as long as the voltage range of ($+V_S$) - ($-V_S$) is within 4.5 V to 36 V

For example, if operating with a single supply, ($-V_S$) = 0 V, then ($+V_S$) can support 4.5 V to 36 V. If using dual supplies with equal absolute values, the minimum voltage would be ±2.25 V and the maximum voltage would be ±18 V. It can even support other voltage configurations, such as ($-V_S$) = 100 V, ($+V_S$) = 136 V, or ($-V_S$) = -6 V, ($+V_S$) = 30 V, and so on.

Rail-to-Rail Output

The device delivers rail-to-rail output swing capability with a class-AB output stage. Different load conditions change the ability of the amplifier to swing close to the rails.

Residual Voltage Ripple

The chopping technique is used in amplifier design due to the internal notch filter. Although the chopping-related voltage ripple is suppressed, a higher noise spectrum exists at the chopping frequency and its harmonics due to residual ripple. To suppress the noise at the chopping frequency, it is recommended that a post filter be placed at the output of the amplifier.

The devices set the chopping frequency to 150 kHz. If the frequency of the input signal is close to the chopping frequency, the signal may be interfered with by the residue ripple.

Application and Implementation

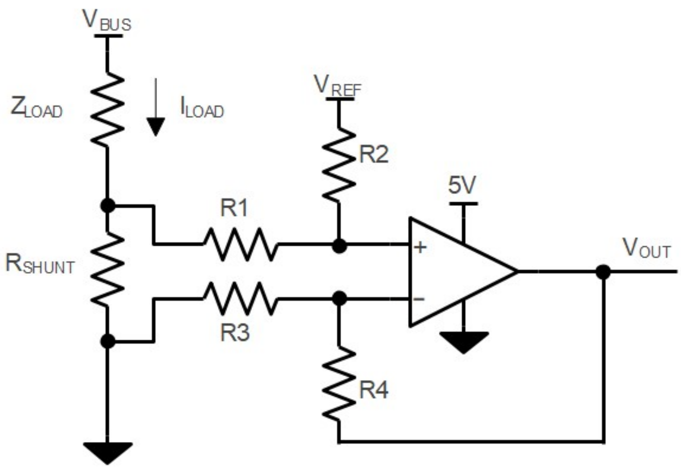
Note

Information in the following application sections is not part of the 3PEAK's component specification and 3PEAK does not warrant its accuracy or completeness. 3PEAK's customers are responsible for determining suitability of components for their purposes. Customers should validate and test their design implementation to confirm system functionality.

Application Information

Low Side Current Sensing Application

Figure 18 shows the device configured in a low-side current sensing application. The low-side current sensing method consists of placing a sense resistor between the load and the circuit ground. The voltage dropping across the resistor is amplified by different amplifier circuits with the device. The V_{REF} can be used to add bias voltage to the output voltage. Particular attention must be paid to the matching and precision of R1, R2, R3, and R4, to maximize the accuracy of the measurement.



$$V_{OUT} = (I_{LOAD} \times R_{SHUNT}) \times (R2 / R1) + V_{REF}$$

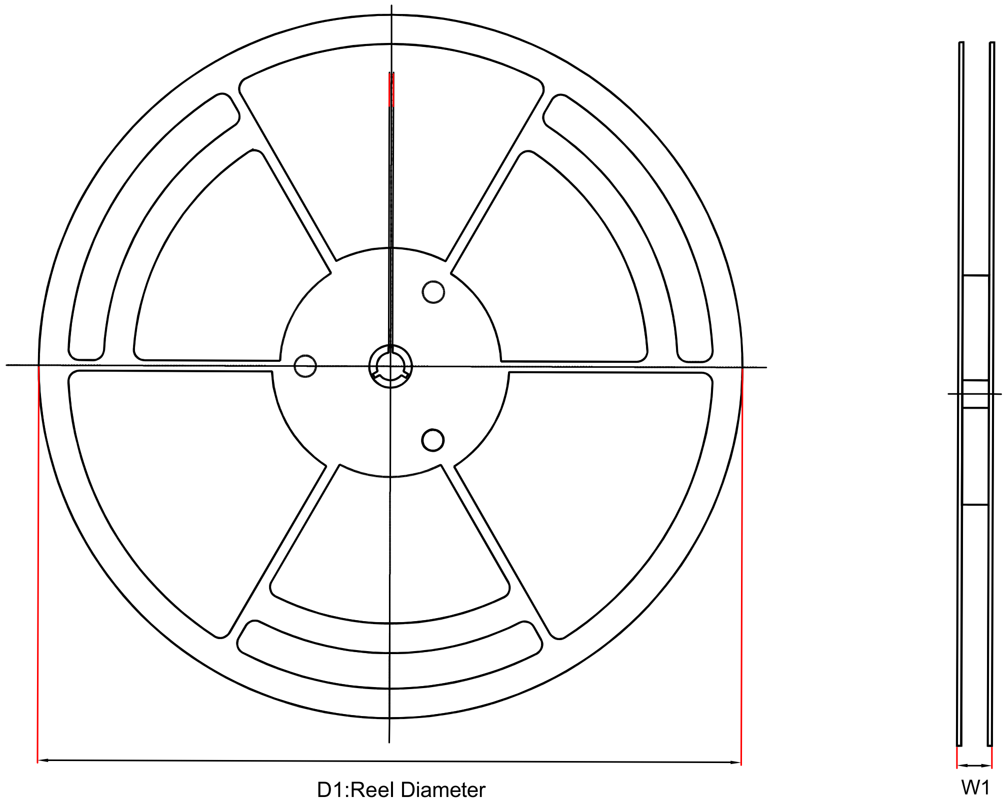
When $R3 = R1$, $R2 = R4$, $R_{SHUNT} << R1$

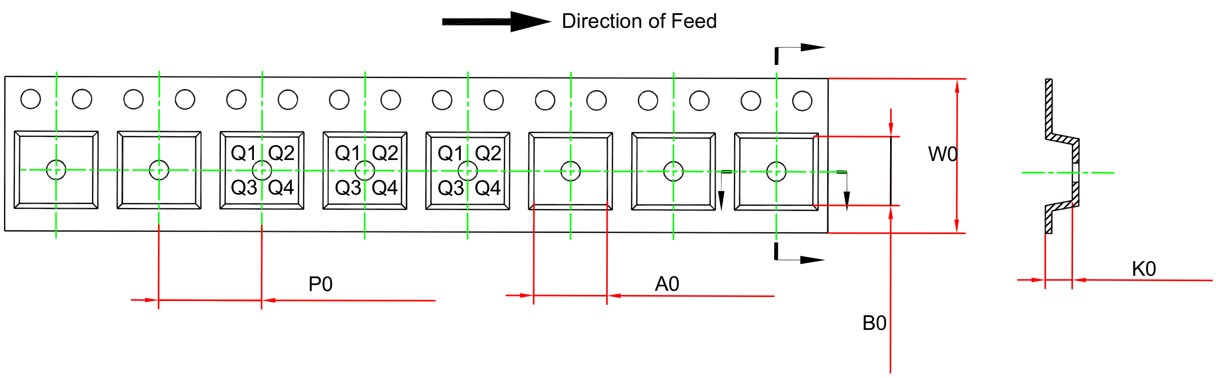
Figure 18. Low-Side Current Sensing Application

Power Supply Recommendations

Place 0.1-μF bypass capacitors close to the power supply pins to reduce coupling errors from the noisy or high-impedance power supplies.

Tape and Reel Information



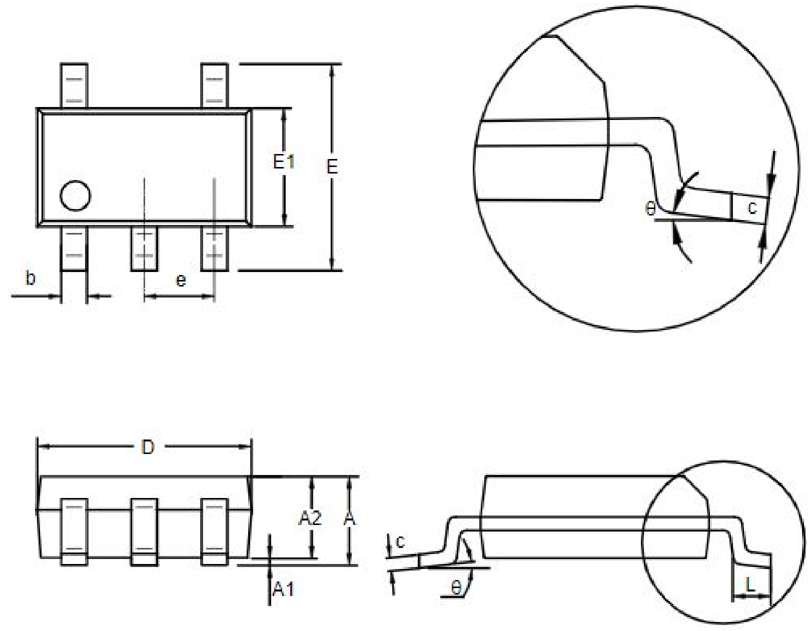


Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	W0 (mm)	Pin1 Quadrant
TPA1861Q-S5TR-S	SOT23-5	180.0	12	3.3	3.25	1.4	4.0	8.0	Q3
TPA1862Q-SO2R-S	SOP8	330.0	17.6	6.5	5.4	2	8.0	12.0	Q1

Package Outline Dimensions

SOT23-5

Package Outline Dimensions S5T(SOT23-5-A)



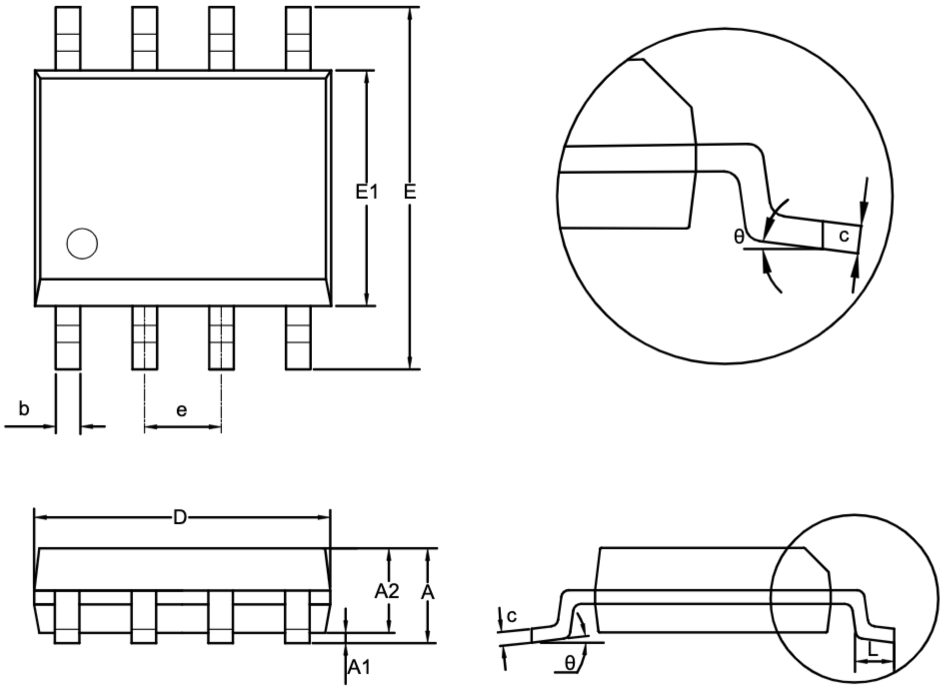
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.050	1.250	0.041	0.049
A1	0.000	0.150	0.000	0.006
A2	1.000	1.200	0.039	0.047
b	0.280	0.500	0.011	0.020
c	0.100	0.230	0.004	0.009
D	2.820	3.020	0.111	0.119
E	2.600	3.000	0.102	0.118
E1	1.500	1.720	0.059	0.068
e	0.950 BSC		0.037 BSC	
L	0.300	0.600	0.012	0.024
θ	0	8°	0	8°

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

SOP8

Package Outline Dimensions SO1(SOP-8-A)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	1.350	1.750	0.053	0.069
A1	0.050	0.250	0.002	0.010
A2	1.250	1.550	0.049	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
e	1.270 BSC		0.050 BSC	
L	0.400	1.000	0.016	0.039
θ	0	8°	0	8°

NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TPA1861Q-S5TR-S	−40 to 125°C	SOT23-5	A41	MSL1	Tape and Reel,3000	Green
TPA1862Q-SO1R-S	−40 to 125°C	SOP8	1862Q	MSL1	Tape and Reel,4000	Green

(1) For future products, contact the 3PEAK factory for more information and samples.

Green: 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

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Performance Information. Performance tests or performance range contained in this document/material are either results of design simulation or actual tests conducted under designated testing environment. Any variation in testing environment or simulation environment, including but not limited to testing method, testing process or testing temperature, may affect actual performance of the product.

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