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**36-V, 6-MHz, 20-V/ $\mu$ s Operational Amplifier**

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## Features

- Supply Voltage: 4.5 V to 36 V
- Rail-to-Rail Output
- Bandwidth: 6 MHz
- Slew Rate: 20 V/ $\mu$ s
- Excellent EMI Suppress Performance
- Offset Voltage:  $\pm 100 \mu$ V (Max)
- Offset Voltage Temperature Drift: 2  $\mu$ V/°C
- Low Noise: 25 nV/ $\sqrt{\text{Hz}}$  at 1 kHz
- Operating Temperature Range: -40°C to 125°C

## Applications

- Instrumentation
- Active Filters, ASIC Input or Output Amplifier
- Sensor Interface
- Motor Control
- Industrial Control

## Description

The TP17 is a series of the newest high supply voltage amplifiers with low offset, low power, and stable high-frequency response. The series incorporates 3PEAK's proprietary and patented design techniques to achieve excellent AC performance with 6-MHz bandwidth, 20-V/ $\mu$ s slew rate, and low distortion while drawing only 1500  $\mu$ A of quiescent current per amplifier. The input common-mode voltage range extends to -Vs, and the outputs swing rail-to-rail. The TP17 series can be used as plug-in replacements for commercially available op amps to reduce power consumption, extend the input/output range, and improve performance.

The combination of features makes the TP17 series an ideal choice for industrial control, instrumentation.

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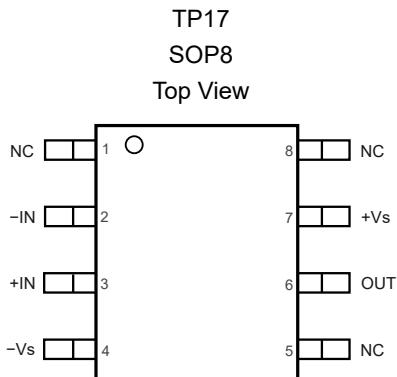
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**Revision History**

Date	Revision	Notes
2018-04-21	Rev.Pre.0	Pre-released version.
2018-10-05	Rev.A.0	Initial version.
2024-12-18	Rev.A.1	<p>The following updates are all about the new datasheet formats or typos, and the actual product remains unchanged.</p> <ul style="list-style-type: none"><li>• Updated to a new datasheet format.</li><li>• Updated the Package Outline Dimensions.</li><li>• Updated the Tape and Reel Information.</li></ul>

## Pin Configuration and Functions



**Table 1. Pin Functions: TP17**

Pin No.	Name	I/O	Description
1	NC		Not connect
2	-IN	I	Inverting input
3	+IN	I	Non-inverting input
4	-Vs		Negative power supply
5	NC		Not connect
6	OUT	O	Output
7	+Vs		Positive power supply
8	NC		Not connect

## Specifications

### Absolute Maximum Ratings (1)

Parameter		Min	Max	Unit
	Supply Voltage: (+Vs) – (-Vs)		40	V
	Input Voltage	(-Vs) – 0.3	(+Vs) + 0.3	V
	Differential Input Voltage		(+Vs) – (-Vs)	V
	Input Current: +IN, -IN (2)	-10	10	mA
	Output Short-Circuit Duration (3)		Infinite	
T <sub>J</sub>	Maximum Junction Temperature		150	°C
T <sub>A</sub>	Operating Temperature Range	-40	125	°C
T <sub>STG</sub>	Storage Temperature Range	-65	150	°C
T <sub>L</sub>	Lead Temperature (Soldering, 10 sec)		260	°C

(1) Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

(2) The inputs are protected by ESD protection diodes to each power supply. If the input extends more than 300 mV beyond the power supply, the input current should be limited to less than 10 mA.

(3) A heat sink may be required to keep the junction temperature below the absolute maximum. This depends on the power supply voltage and how many amplifiers are shorted. The thermal resistance varies with the amount of PC board metal connected to the package. The specified values are for short traces connected to the leads.

### ESD, Electrostatic Discharge Protection

Symbol	Parameter	Condition	Minimum Level	Unit
HBM	Human Body Model ESD	ANSI/ESDA/JEDEC JS-001 (1)	2	kV
CDM	Charged Device Model ESD	ANSI/ESDA/JEDEC JS-002 (2)	1	kV

(1) JEDEC document JEP155 states that 500-V HBM allows safe manufacturing with a standard ESD control process.

(2) JEDEC document JEP157 states that 250-V CDM allows safe manufacturing with a standard ESD control process.

### Thermal Information

Package Type	θ <sub>JA</sub>	θ <sub>Jc</sub>	Unit
SOP8	158	43	°C/W

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**Electrical Characteristics**

All test conditions:  $V_S = 30 \text{ V}$ ,  $T_A = 25^\circ\text{C}$ ,  $R_L = 10 \text{ k}\Omega$ , unless otherwise noted.

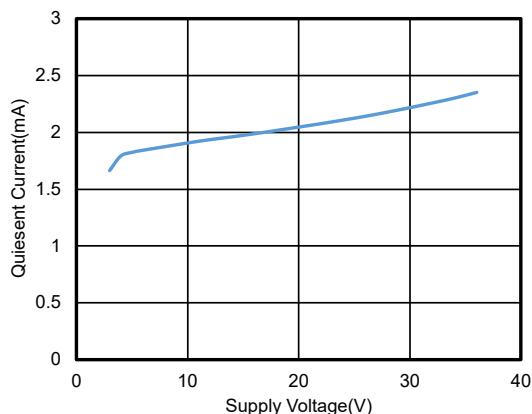
Symbol	Parameter	Conditions	$T_A$	Min	Typ	Max	Unit
<b>Power Supply</b>							
$V_S$	Supply Voltage Range			4.5		36	V
$I_Q$	Quiescent Current per Amplifier	$V_S = 30 \text{ V}$			1.5	2	mA
			$-40^\circ\text{C} \text{ to } 125^\circ\text{C}$			3	mA
$\text{PSRR}$	Power Supply Rejection Ratio	$V_S = 4.5 \text{ V} \text{ to } 36 \text{ V}$		105	130		dB
			$-40^\circ\text{C} \text{ to } 125^\circ\text{C}$	100			dB
<b>Input Characteristics</b>							
$V_{\text{os}}$	Input Offset Voltage	$V_S = 30 \text{ V}, V_{\text{CM}} = 15 \text{ V}$		-100	50	100	$\mu\text{V}$
			$-40^\circ\text{C} \text{ to } 85^\circ\text{C}$	-400		400	$\mu\text{V}$
			$-40^\circ\text{C} \text{ to } 125^\circ\text{C}$	-600		600	$\mu\text{V}$
		$V_S = 25 \text{ V}, V_{\text{CM}} = 12.5 \text{ V}$		-100	50	100	$\mu\text{V}$
			$-40^\circ\text{C} \text{ to } 85^\circ\text{C}$	-400		400	$\mu\text{V}$
			$-40^\circ\text{C} \text{ to } 125^\circ\text{C}$	-600		600	$\mu\text{V}$
$V_{\text{os TC}}$	Input Offset Voltage Drift	$V_S = 5 \text{ V}, V_{\text{CM}} = 2.5 \text{ V}$		-150	50	150	$\mu\text{V}$
			$-40^\circ\text{C} \text{ to } 85^\circ\text{C}$	-500		500	$\mu\text{V}$
			$-40^\circ\text{C} \text{ to } 125^\circ\text{C}$	-600		600	$\mu\text{V}$
		$V_S = 30 \text{ V}, V_{\text{CM}} = 15 \text{ V}$	$-40^\circ\text{C} \text{ to } 125^\circ\text{C}$		1		$\mu\text{V}/^\circ\text{C}$
					25		pA
			$-40^\circ\text{C} \text{ to } 85^\circ\text{C}$		80		pA
$I_{\text{os}}$	Input Offset Current		$-40^\circ\text{C} \text{ to } 125^\circ\text{C}$		1000		pA
					25		pA
$I_{\text{IN}}$	Different Input Current	$V_S = 36 \text{ V}, V_{\text{ID}} = 36 \text{ V}$			10	100	nA
			$-40^\circ\text{C} \text{ to } 125^\circ\text{C}$		100	300	nA
$C_{\text{IN}}$	Input Capacitance		Differential mode			5	pF
			Common mode			2.5	pF
$A_V$	Open-Loop Voltage Gain	$V_S = 30 \text{ V}, V_{\text{OUT}} = 0.5 \text{ V} \text{ to } 29.5 \text{ V}$		120	130		dB
			$-40^\circ\text{C} \text{ to } 125^\circ\text{C}$	105			dB
$V_{\text{CMR}}$	Common-Mode Input Voltage Range			( $-V_S$ )		$(+V_S) - 1.5$	V
$\text{CMRR}$	Common-Mode Rejection Ratio	$V_{\text{CM}} = 0.5 \text{ V} \text{ to } 28.5 \text{ V}$		100	125		dB
			$-40^\circ\text{C} \text{ to } 125^\circ\text{C}$	95			dB
<b>Output Characteristics</b>							
	Output Swing from Positive Rail	$R_{\text{LOAD}} = 100 \text{ k}\Omega \text{ to } V_S / 2$			5	15	mV
			$-40^\circ\text{C} \text{ to } 85^\circ\text{C}$			30	mV

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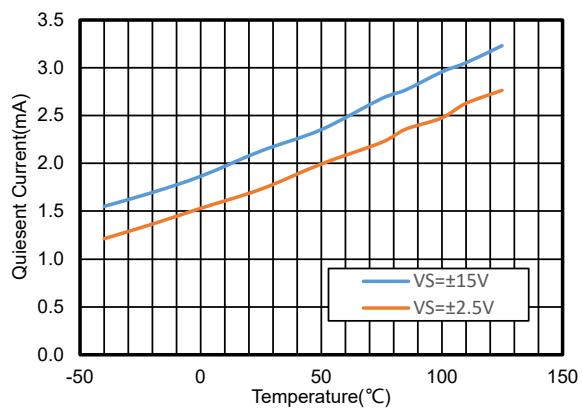
Symbol	Parameter	Conditions	T <sub>A</sub>	Min	Typ	Max	Unit
		R <sub>LOAD</sub> = 10 kΩ to V <sub>s</sub> / 2	−40°C to 125°C			40	mV
					50	80	mV
			−40°C to 85°C			120	mV
			−40°C to 125°C			130	mV
	Output Swing from Negative Rail	R <sub>LOAD</sub> = 100 kΩ to V <sub>s</sub> / 2			5	10	mV
			−40°C to 85°C			20	mV
			−40°C to 125°C			25	mV
		R <sub>LOAD</sub> = 10 kΩ to V <sub>s</sub> / 2			40	50	mV
			−40°C to 85°C			80	mV
			−40°C to 125°C			100	mV
I <sub>SC</sub>	Output Short-Circuit Current	Source current		20	32		mA
		Sink current		15	25		mA
<b>AC Specifications</b>							
GBW	Gain-Bandwidth Product				6		MHz
SR	Slew Rate	G = 1, 10-V step		13	20		V/μs
			−40°C to 125°C	10			V/μs
t <sub>OR</sub>	Overload Recovery				100		ns
t <sub>s</sub>	Settling Time, 0.1%	G = 1, 10-V step			0.5		μs
	Settling Time, 0.01%				0.8		μs
PM	Phase Margin	V <sub>s</sub> = 36 V, R <sub>L</sub> = 10 kΩ, C <sub>L</sub> = 100 pF			60		°
GM	Gain Margin	V <sub>s</sub> = 36 V, R <sub>L</sub> = 10 kΩ, C <sub>L</sub> = 100 pF			10		dB
<b>Noise Performance</b>							
E <sub>N</sub>	Input Voltage Noise	f = 0.1 Hz to 10 Hz			2		μV <sub>RMS</sub>
e <sub>N</sub>	Input Voltage Noise Density	f = 1 kHz			25		nV/√Hz
i <sub>N</sub>	Input Current Noise	f = 1 kHz			2		fA/√Hz
THD+N	Total Harmonic Distortion and Noise	f = 1 kHz, G = 1, R <sub>L</sub> = 10 kΩ, V <sub>OUT</sub> = 6 V <sub>RMS</sub>			0.0005		%
<b>Thermal Shutdown</b>							
	Thermal Shutdown Temperature				170		°C
	Recover Temperature				150		°C

**36-V, 6-MHz, 20-V/ $\mu$ s Operational Amplifier**
**Typical Performance Characteristics**

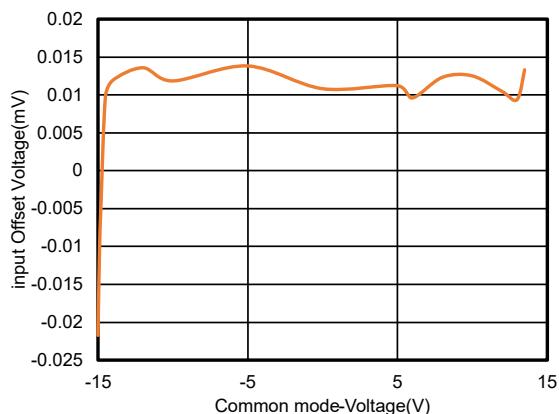
All test conditions:  $V_S = \pm 15$  V,  $V_{CM} = 0$  V,  $R_L = 10$  k $\Omega$ , unless otherwise noted.



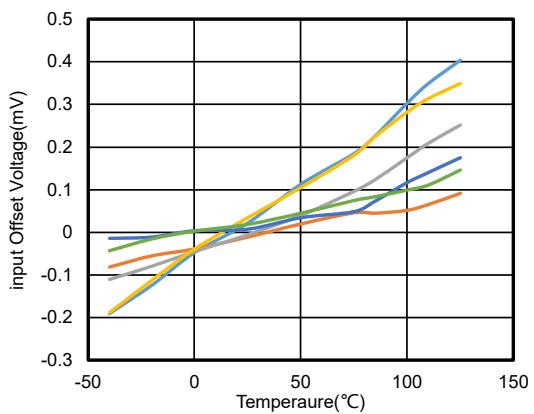
**Figure 1. Quiescent Current vs. Supply Voltage**



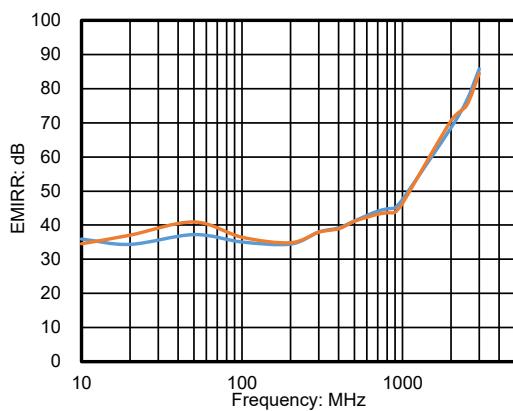
**Figure 2. Quiescent Current vs. Temperature**



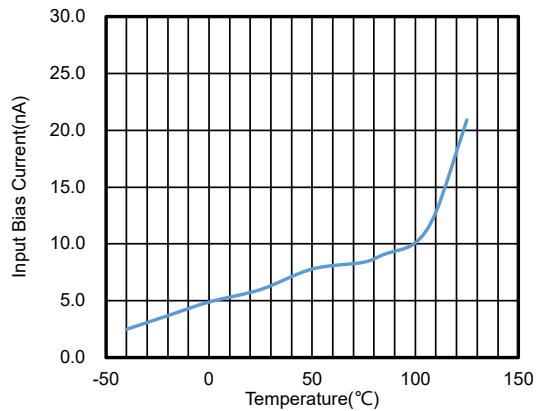
**Figure 3. Offset Voltage vs. Common-Mode Voltage**



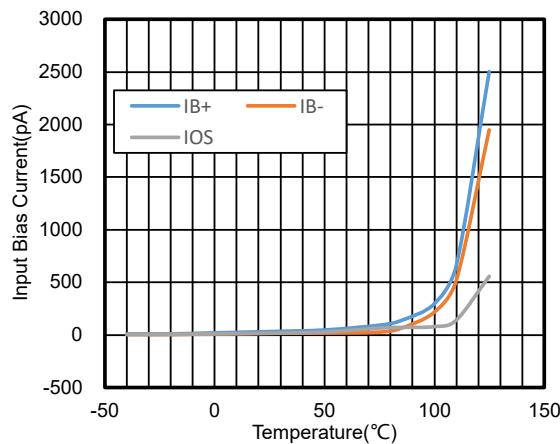
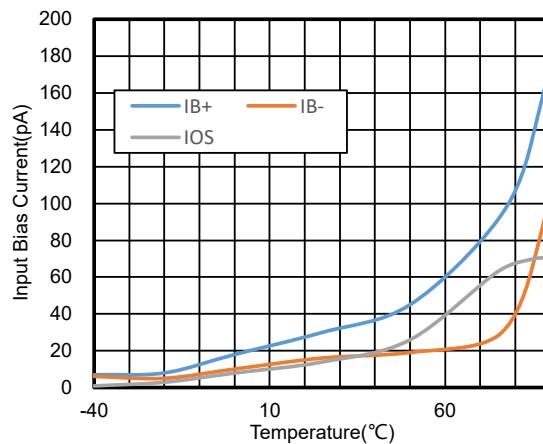
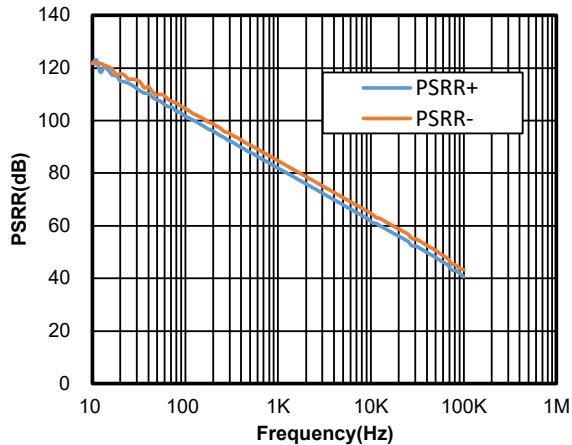
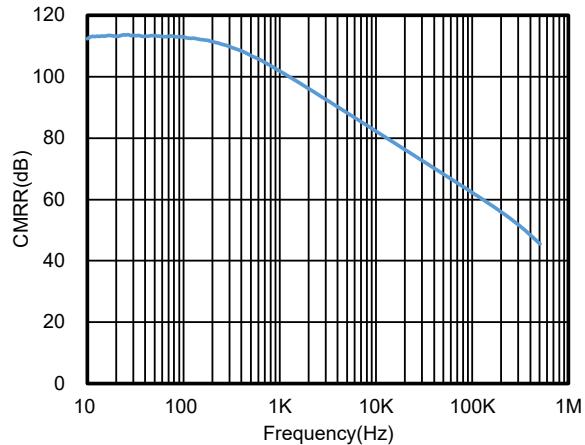
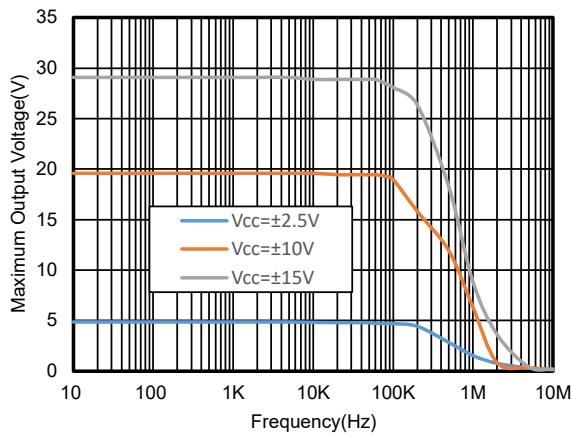
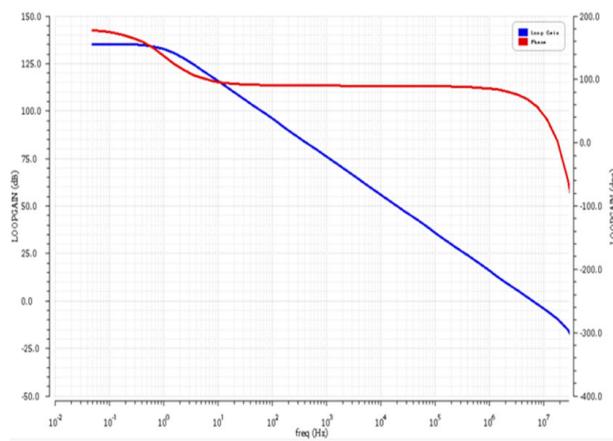
**Figure 4.  $V_{OS}$  vs. Temperature**



**Figure 5. EMIRR vs. Frequency**



**Figure 6. Input Current in Large  $V_{DM}$  vs. Temperature**

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**Figure 7.  $I_B$  vs. Temperature,  $-40^{\circ}\text{C}$  to  $125^{\circ}\text{C}$** 

**Figure 8.  $I_B$  vs. Temperature,  $-40^{\circ}\text{C}$  to  $90^{\circ}\text{C}$** 

**Figure 9. PSRR vs. Frequency**

**Figure 10. CMRR vs. Frequency**

**Figure 11. Maximum Output Voltage vs. Frequency**

**Figure 12. Open-Loop Gain and Phase vs. Frequency,  
 $R_{LOAD} = 10 \text{ k}\Omega$ ,  $C_{LOAD} = 100 \text{ pF}$**

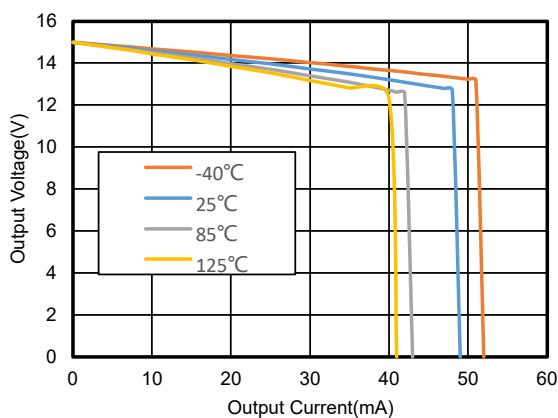
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Figure 13. Positive Output Voltage vs. Output Current

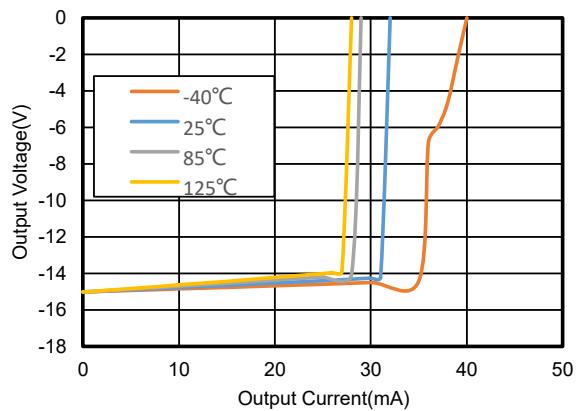


Figure 14. Negative Output Voltage vs. Output Current



Voltage: 5 V/div for Output, Time: 500 ns/div

 $G = -10$ ,  $V_{REF} = GND$ ;  $V_{IN} = 5 \text{ V}_{PP}$ ,  $R_{Load} = 2 \text{ k}\Omega$ ,  $C = 100 \text{ pF}$ 

Figure 15. Positive Overload Recovery



Voltage: 5 V/div for Output, Time: 500 ns/div

 $G = -10$ ,  $V_{REF} = GND$ ;  $V_{IN} = 5 \text{ V}_{PP}$ ,  $R_{Load} = 2 \text{ k}\Omega$ ,  $C = 100 \text{ pF}$ 

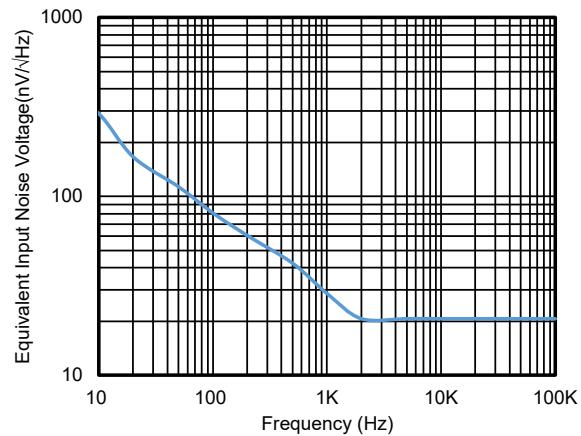
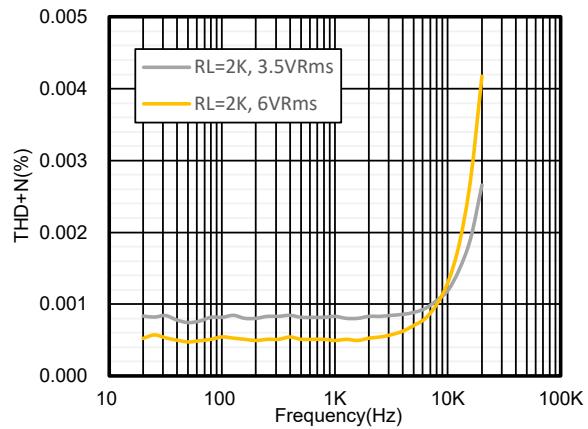
Figure 16. Negative Overload Recovery

Voltage: 50 mV/div, Time: 1  $\mu$ s/div $R_L = 2 \text{ k}\Omega$ ,  $C_L = 100 \text{ pF}$ ,  $G = 1$ 

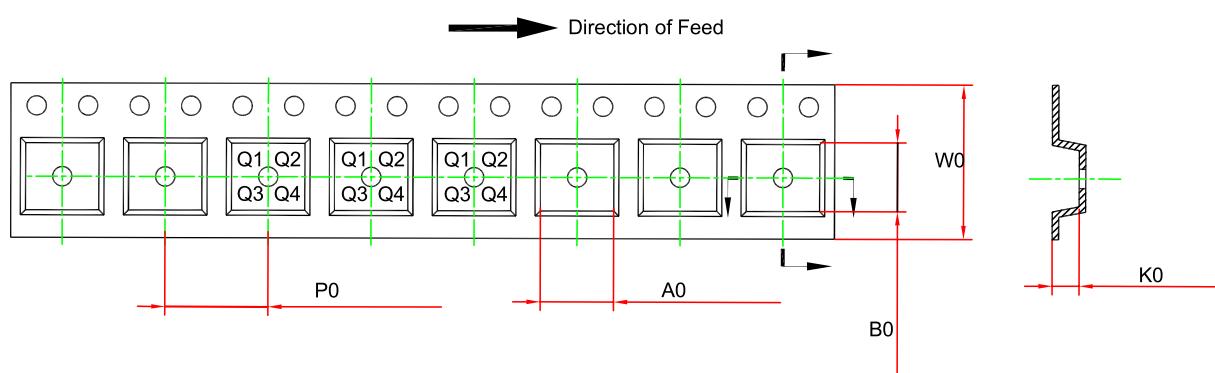
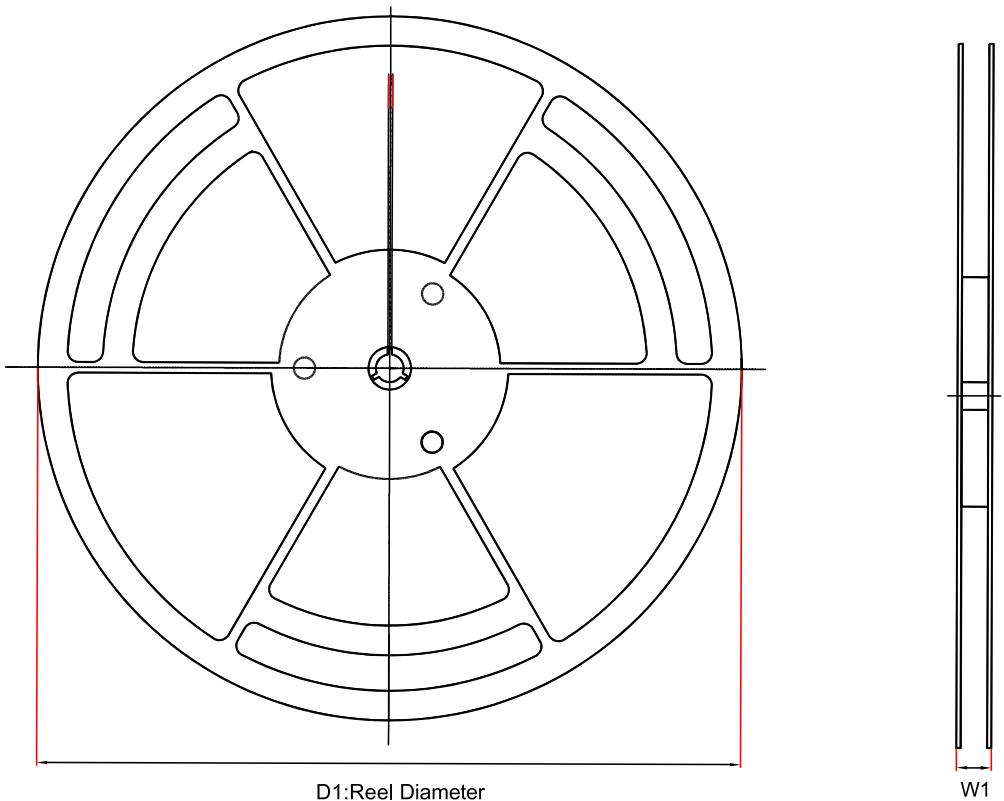
Figure 17. 100-mV Signal Step Response

Voltage: 5 V/div, Time: 1  $\mu$ s/div $R_L = 2 \text{ k}\Omega$ ,  $C_L = 100 \text{ pF}$ ,  $G = 1$ 

Figure 18. 10-V Signal Step Response

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 $V_S = \pm 15 \text{ V}, V_{CM} = 0 \text{ V}$ 
**Figure 19. Voltage Noise Spectral Density vs. Frequency**

 $V_S = \pm 15 \text{ V}, V_{CM} = 0 \text{ V}, G = 1$ 
**Figure 20. THD+N vs. Frequency**

### Tape and Reel Information



Order Number	Package	D1 (mm)	W1 (mm)	A0 (mm) <sup>(1)</sup>	B0 (mm) <sup>(1)</sup>	K0 (mm) <sup>(1)</sup>	P0 (mm)	W0 (mm)	Pin1 Quadrant
TP17-SR	SOP8	330	17.6	6.5	5.4	2	8	12	Q1

(1) The value is for reference only. Contact the 3PEAK factory for more information.

## Package Outline Dimensions

**SOP8**

Package Outline Dimensions		SO1(SOP-8-A)			
Symbol	Dimensions In Millimeters		Dimensions In Inches		
	MIN	MAX	MIN	MAX	
A	1.350	1.750	0.053	0.069	
A1	0.050	0.250	0.002	0.010	
A2	1.250	1.550	0.049	0.061	
b	0.330	0.510	0.013	0.020	
c	0.170	0.250	0.007	0.010	
D	4.700	5.100	0.185	0.201	
E	5.800	6.200	0.228	0.244	
E1	3.800	4.000	0.150	0.157	
e	1.270 BSC		0.050 BSC		
L	0.400	1.000	0.016	0.039	
θ	0	8°	0	8°	

**NOTES**

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

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**36-V, 6-MHz, 20-V/μs Operational Amplifier****Order Information**

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity	Eco Plan
TP17-SR	-40 to 125°C	SOP8	TP17 XXXX <sup>(1)</sup>	3	Tape and Reel, 4000	Green

(1) "XXXX" identifies the manufacturing information.

**Green:** 3PEAK defines "Green" to mean RoHS compatible and free of halogen substances.

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