

Features

- Supply Voltage: 3V to 36V or $\pm 1.5V$ to $\pm 18V$
- Low Supply Current: 100 μA per channel
- Input Common-Mode Voltage Range Includes Ground
- Can Work as Comparator
- Rail to Rail Output
- Bandwidth: 0.9 MHz
- Slew Rate: 0.5V/ μs
- Excellent EMI Suppress Performance: 71dB at 1GHz
- Offset Voltage: $\pm 3mV$ Maximum
- Offset Voltage Temperature Drift: 7 $\mu V/^{\circ}C$
- $-40^{\circ}C$ to $125^{\circ}C$ Operation Temperature Range

Applications

- Power Module
- Sensor Interface
- Motor Control
- Audio

Description

The LM2904A/2902A series amplifiers are newest high supply voltage amplifiers with low offset, low power and stable frequency response. They incorporate 3PEAK's proprietary and patented design techniques to achieve very good AC performance with 0.9MHz bandwidth, 0.5V/ μs slew rate and maximum 3mV offset while drawing only 100 μA of quiescent current per amplifier. The input common-mode voltage range extends to V_{-} , and the outputs swing rail-to-rail. The LM2904A/2902A family can be used as plug-in replacements for many commercially available op-amps to reduce power and improve input/output range and performance.

The combination of features makes the LM2904A/2902A ideal choices for power module, industrial control, motor control and audio application.

Pin Configuration

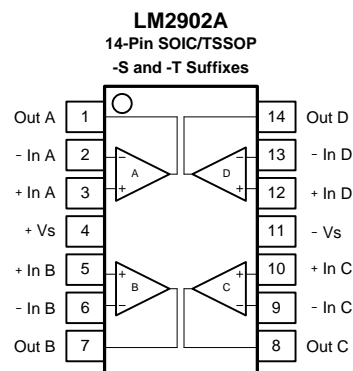
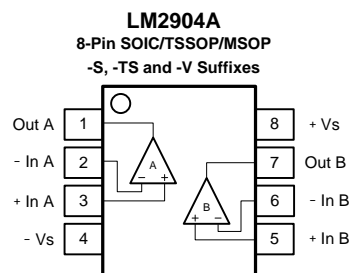


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Revision History

Date	Revision	Notes
2018/3/21	Rev.Pre	Pre-Release Version
2018/10/30	Rev.0	Initial Version
2018/11/11	Rev.0.01	Add LM2902AL1-SR, Correct the condition of VOL,VOH to VS/2, Add mark information.
2019/1/9	Rev.0.02	Update Package Outline Dimension
2019/6/26	Rev.0.03	Add temperature limit of VOL, VOH, Isc Update Package Dimension
2019/8/16	Rev.0.04	Update the description of VOL, VOH in Electrical Characteristics: Remove VOL, VOH, Maximum Output Swing -> Output Voltage Swing from Positive and Negative Rail; Output Voltage Swing Low-> Output Voltage Swing from Negative Rail
2020/12/11	Rev.A.0	Add Maximum Value of I_B , I_{IN} in Electrical Characteristics
2022/4/29	Rev.A.1	Update EC table: Add max of I_{OS} , Add common voltage range in 25 degree. Update order information
2023/11/10	Rev.A.2	The following updates are all about the new datasheet formats or typo, the actual product remains unchanged. Updated to new format of package dimensions. Updated tape and reel information. Updated EC table: Modified V_{OS} in -40 to 125°C: change Min -7 to Min -6; change Max 7 to Max 6. Added V_{OS} in -40 to 85°C.

Order Information

Order Number	Operating Temperature Range	Package	Marking Information	MSL	Transport Media, Quantity
LM2904A-SR	-40 to 125°C	SOP8	2904A	3	Tape and Reel, 4000
LM2904A-TSR	-40 to 125°C	TSSOP8	2904A	3	Tape and Reel, 3000
LM2904A-VR	-40 to 125°C	MSOP8	2904A	3	Tape and Reel, 3000
LM2902A-SR	-40 to 125°C	SOP14	2902A	3	Tape and Reel, 2500
LM2902A-TR	-40 to 125°C	TSSOP14	2902A	3	Tape and Reel, 3000

Absolute Maximum Ratings ^{Note 1}

Parameters	Rating
Supply Voltage, (+V _S)– (-V _S)	40 V
Input Voltage	(-V _S) – 0.3 to (+V _S) + 0.3
Differential Input Voltage	(+V _S) - (-V _S)
Input Current: +IN, –IN ^{Note 2}	±10mA
Output Short-Circuit Duration ^{Note 3}	Infinite
Maximum Junction Temperature	150°C
Operating Temperature Range	–40 to 125°C
Storage Temperature Range	–65 to 150°C
Lead Temperature (Soldering, 10 sec)	260°C

Note 1: Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

Note 2: The inputs are protected by ESD protection diodes to each power supply. If the input extends more than 300mV beyond the power supply, the input current should be limited to less than 10mA.

Note 3: A heat sink may be required to keep the junction temperature below the absolute maximum. This depends on the power supply voltage and how many amplifiers are shorted. Thermal resistance varies with the amount of PC board metal connected to the package. The specified values are for short traces connected to the leads.

ESD Rating

Symbol	Parameter	Condition	Minimum Level	Unit
HBM	Human Body Model ESD	ANSI/ESDA/JEDEC JS-001	3	kV
CDM	Charged Device Model ESD	ANSI/ESDA/JEDEC JS-002	2	kV

Thermal Information

Package Type	θ_{JA}	θ_{JC}	Unit
8-Pin SOIC	158	43	°C/W
8-Pin TSSOP	191	44	°C/W
8-Pin MSOP	210	45	°C/W
14-Pin SOIC	120	36	°C/W
14-Pin TSSOP	180	35	°C/W

Electrical Characteristics

All test condition is $V_S = 30V$, $T_A = 25^\circ C$, $R_L = 10k\Omega$, $C_L = 100pF$, unless otherwise noted.

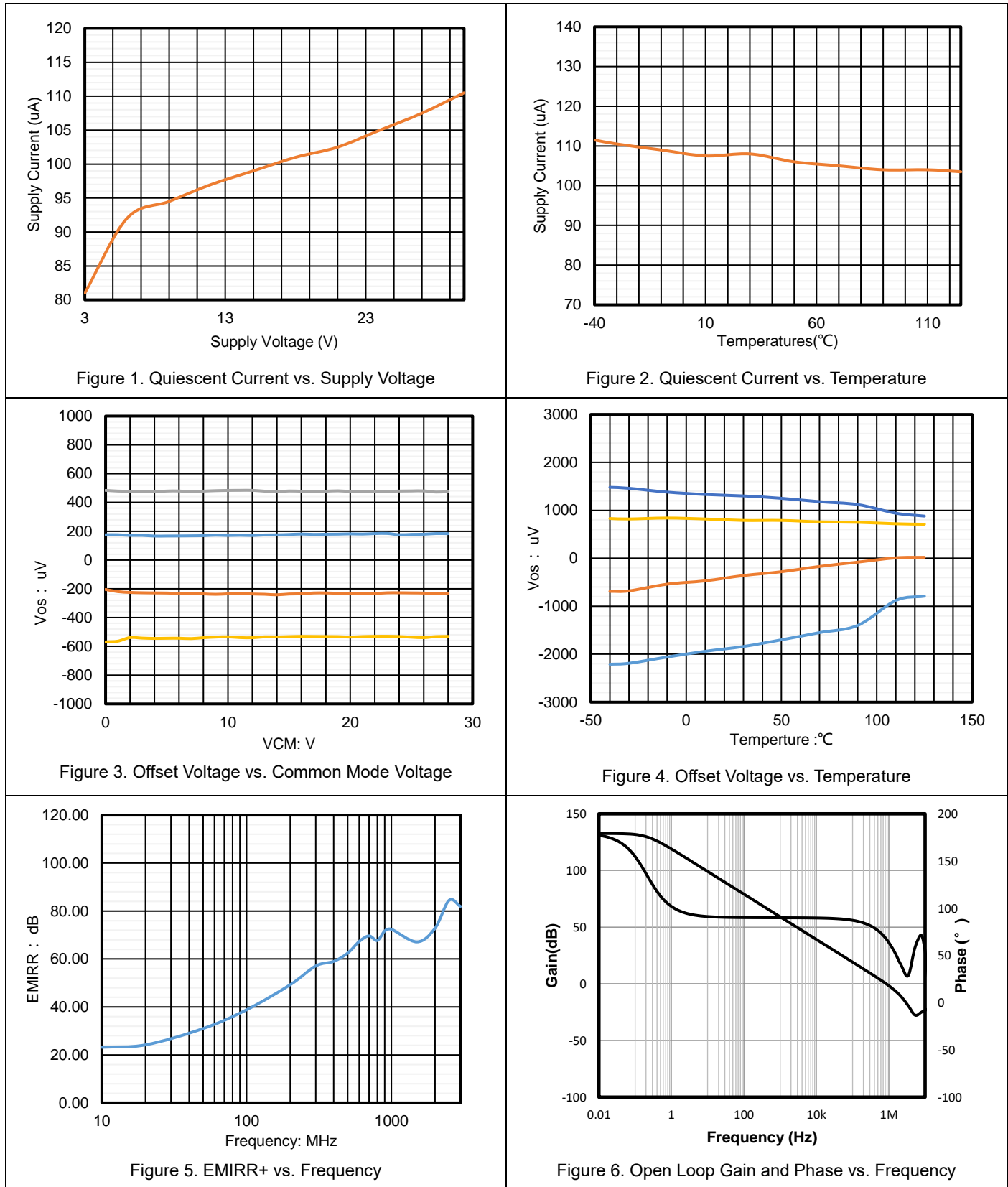
Symbol	Parameter	Conditions	T_A	Min	Typ	Max	Unit
Power Supply							
V_S	Supply Voltage Range	$(+V_S) - (-V_S)$		3		36	V
I_Q	Quiescent Current per Amplifier	$V_S = 30V$			110	200	μA
			-40 to 125°C			250	μA
		$V_S = 5V$			100	150	μA
			-40 to 125°C			200	μA
PSRR	Power Supply Rejection Ratio	$V_S = 5V$ to 36V		85	120		dB
			-40 to 125°C	80			dB
Input Characteristics							
V_{OS}	Input Offset Voltage	$V_S = 30V$, $V_{CM} = 0V$ to 28V		-3	0.1	3	mV
			-40 to 85°C	-5		5	mV
			-40 to 125°C	-6		6	mV
		$V_S = 5V$, $V_{CM} = 0V$ to 3V		-3	0.1	3	mV
			-40 to 85°C	-5		5	mV
			-40 to 125°C	-6		6	mV
$V_{OS\ TC}$	Input Offset Voltage Drift		-40 to 125°C		7		$\mu V/^\circ C$
I_B	Input Bias Current				60	500	pA
		-40 to 125°C			600	1000	pA
I_{OS}	Input Offset Current ^{Note 1}				60	500	pA
		-40 to 125°C			600	1000	pA
I_{IN}	Different Input Current	$V_S = 36V$, $V_{ID} = 36V$			5	150	nA
		$V_S = 36V$, $V_{ID} = 36V$	-40 to 125°C		20	1000	nA
C_{IN}	Input Capacitance	Differential Mode			5		pF
		Common Mode			5		pF
A_v	Open-loop Voltage Gain			95	110		dB
		-40 to 125°C		90			dB
V_{CMR}	Common-mode Input Voltage Range			(V-)		(V+) - 1.5	V
		-40 to 125°C		(V-)		(V+) - 2	V
CMRR	Common Mode Rejection Ratio	$V_{CM} = 0V$ to 28V		75	120		dB
			-40 to 125°C	70	90		dB
Output Characteristics							
	Output Voltage Swing from Positive and Negative Rail	$R_{LOAD} = 10k\Omega$ to $V_S/2$			200	300	mV
			-40 to 125°C			700	mV
		$R_{LOAD} = 2k\Omega$ to $V_S/2$			1.1	1.3	V
			-40 to 125°C			2	V

	Output Voltage Swing from Negative Rail	$V_S = 5V, R_{LOAD} = 10k\Omega$ to 0V			5	10	mV
I_{SC}	Output Short-Circuit Current			25	30		mA
			-40 to 125°C	15			mA
AC Specifications							
GBW	Gain-Bandwidth Product				0.9		MHz
SR	Slew Rate	$G = 1, 2V$ step			0.5		V/ μ s
t_s	Settling Time, 0.1%	$G = 1, 2V$ step			4		μ s
	Settling Time, 0.01%				5		μ s
PM	Phase Margin	$V_S = 30V, R_L = 1K, C_L = 100pF$			60		°
GM	Gain Margin	$V_S = 30V, R_L = 1K, C_L = 100pF$			15		dB
	Channel Separation	$f = 1$ kHz to 20 kHz			120		dB
Noise Performance							
E_N	Input Voltage Noise	$f = 0.1Hz$ to 10Hz			3		μV_{RMS}
e_N	Input Voltage Noise Density	$f = 1kHz$			70		nV/ \sqrt{Hz}
i_N	Input Current Noise	$f = 1kHz$			3		fA/ \sqrt{Hz}
THD+N	Total Harmonic Distortion and Noise	$f = 1kHz, G = 1, R_L = 10k\Omega,$ $V_{OUT} = 6V_{RMS}$			0.001		%

Note 1: Provided by bench test and design simulation

Typical Performance Characteristics

V_S = ±15V, V_{CM} = 0V, R_L = 10kΩ, unless otherwise specified.



$V_s = \pm 15V$, $V_{CM} = 0V$, $R_L = 10k\Omega$, unless otherwise specified.

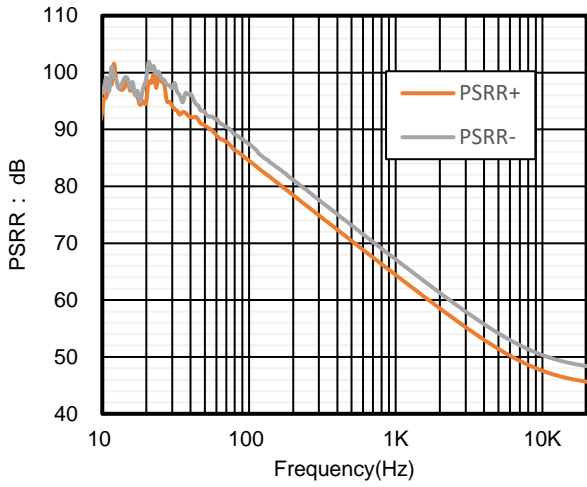


Figure 7. PSRR vs. Frequency

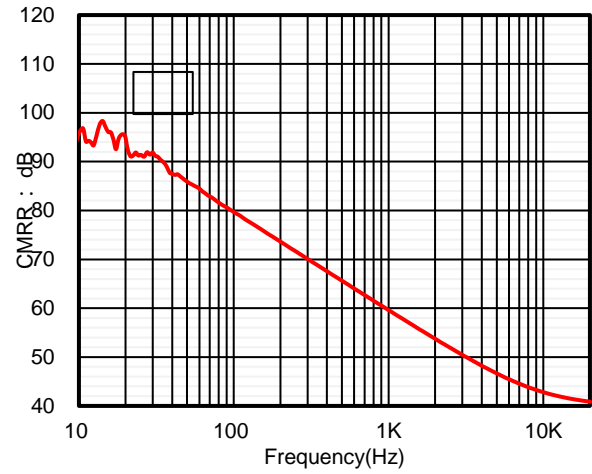


Figure 8. CMRR vs. Frequency

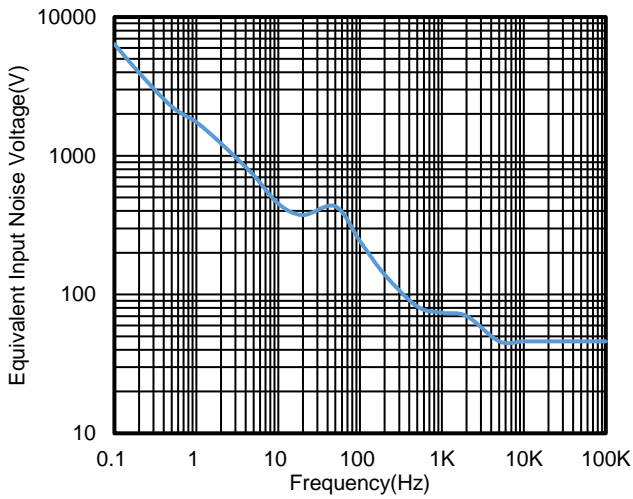


Figure 9. Voltage Noise Spectral Density vs. Frequency

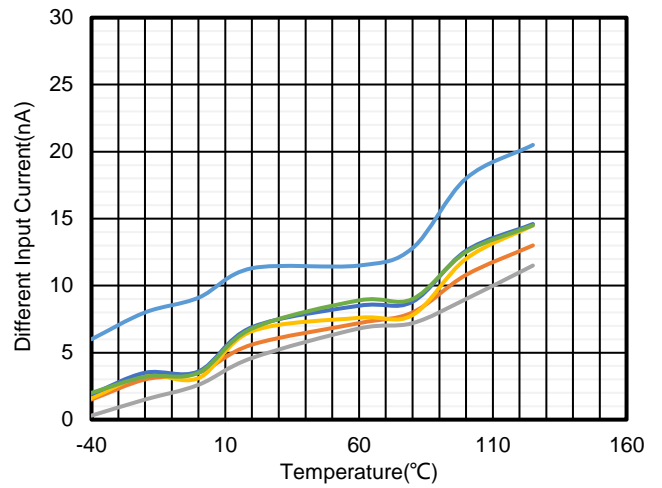
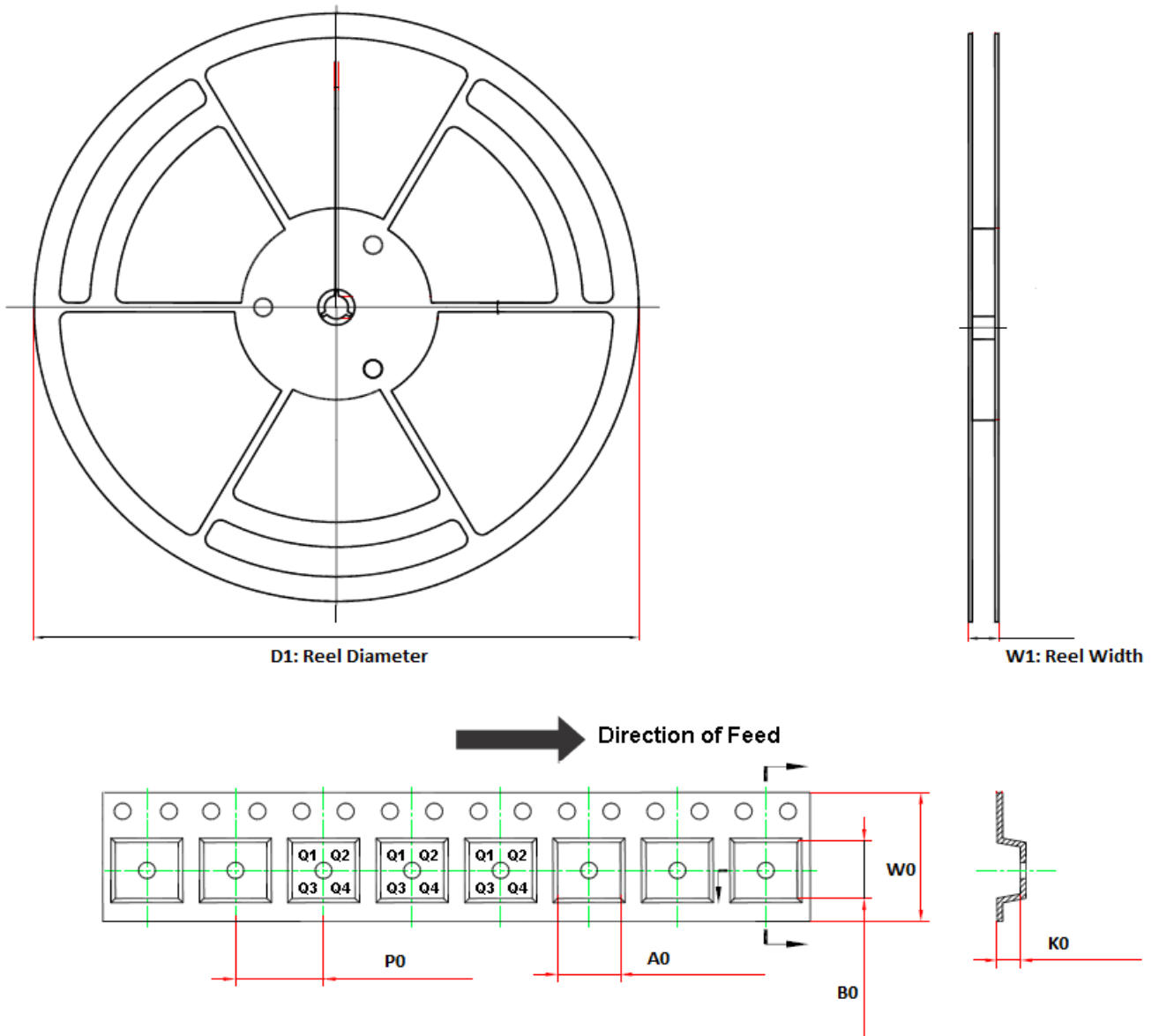


Figure 10. Different Input Current vs. Temperature

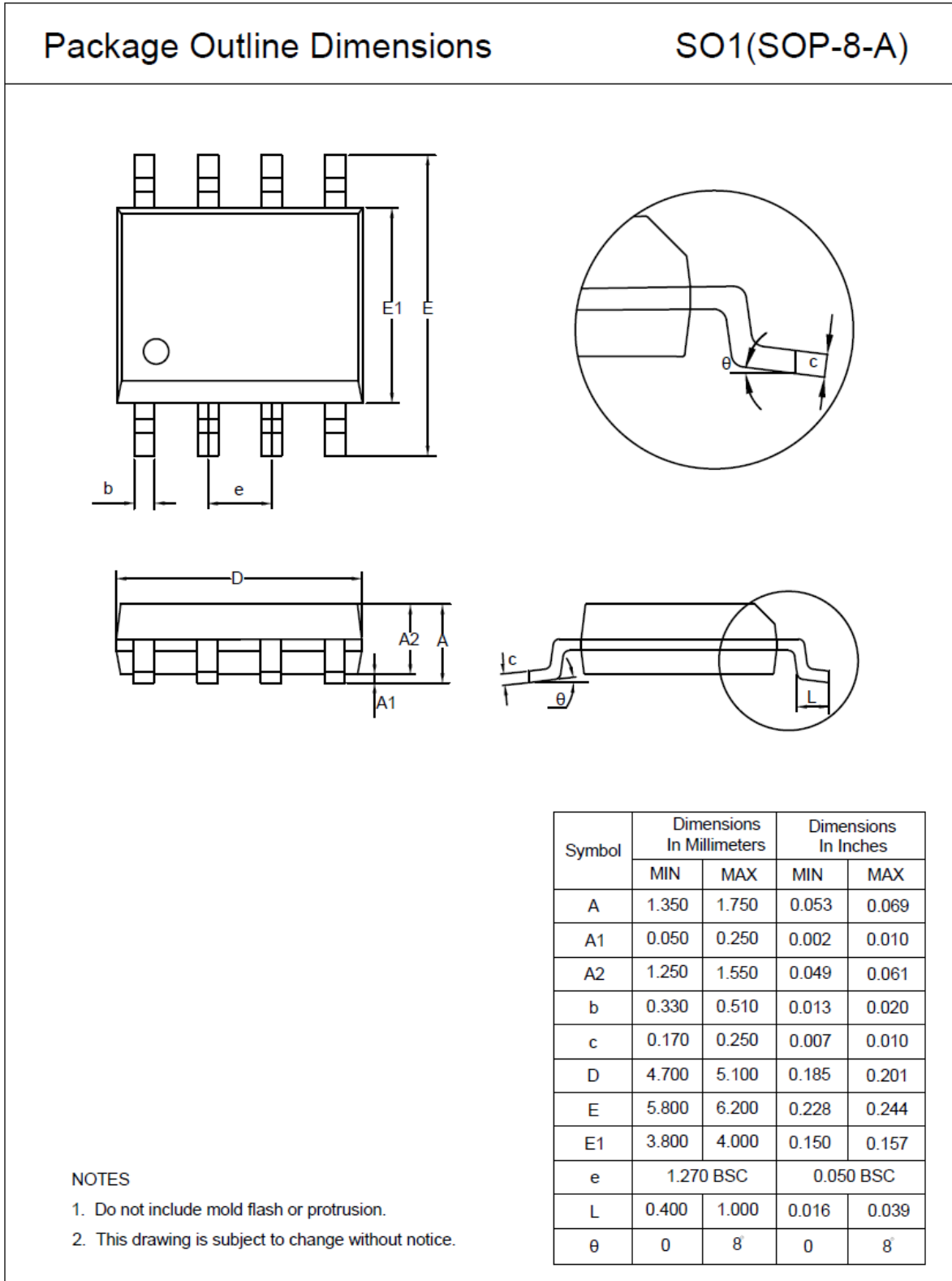
Tape and Reel Information



Order Number	Package	D1	W1	A0	B0	K0	P0	W0	Pin1 Quadrant
LM2904A-SR	SOP8	330.0	17.6	6.5	5.4	2.0	8.0	12.0	Q1
LM2904AL1-SR	SOP8	330.0	17.6	6.5	5.4	2.0	8.0	12.0	Q1
LM2904A-VR	MSOP8	330.0	17.6	5.3	3.3	1.4	8.0	12.0	Q1
LM2904A-TSR	TSSOP8	330.0	17.6	6.8	3.4	1.7	8.0	12.0	Q1
LM2902A-SR	SOP14	330.0	21.6	6.5	9.15	1.8	8.0	16.0	Q1
LM2902A-TR	TSSOP14	330.0	17.6	6.8	5.5	1.7	8.0	12.0	Q1

Package Outline Dimensions

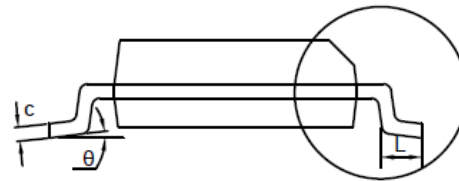
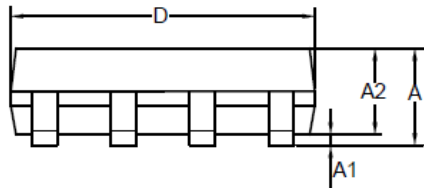
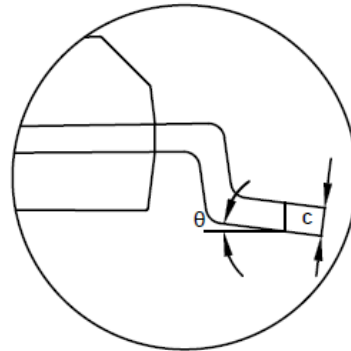
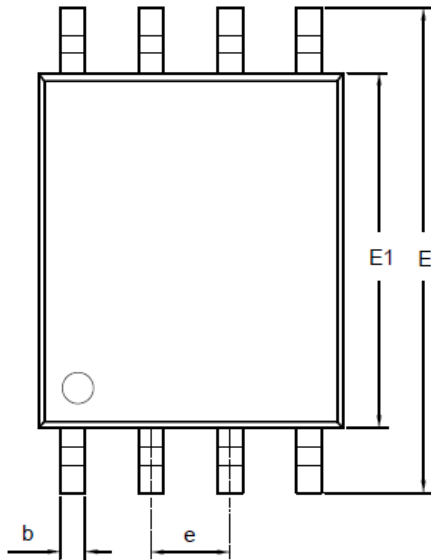
SOP8



TSSOP8

Package Outline Dimensions

TS1(TSSOP-8-A)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.900	1.200	0.035	0.047
A1	0.050	0.150	0.002	0.006
A2	0.800	1.050	0.031	0.041
b	0.190	0.300	0.007	0.012
c	0.090	0.200	0.004	0.008
D	2.900	3.100	0.114	0.122
E	6.200	6.600	0.244	0.260
E1	4.300	4.500	0.169	0.177
e	0.650 BSC		0.026 BSC	
L	0.450	0.750	0.018	0.030
θ	0	8°	0	8°

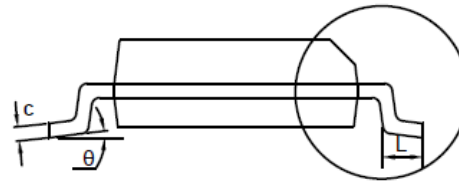
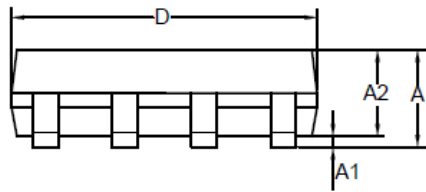
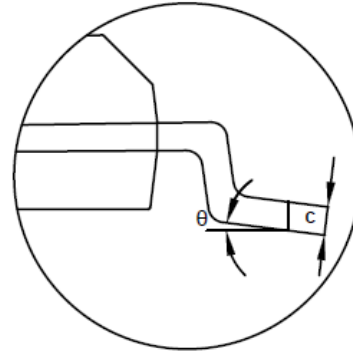
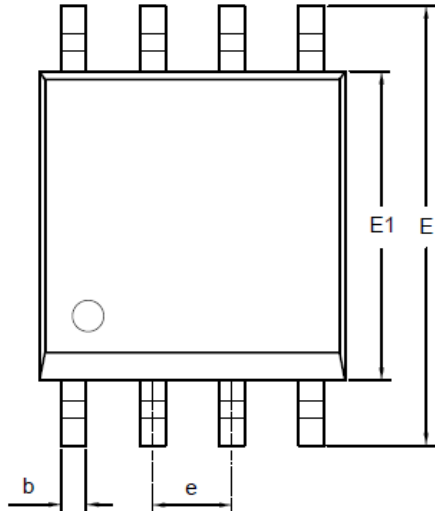
NOTES

1. Do not include mold flash or protrusion.
2. This drawing is subject to change without notice.

MSOP8

Package Outline Dimensions

VS1(MSOP-8-A)

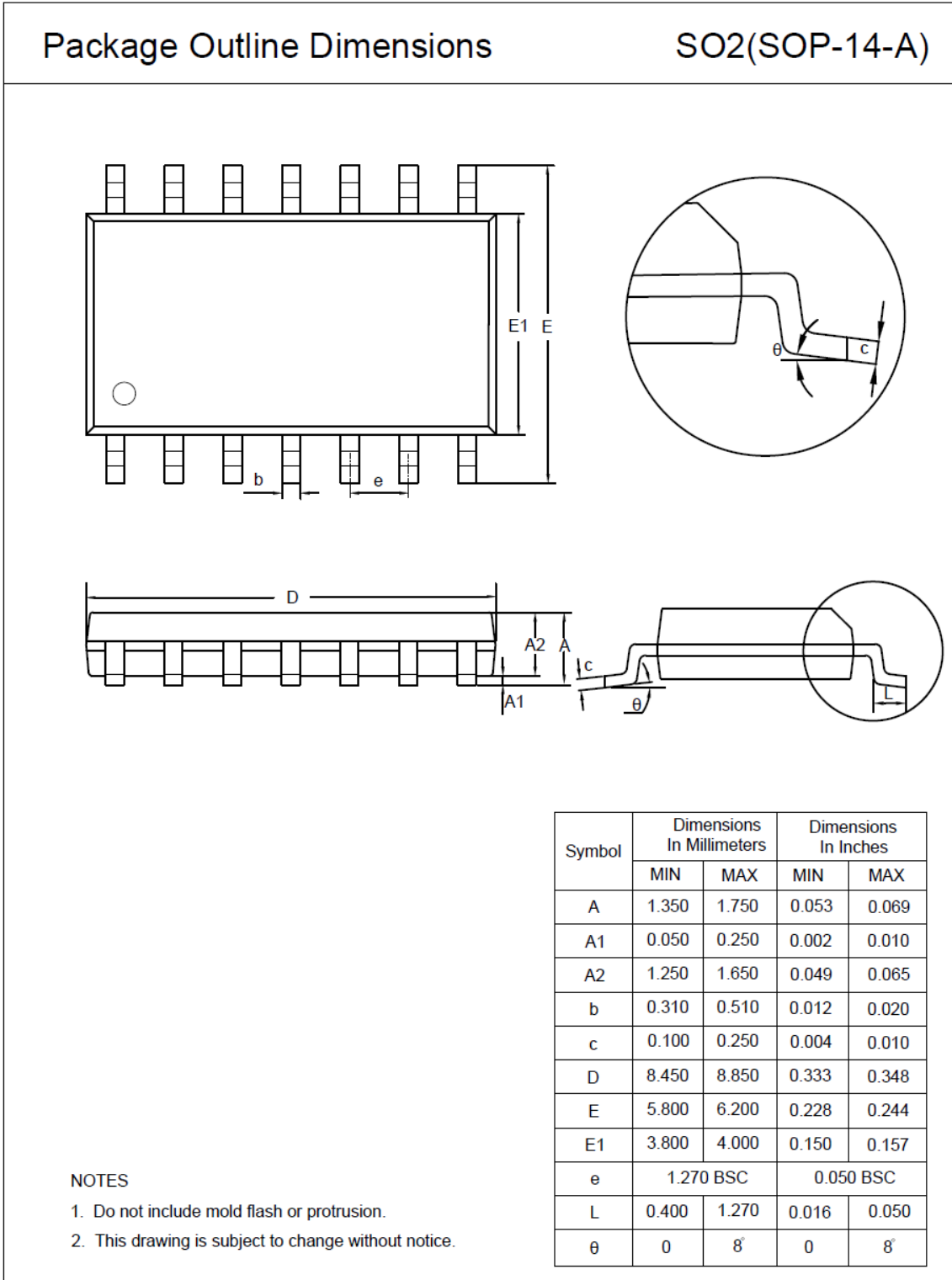


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.800	1.100	0.031	0.043
A1	0.050	0.150	0.002	0.006
A2	0.750	0.950	0.030	0.037
b	0.250	0.380	0.010	0.015
c	0.090	0.230	0.004	0.009
D	2.900	3.100	0.114	0.122
E	4.700	5.100	0.185	0.201
E1	2.900	3.100	0.114	0.122
e	0.650 BSC		0.026 BSC	
L	0.400	0.800	0.016	0.031
θ	0	8°	0	8°

NOTES

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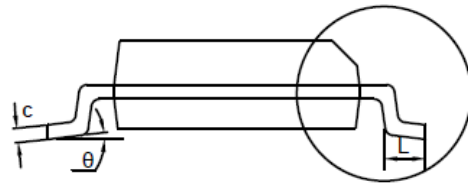
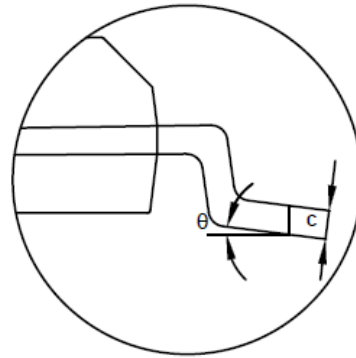
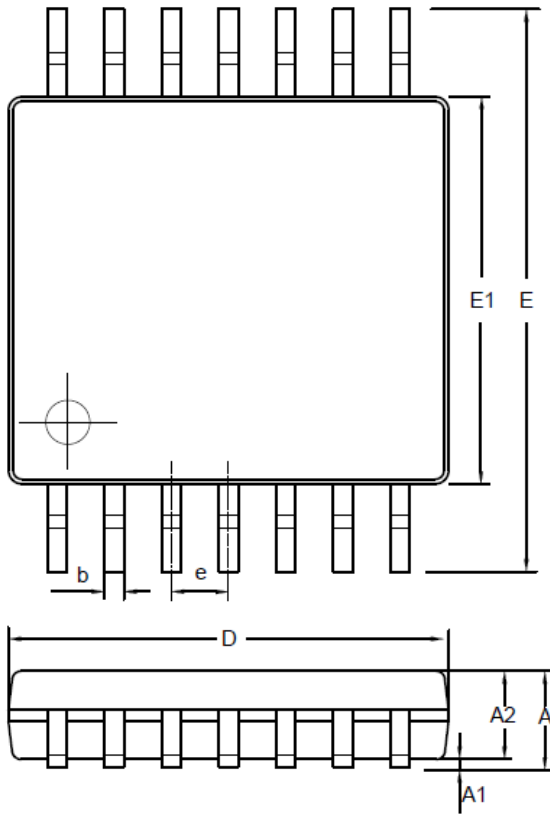
SOP14



TSSOP14

Package Outline Dimensions

TS2(TSSOP-14-A)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	0.900	1.200	0.035	0.047
A1	0.050	0.150	0.002	0.006
A2	0.800	1.050	0.031	0.041
b	0.190	0.300	0.007	0.012
c	0.090	0.200	0.004	0.008
D	4.900	5.100	0.193	0.201
E	6.200	6.600	0.244	0.260
E1	4.300	4.500	0.169	0.177
e	0.650 BSC		0.026 BSC	
L	0.450	0.750	0.018	0.030
θ	0	8°	0	8°

NOTES

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